

Look-Ahead SLP: Auto-vectorization in the presence of commutative operations

Vasileios Porpudas¹, Rodrigo C. O. Rocha²
and Luís F. W. Góes³

Intel Santa Clara, USA¹
University of Edinburgh, UK²
PUC Minas, Brazil³

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informatics



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Notice revision #20110804

SLP Straight-Line Code Vectorizer

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 - Usually run SLP after the Loop Vectorizer

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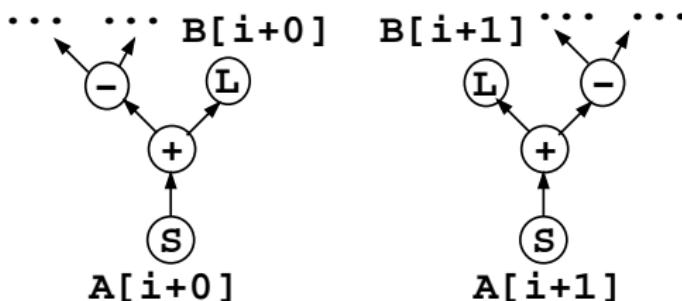
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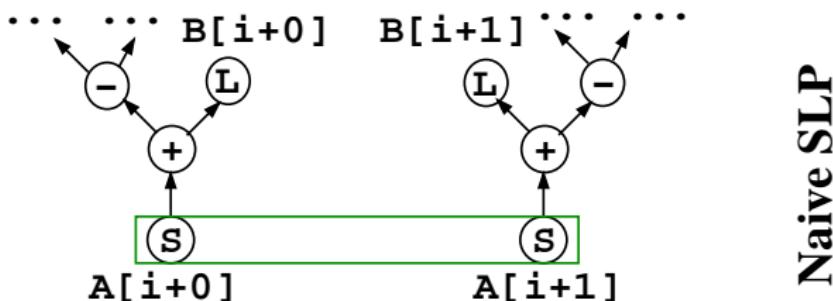
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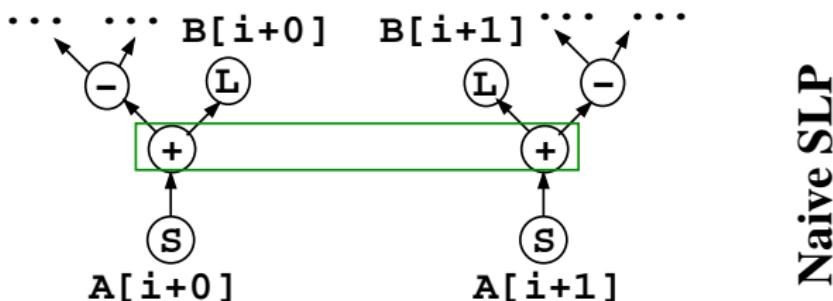
Naive SLP

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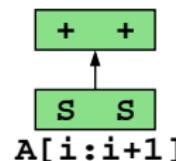
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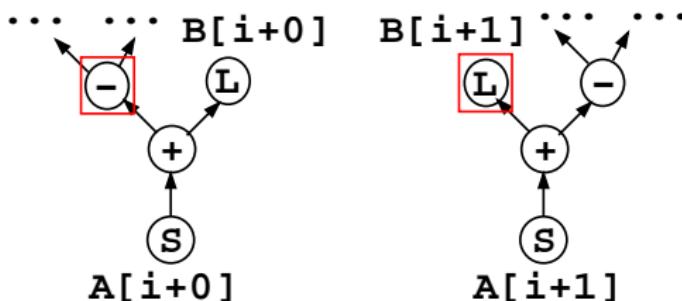
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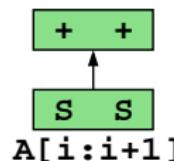
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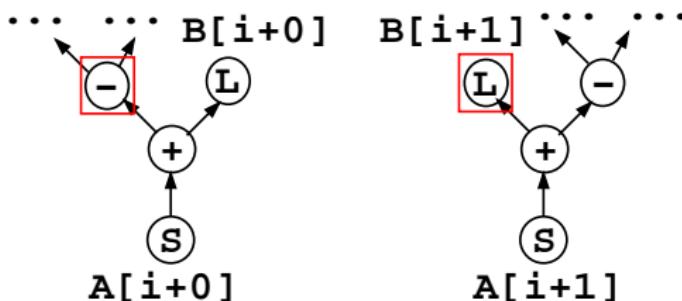


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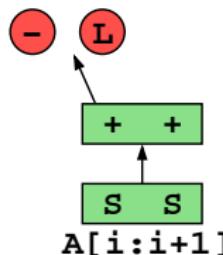
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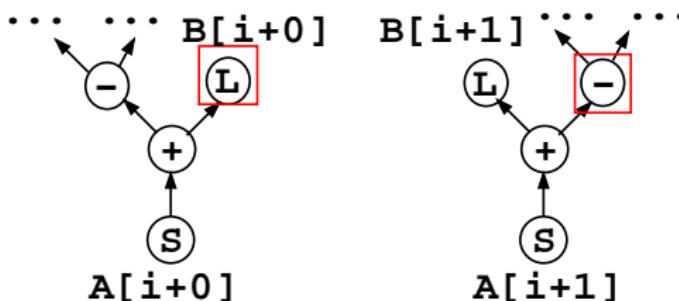
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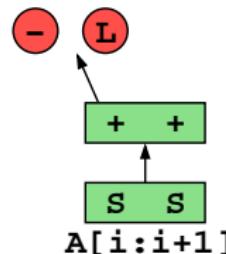
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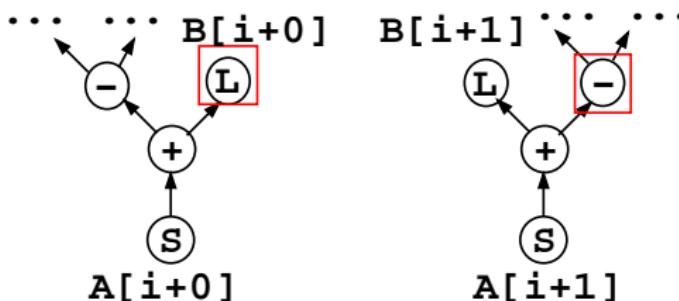
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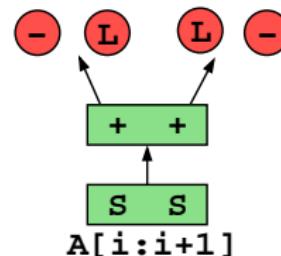
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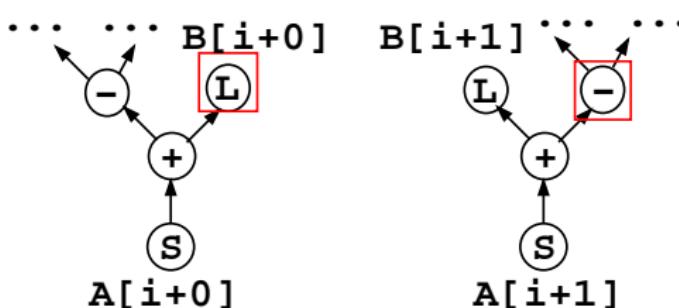
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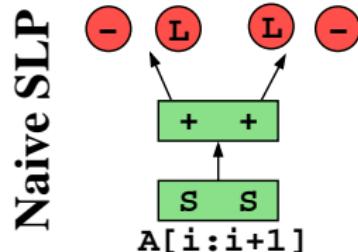
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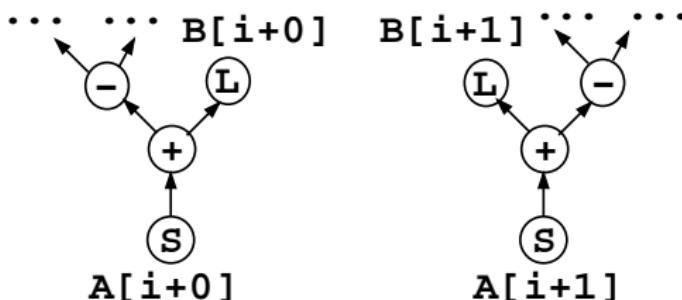
Vectorization would STOP without reordering!



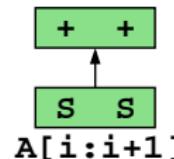
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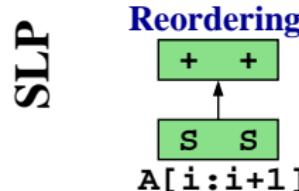
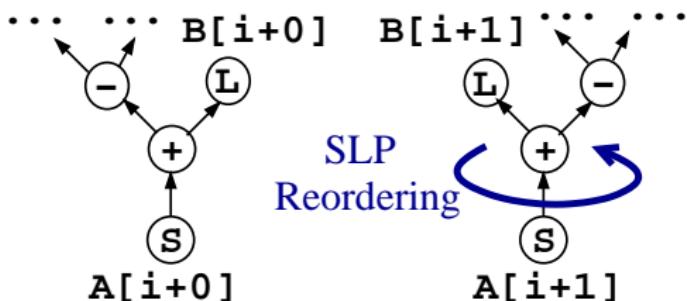


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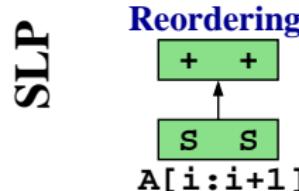
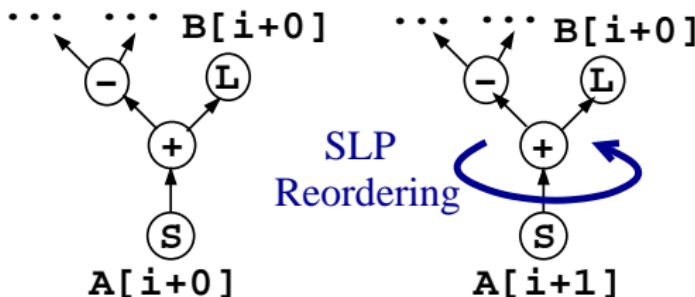
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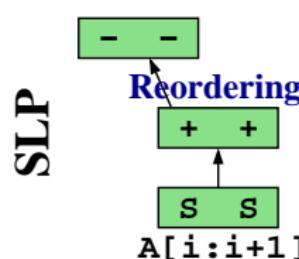
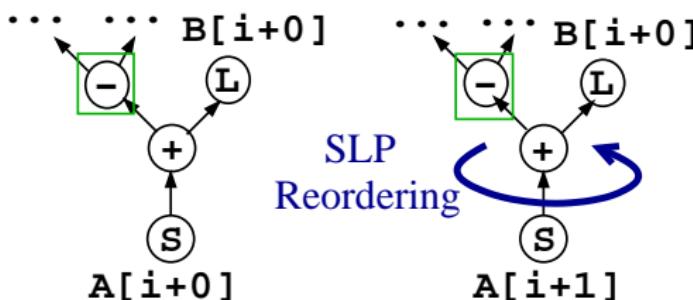


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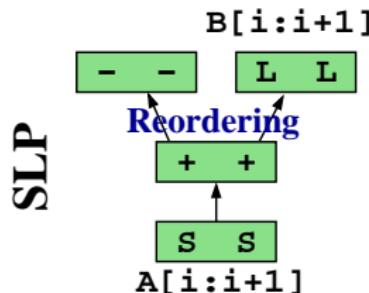
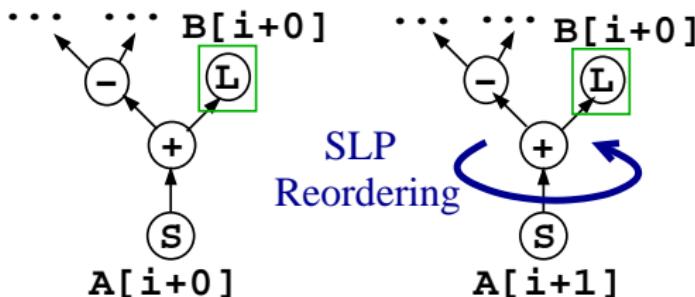


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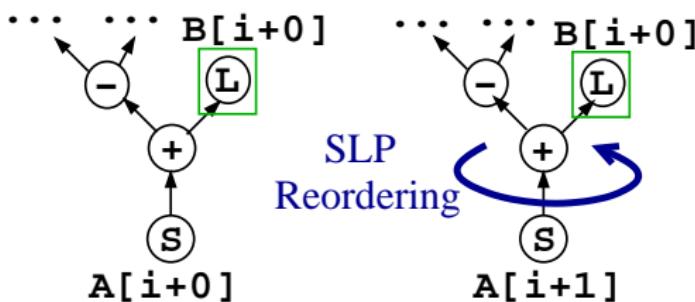
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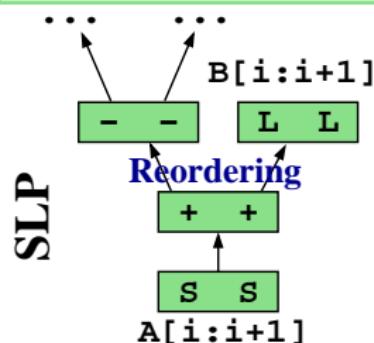
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Vectorization continues !



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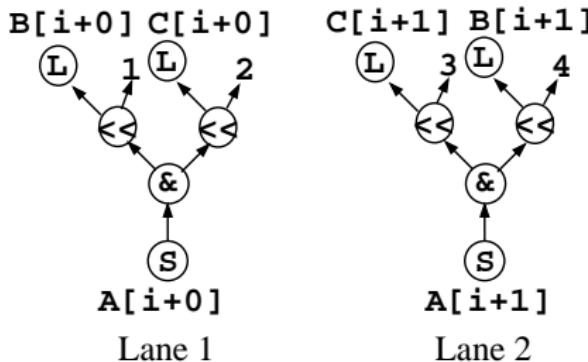
- SLP reordering not effective for:
 - ① Load address mismatch further up the graph
 - ② Opcode mismatch further up the graph
 - ③ Reordering across chains of commutative operations
- Look-Ahead SLP (LSLP) provides a solution to all three.

1/3 Load address mismatch

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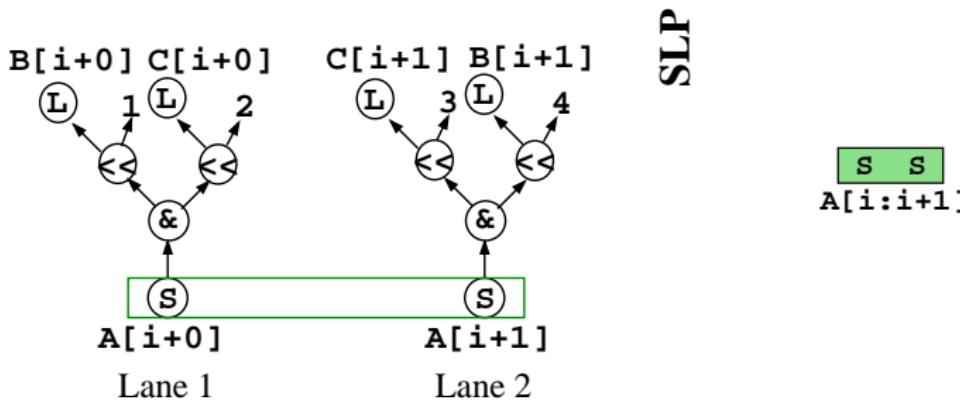
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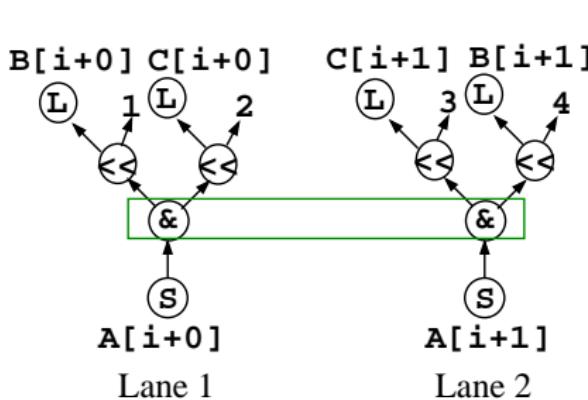
SLP

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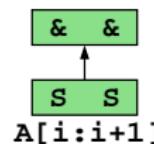
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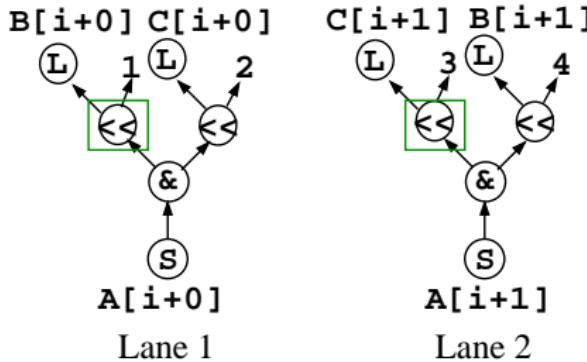


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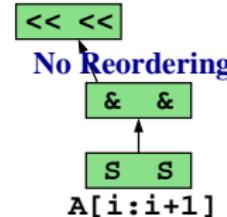


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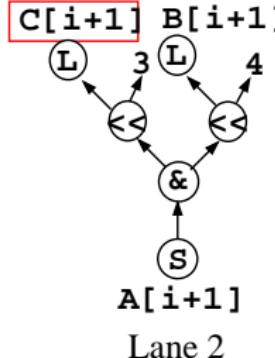
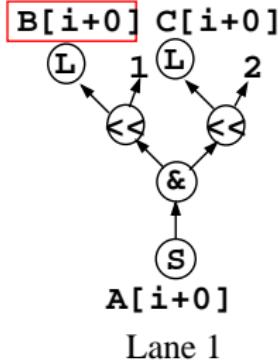


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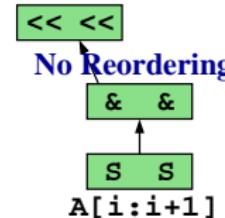


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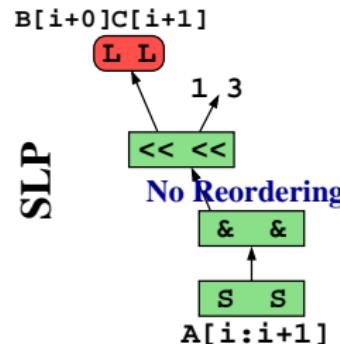
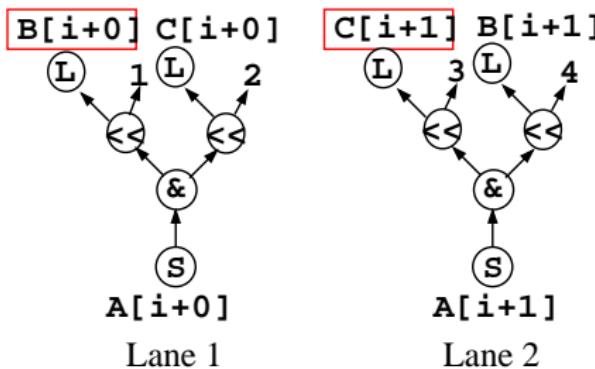


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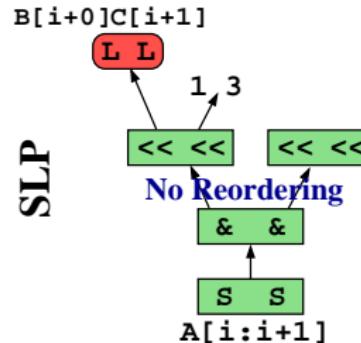
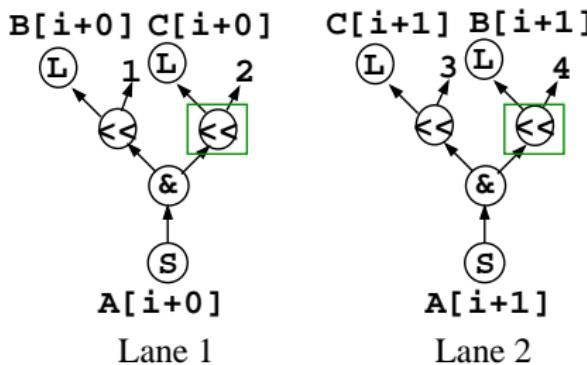
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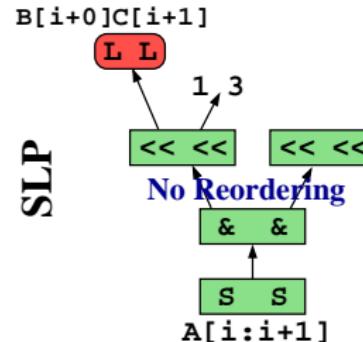
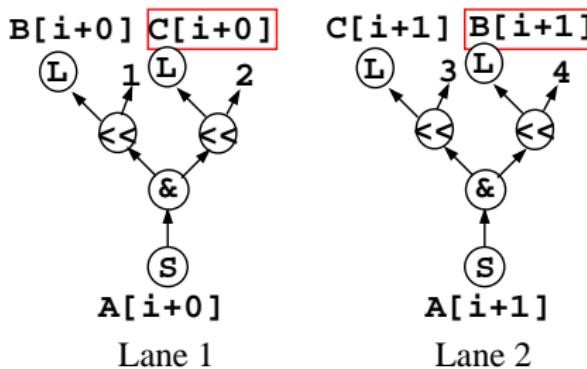
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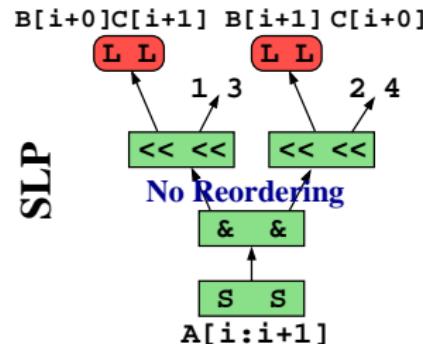
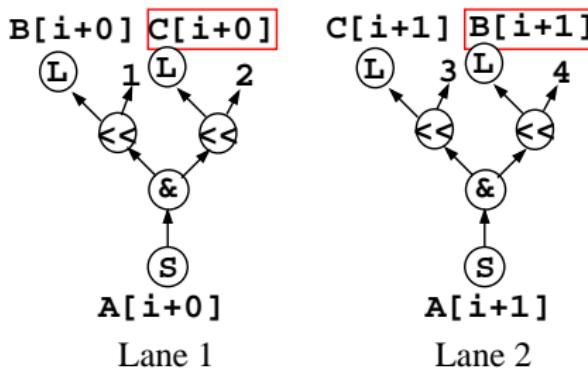
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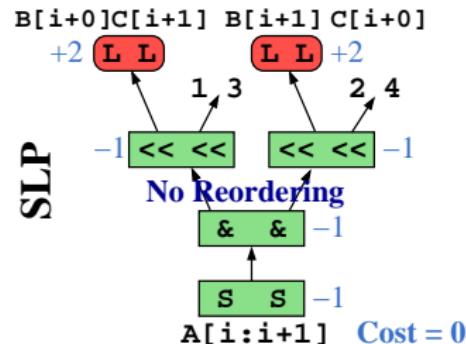
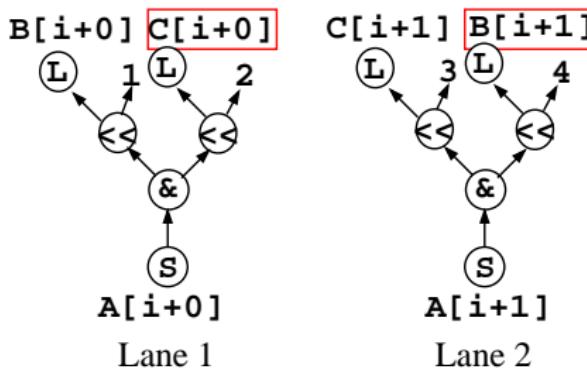
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long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



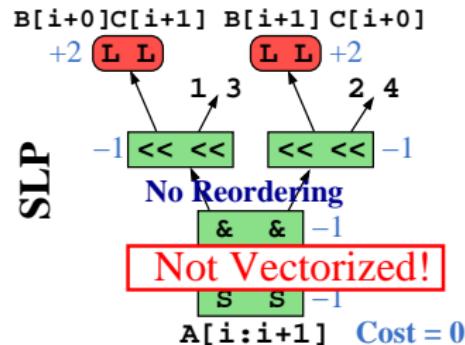
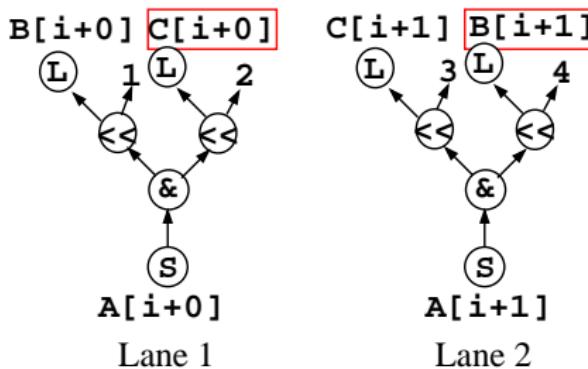
1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



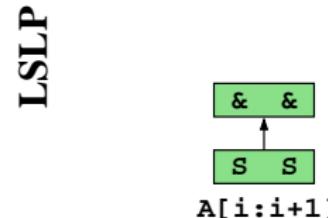
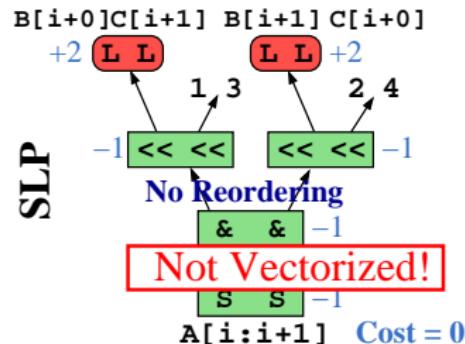
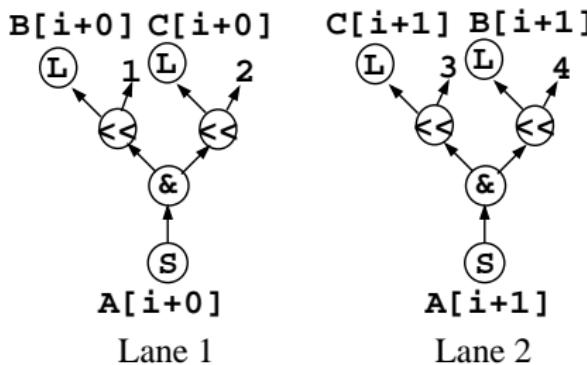
1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



1/3 Load address mismatch

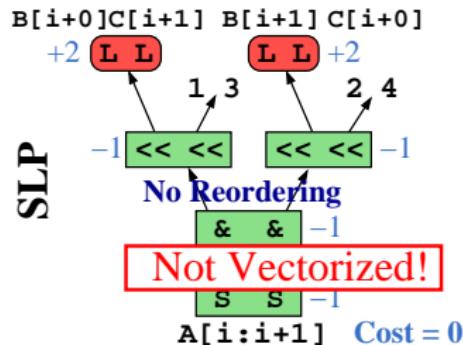
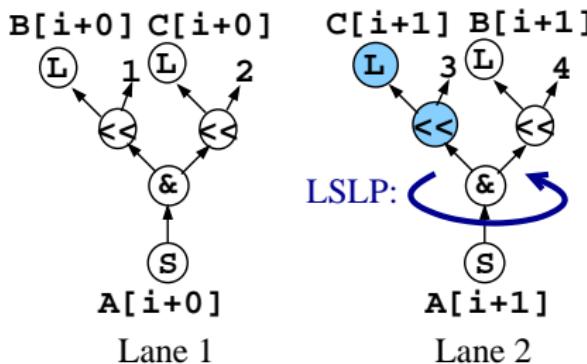
```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



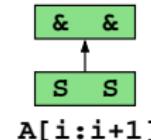
■	Non-Vectorizable	■	Vectorizable	+/#	Cost
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1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



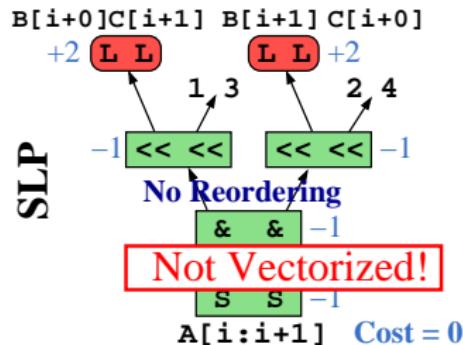
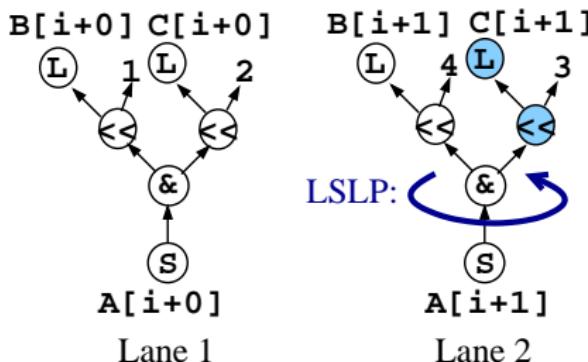
LSLP



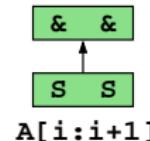
■	Non-Vectorizable	■	Vectorizable	+/#	Cost
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1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



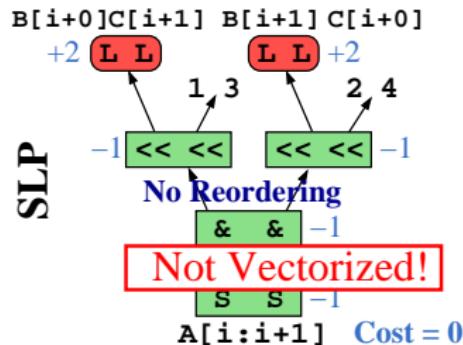
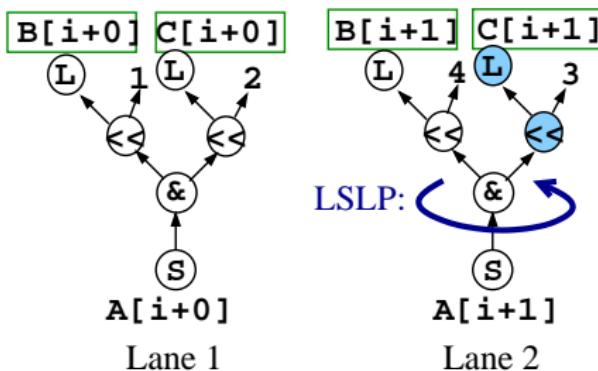
LSLP



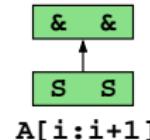
●	Non-Vectorizable	■	Vectorizable	+/#Cost
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1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



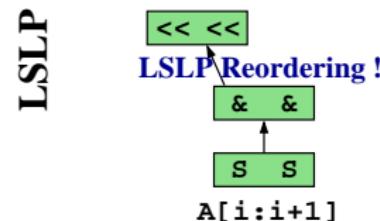
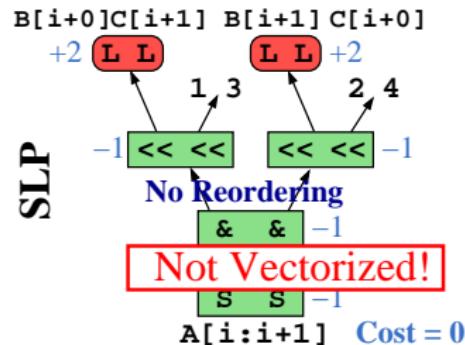
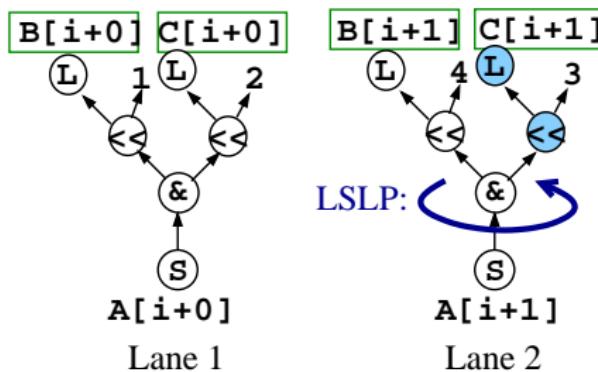
LSLP



■	Non-Vectorizable	■	Vectorizable	+/#Cost
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1/3 Load address mismatch

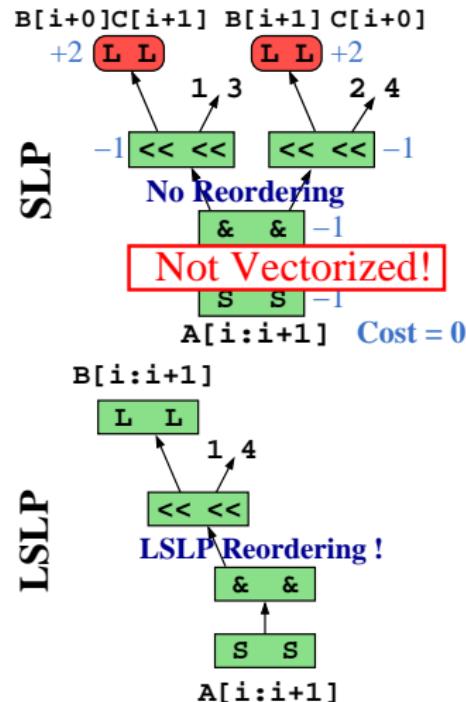
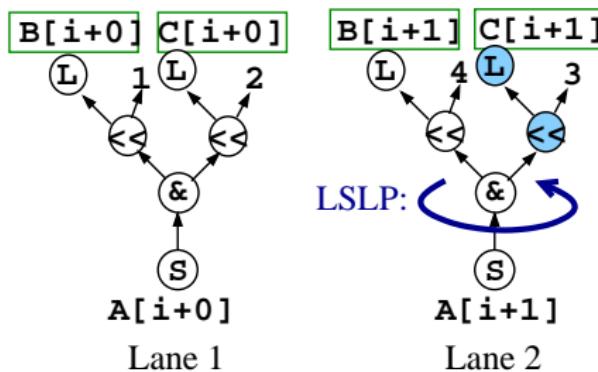
```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



■	Non-Vectorizable	■	Vectorizable	+/#	Cost
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1/3 Load address mismatch

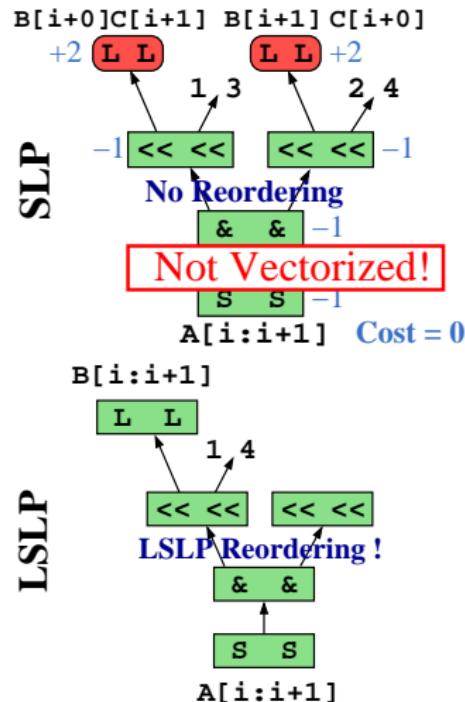
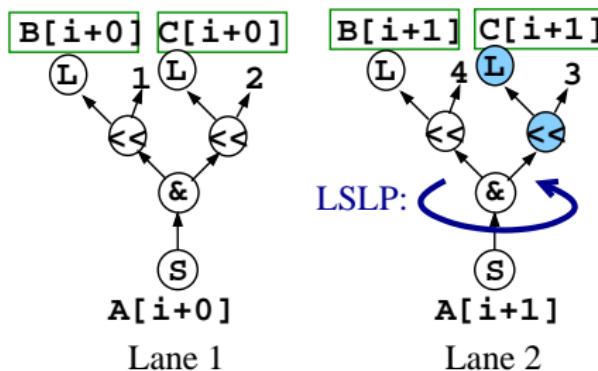
```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



	Non-Vectorizable		Vectorizable	+/#Cost
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1/3 Load address mismatch

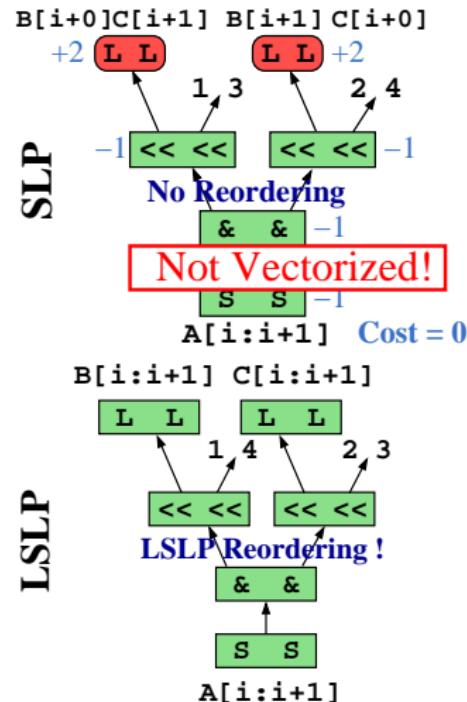
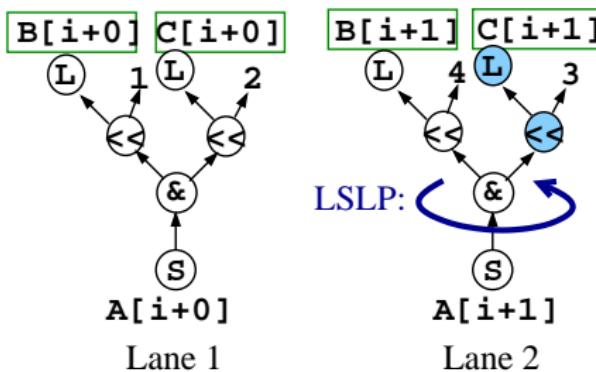
```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



	Non-Vectorizable		Vectorizable	+/#Cost
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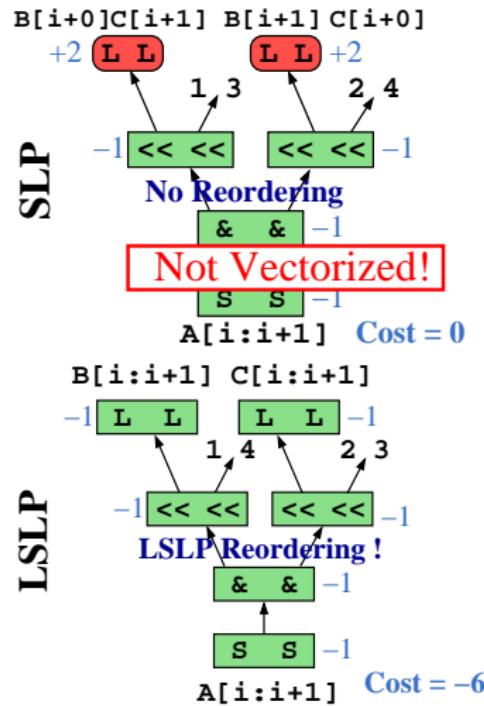
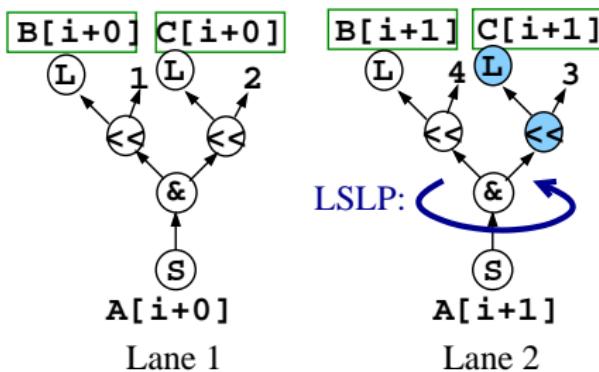
1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



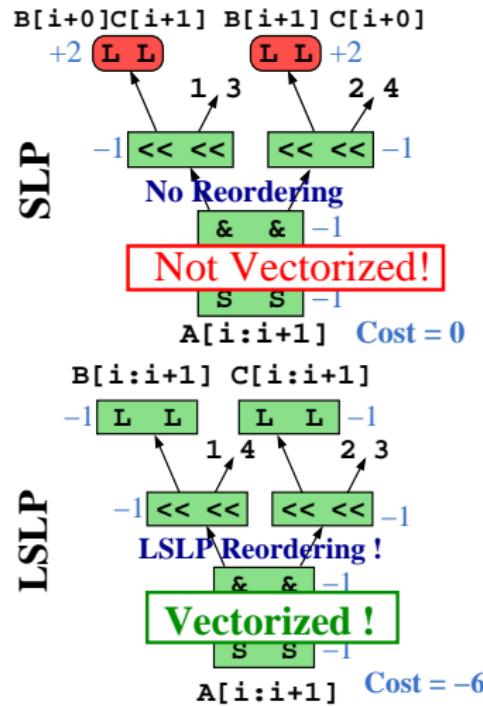
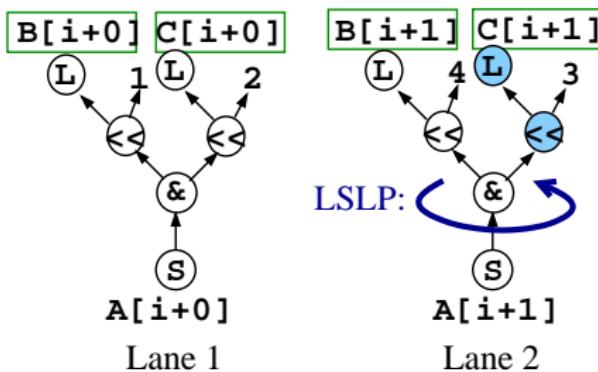
1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



1/3 Load address mismatch

```
long A[],B[],C[];
A[i+0]=(B[i+0]<<1)&(C[i+0]<<2);
A[i+1]=(C[i+1]<<3)&(B[i+1]<<4);
```



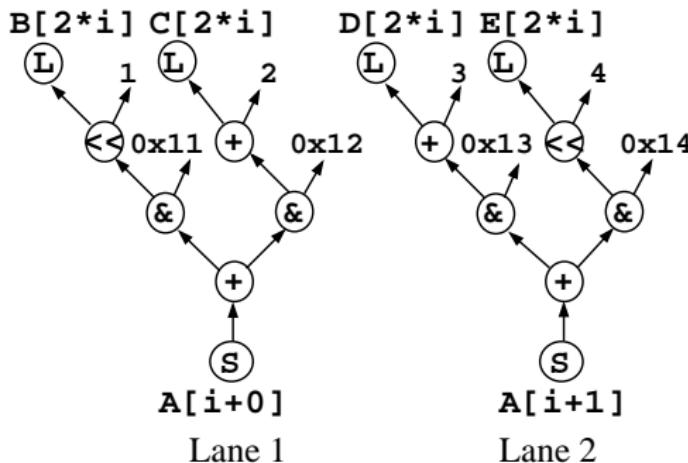
● Non-Vectorizable ■ Vectorizable +/#Cost

2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];  
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);  
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```

2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];  
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);  
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```

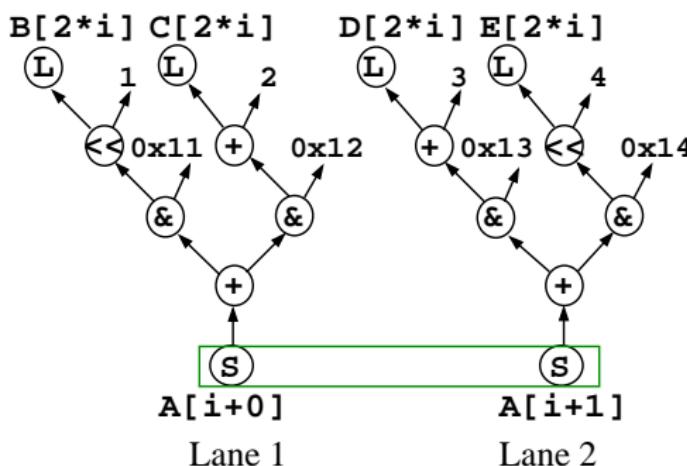


2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```

SLP

S	S
A[i:i+1]	

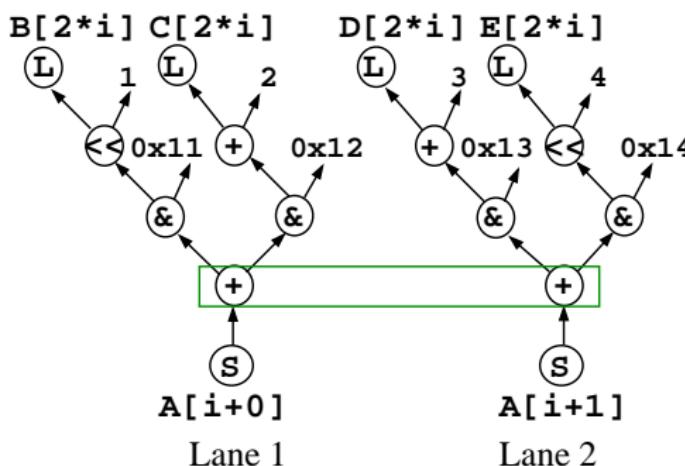
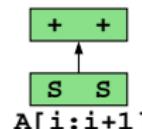


● Non-Vectorizable ■ Vectorizable +/# Cost

2/3 Opcodes mismatch

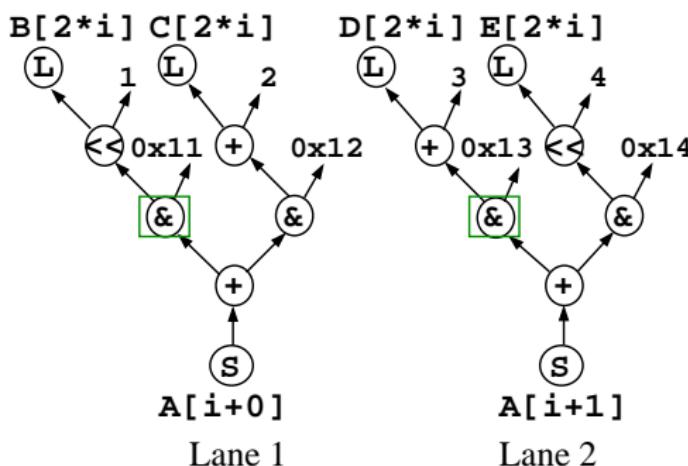
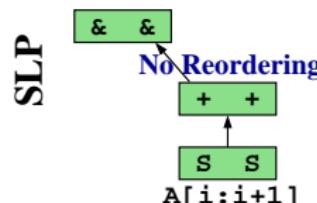
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```

SLP



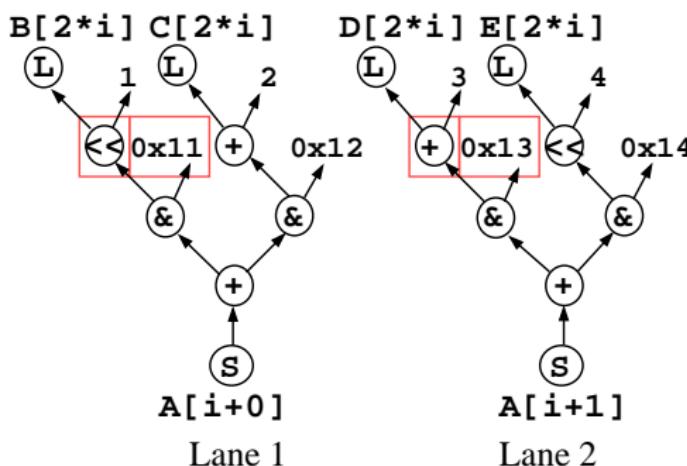
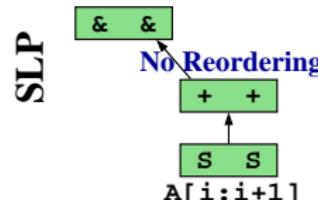
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



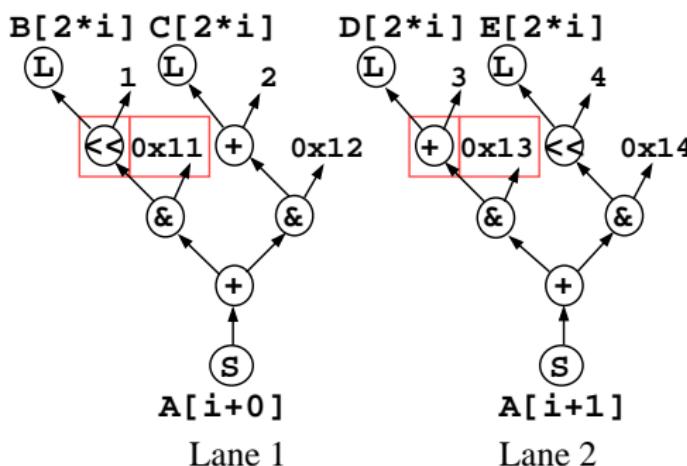
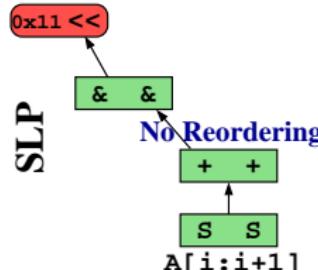
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+2)&0x12);
A[i+1]=((D[2*i]+3)&0x13)+((E[2*i]<<4)&0x14);
```



2/3 Opcodes mismatch

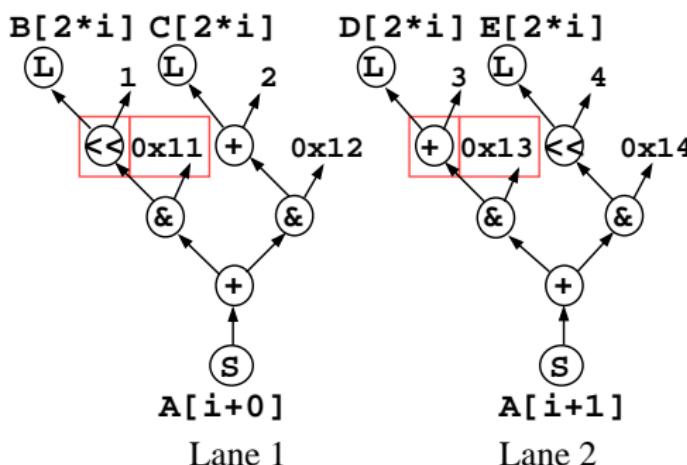
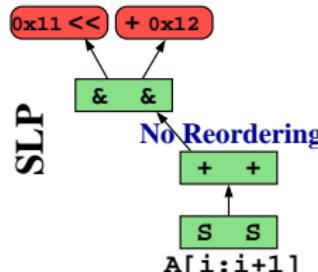
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+2)&0x12);
A[i+1]=((D[2*i]+3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	+/# Cost
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2/3 Opcodes mismatch

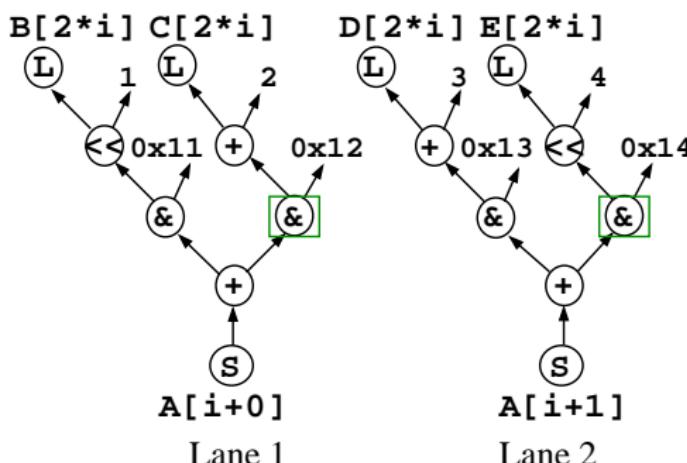
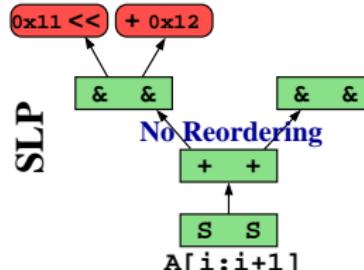
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+2)&0x12);
A[i+1]=((D[2*i]+3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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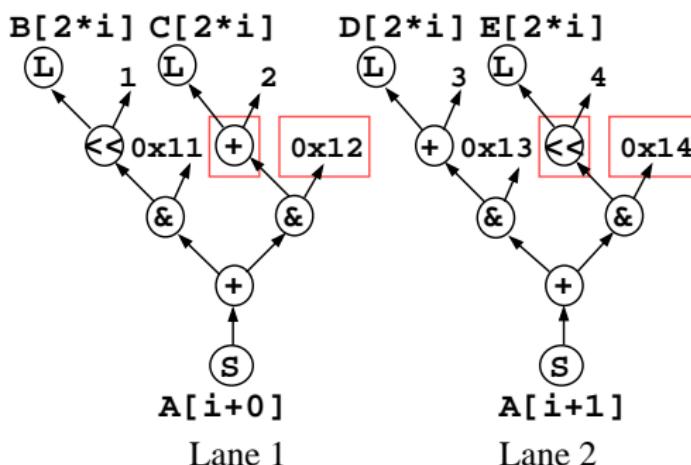
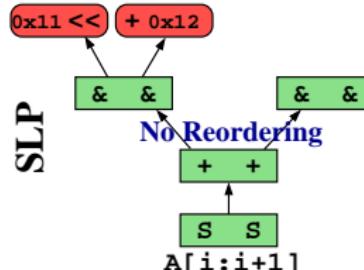
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



2/3 Opcodes mismatch

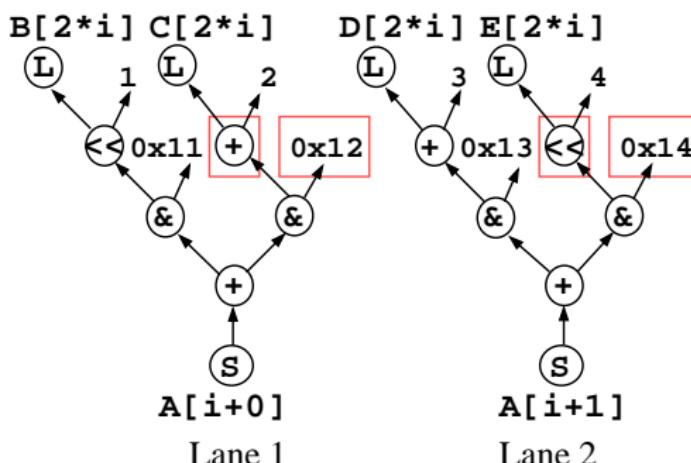
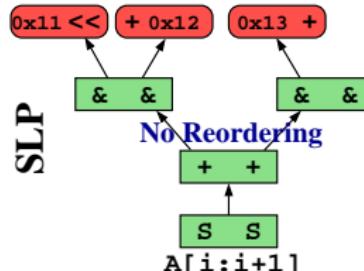
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



	Non-Vectorizable		Vectorizable	+/# Cost
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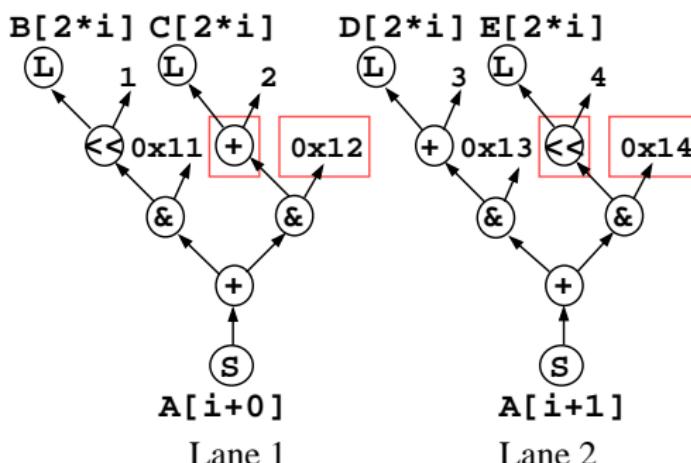
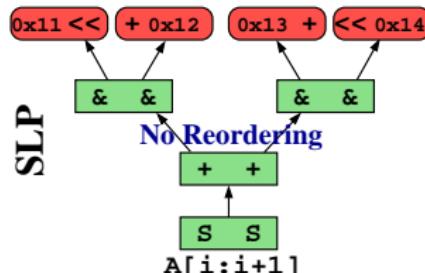
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



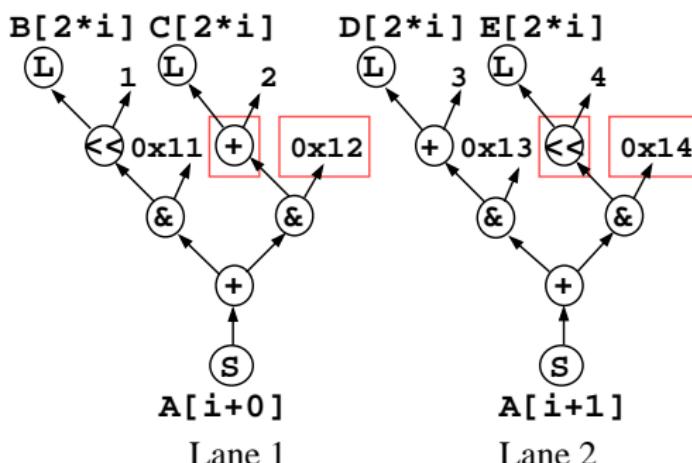
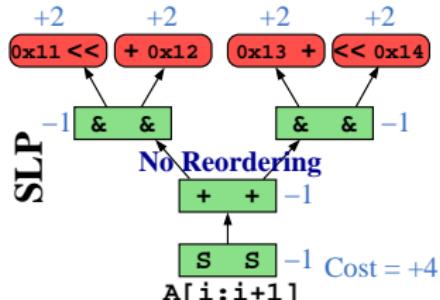
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



2/3 Opcodes mismatch

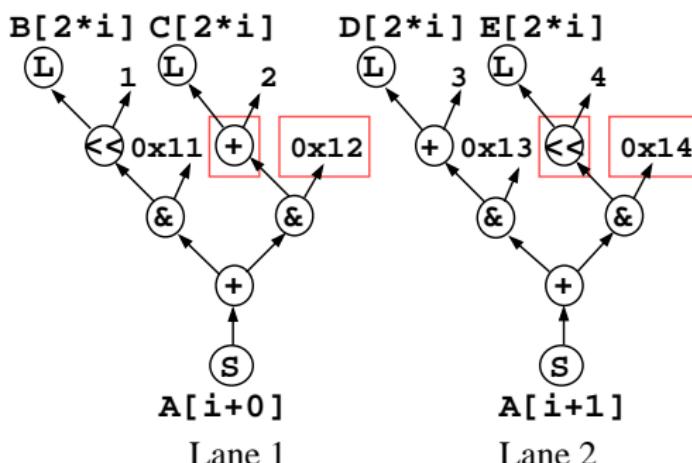
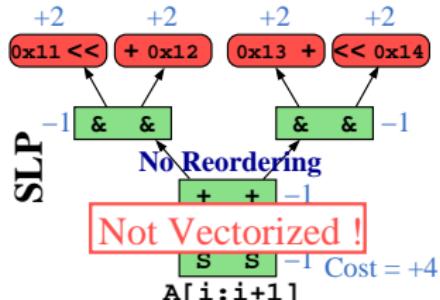
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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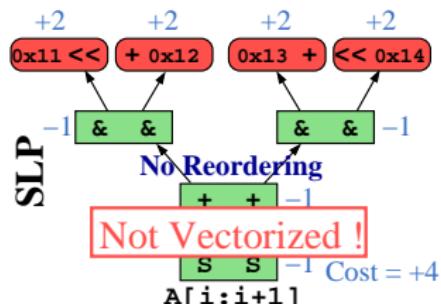
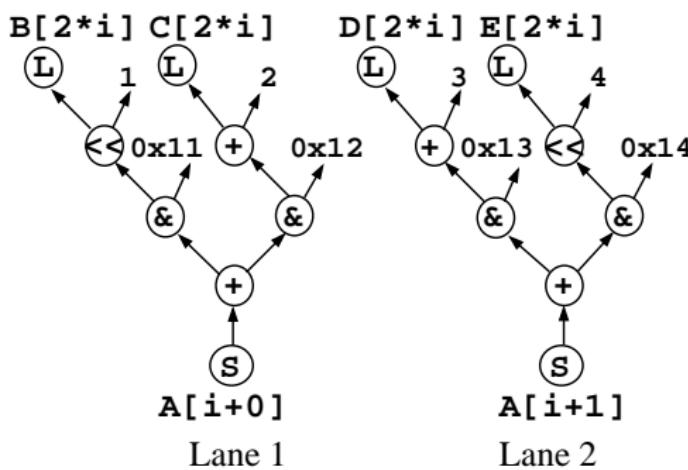
2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



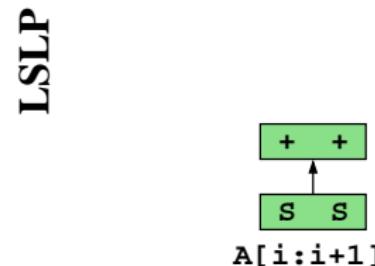
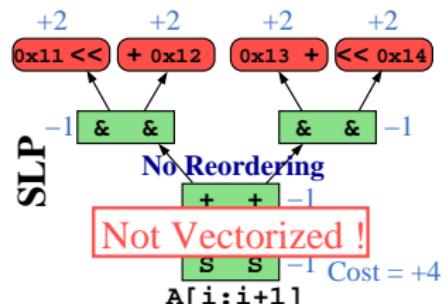
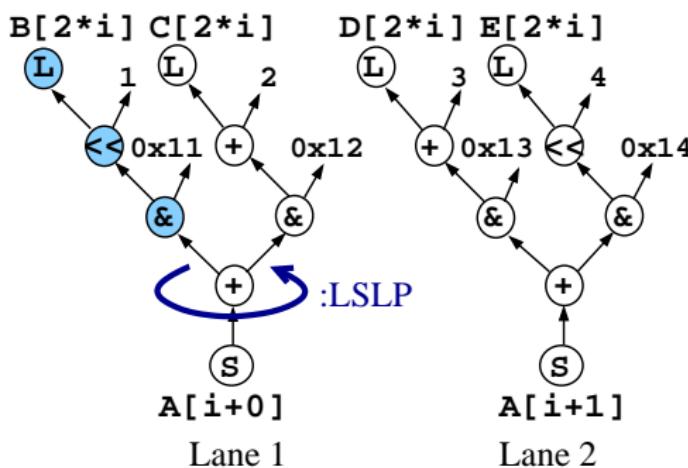
LSILP



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

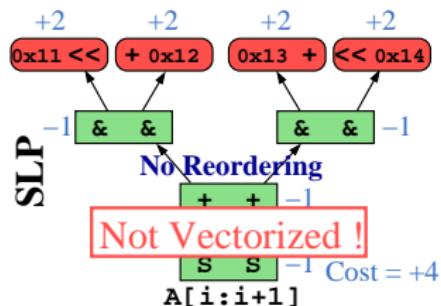
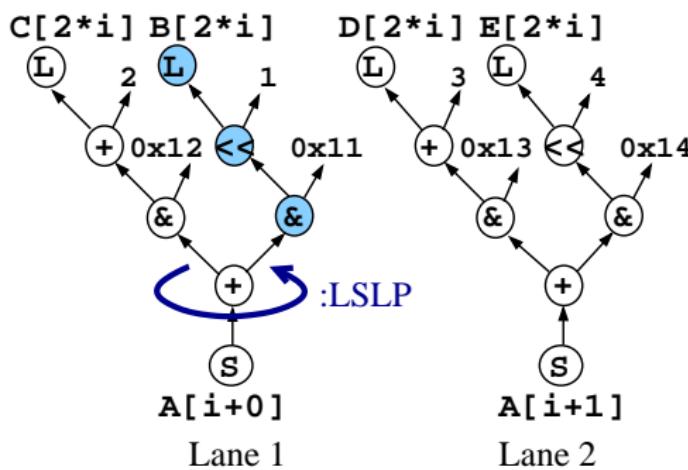
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



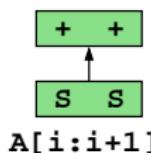
	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



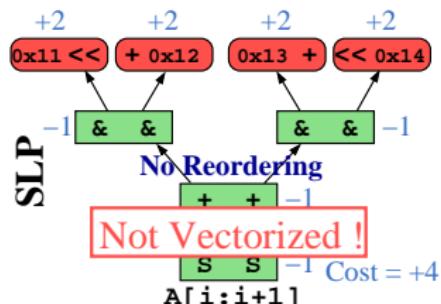
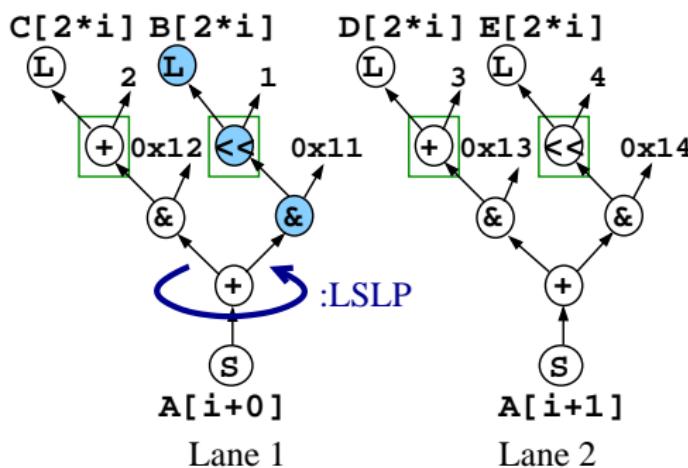
LSLP



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



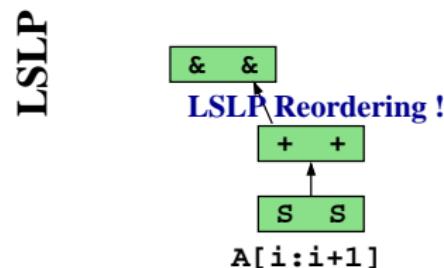
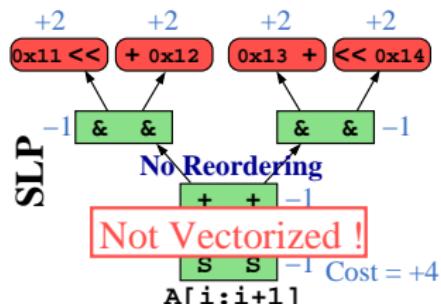
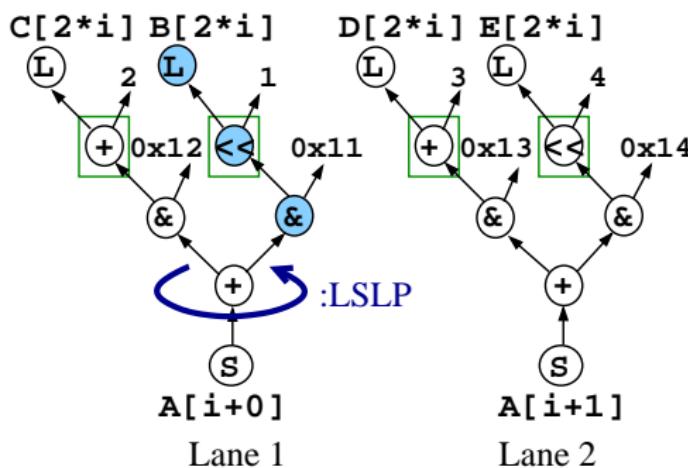
LSLP



● Non-Vectorizable	■ Vectorizable	+/# Cost
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2/3 Opcodes mismatch

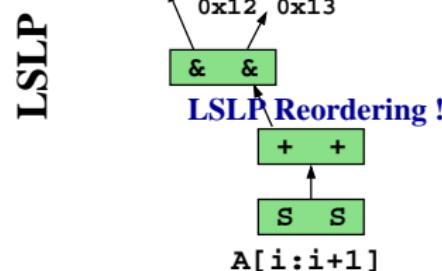
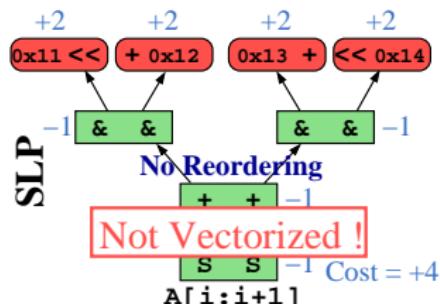
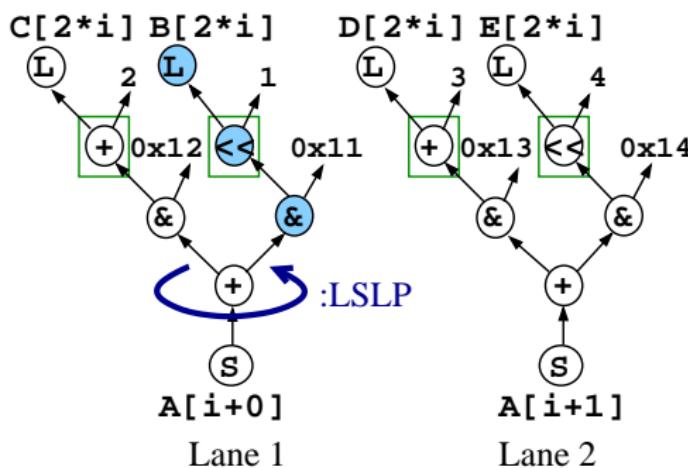
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

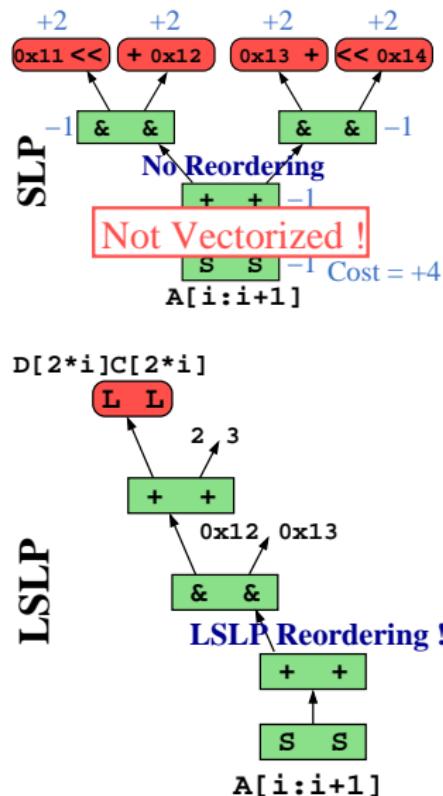
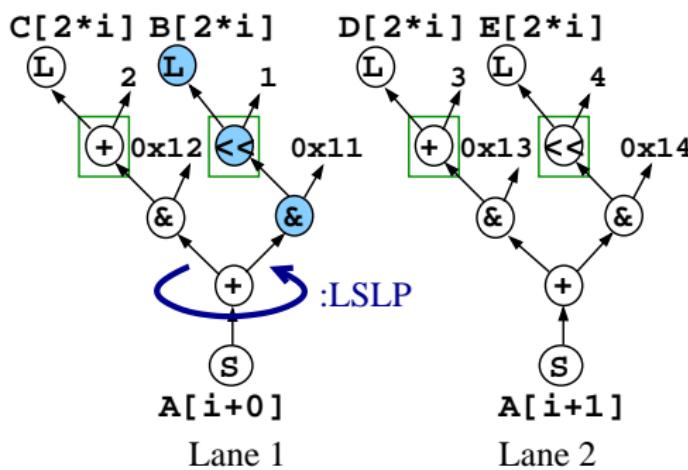
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/-\#$ Cost
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2/3 Opcodes mismatch

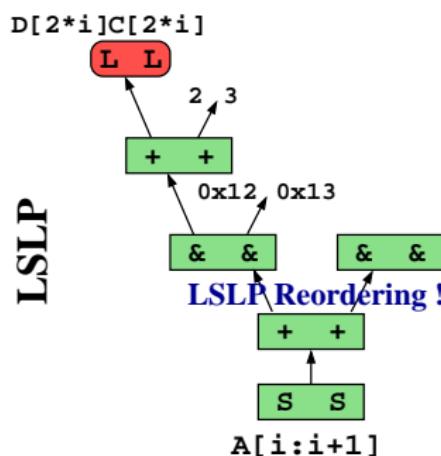
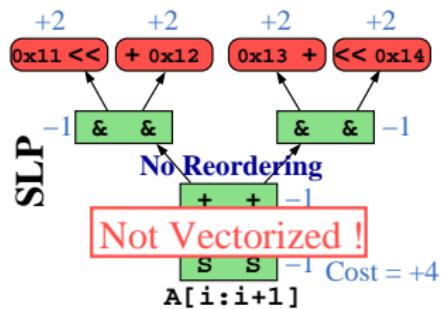
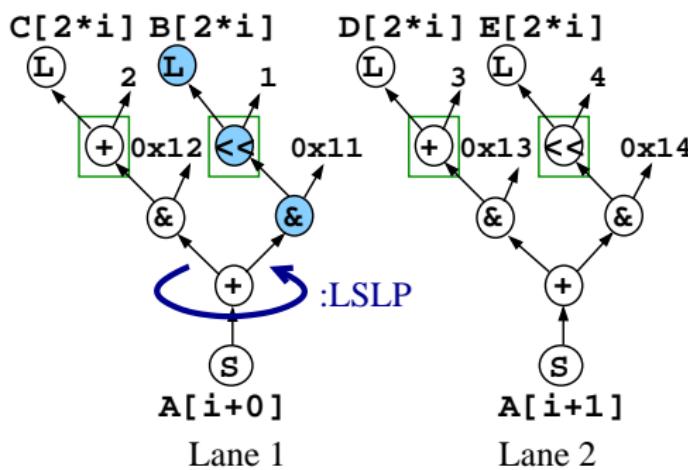
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

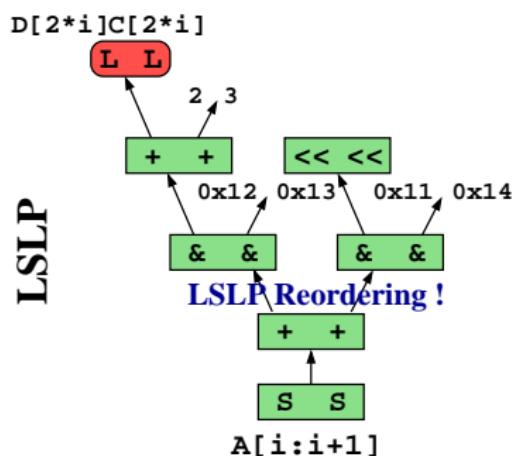
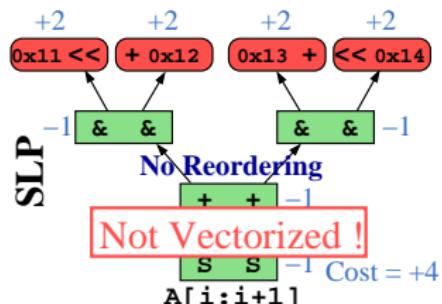
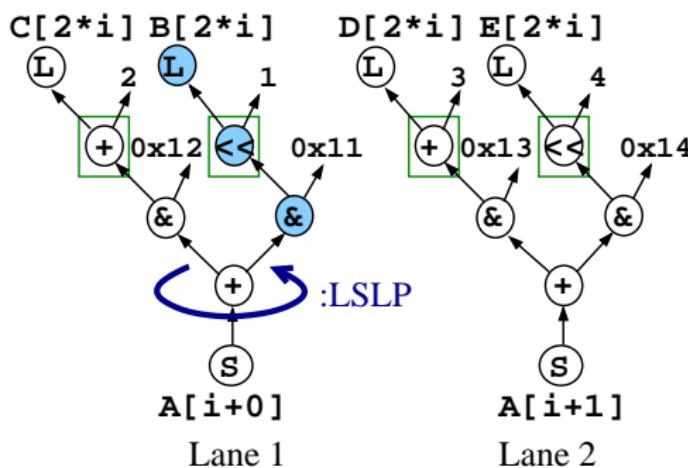
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

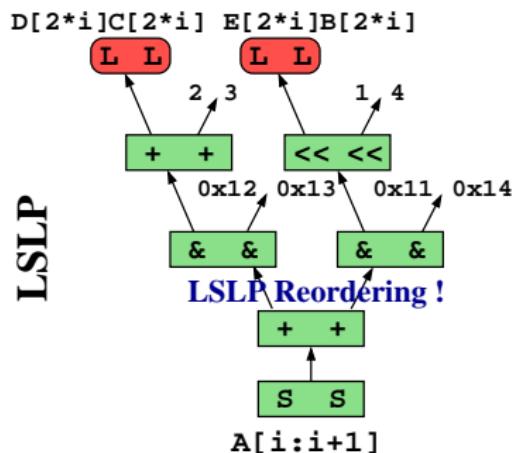
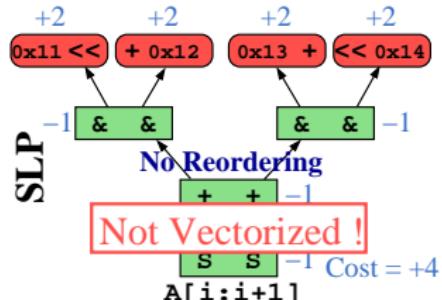
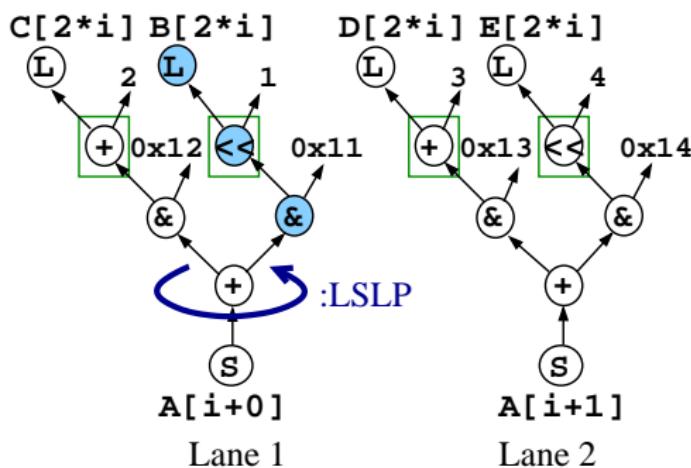
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



●	Non-Vectorizable	■	Vectorizable	$+/\#$ Cost
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2/3 Opcodes mismatch

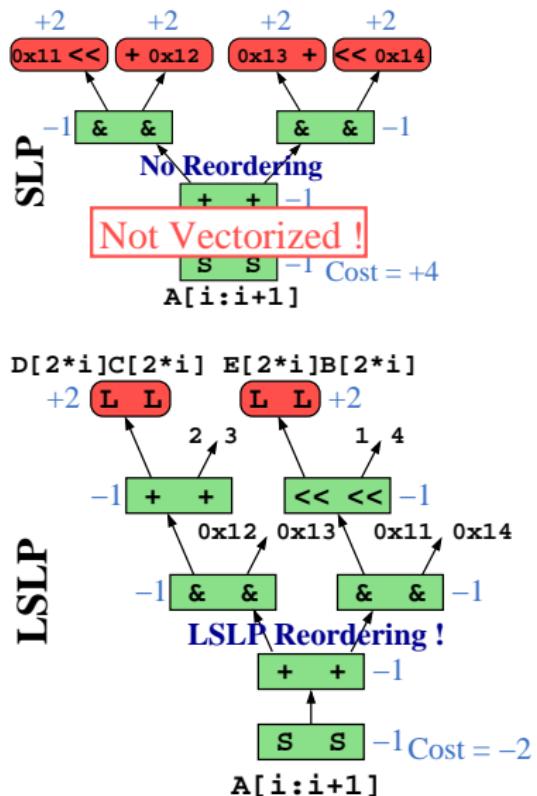
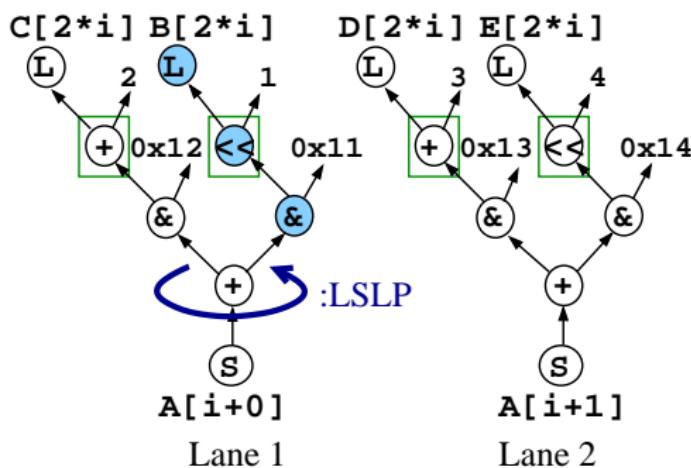
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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2/3 Opcodes mismatch

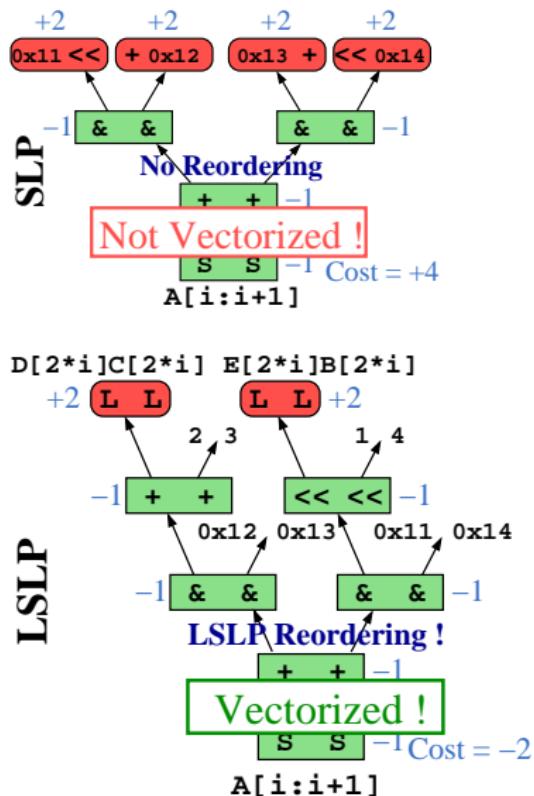
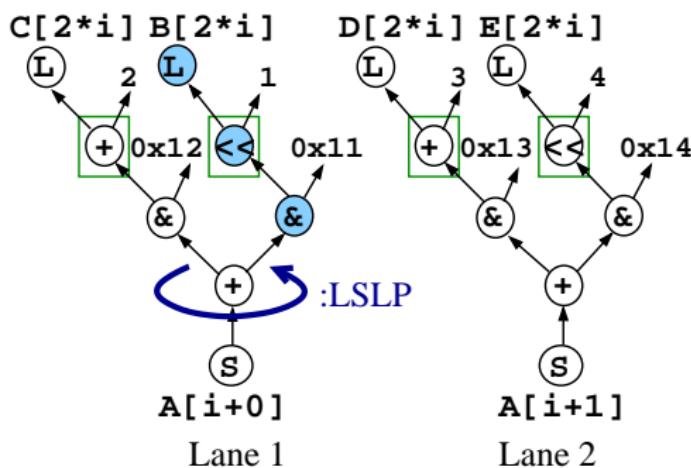
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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2/3 Opcodes mismatch

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=((B[2*i]<<1)&0x11)+((C[2*i]+ 2)&0x12);
A[i+1]=((D[2*i]+ 3)&0x13)+((E[2*i]<<4)&0x14);
```



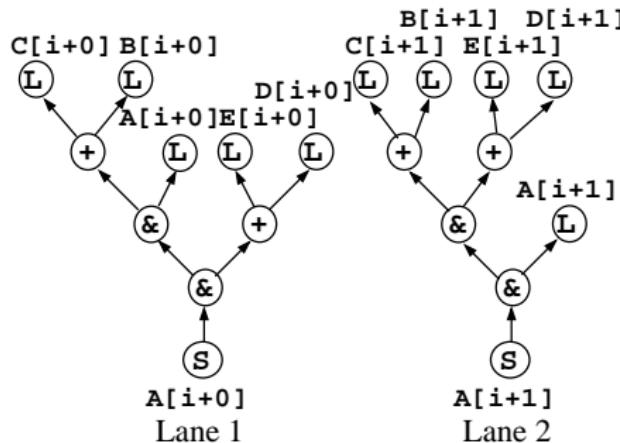
	Non-Vectorizable		Vectorizable	$+/\#$ Cost
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3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```

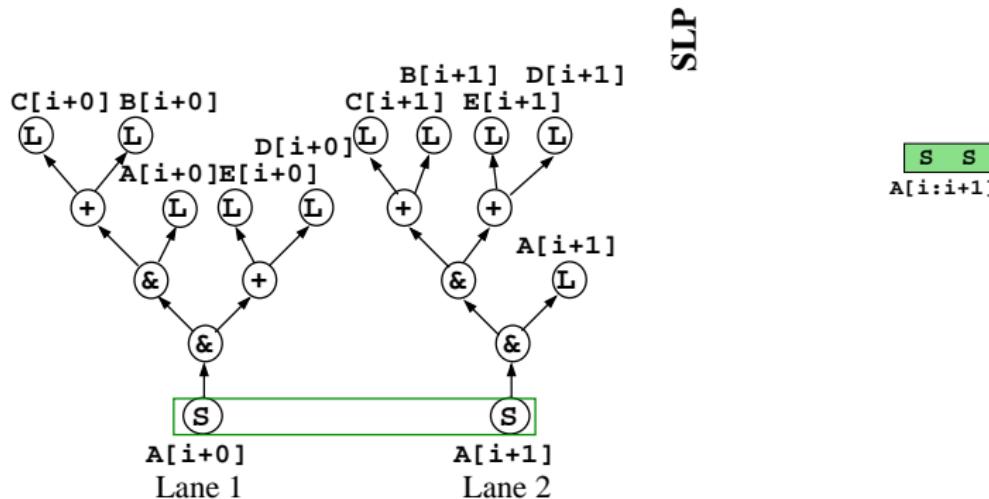
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



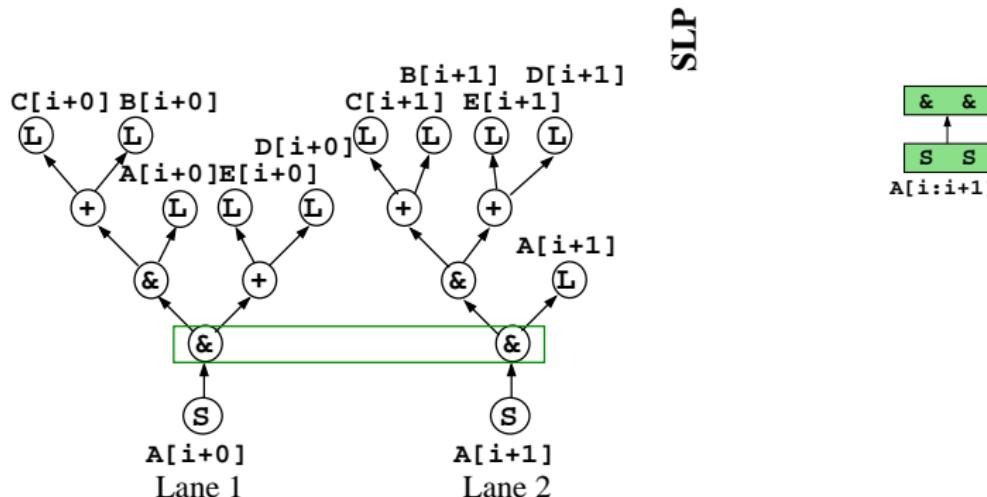
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



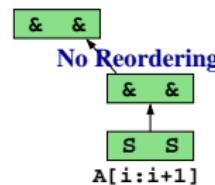
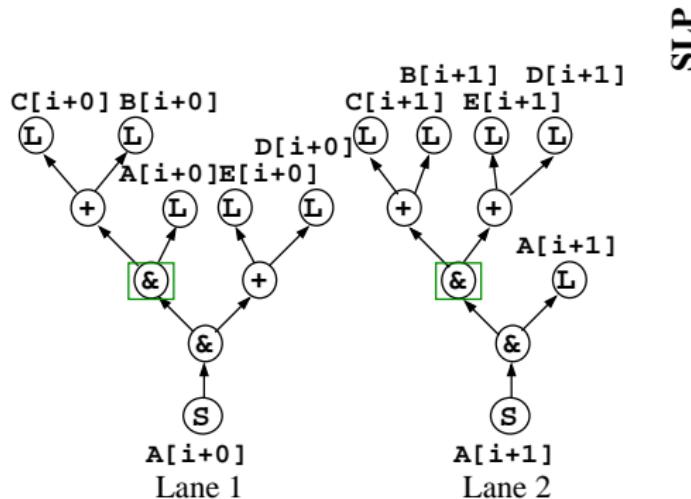
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



3/3 Chains of Commutative Operations

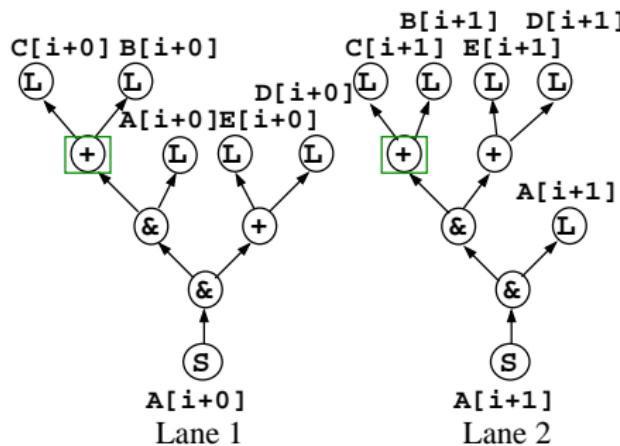
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



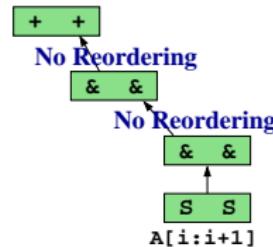
	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```

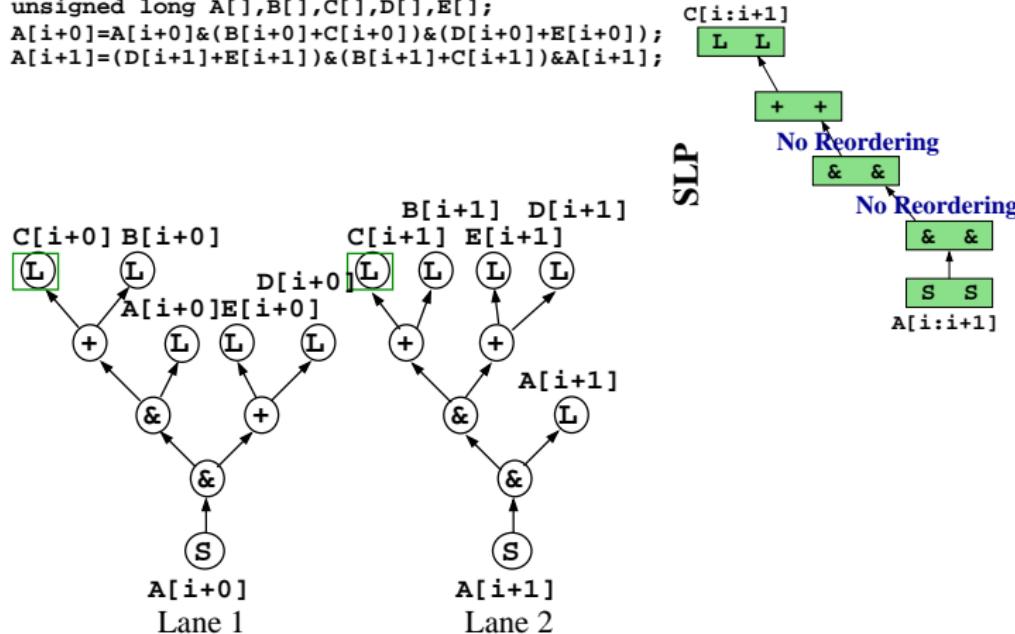


SLP



3/3 Chains of Commutative Operations

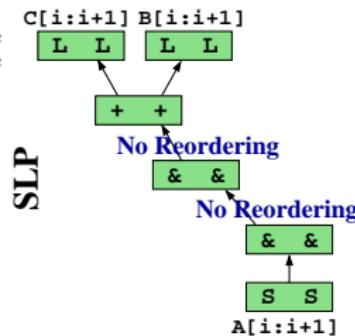
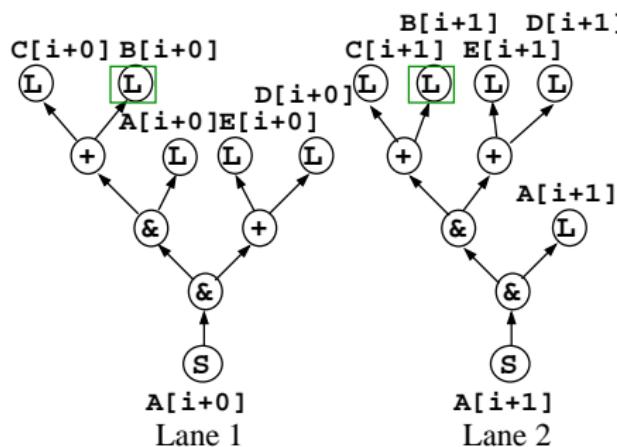
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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3/3 Chains of Commutative Operations

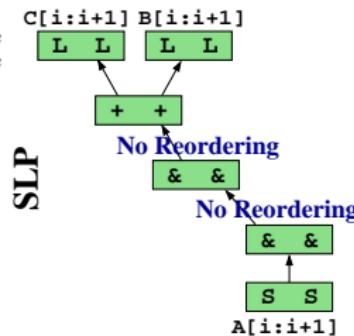
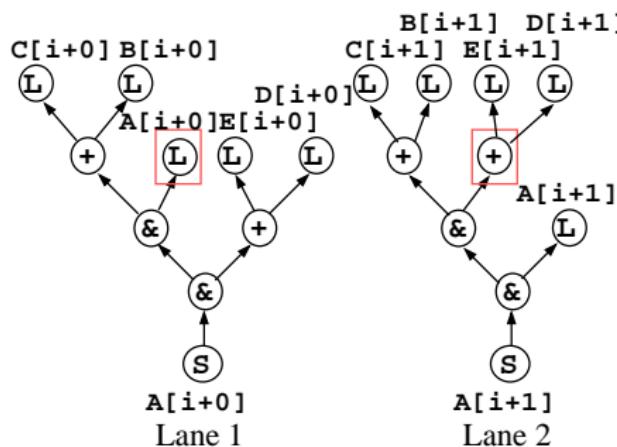
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/ \#$ Cost
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3/3 Chains of Commutative Operations

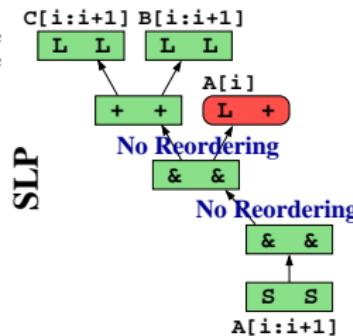
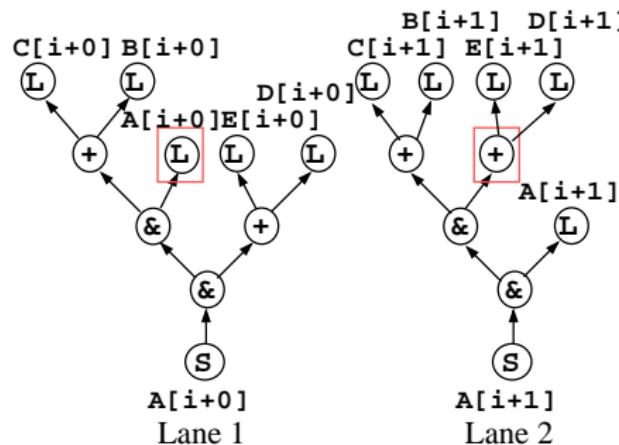
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/-\#Cost$
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3/3 Chains of Commutative Operations

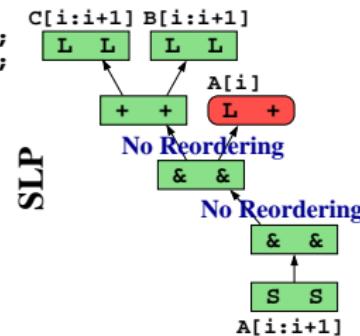
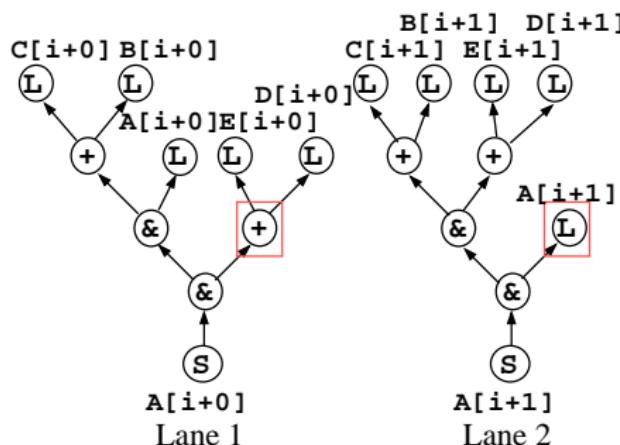
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable		#Cost
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3/3 Chains of Commutative Operations

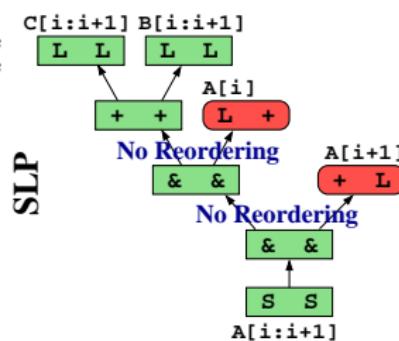
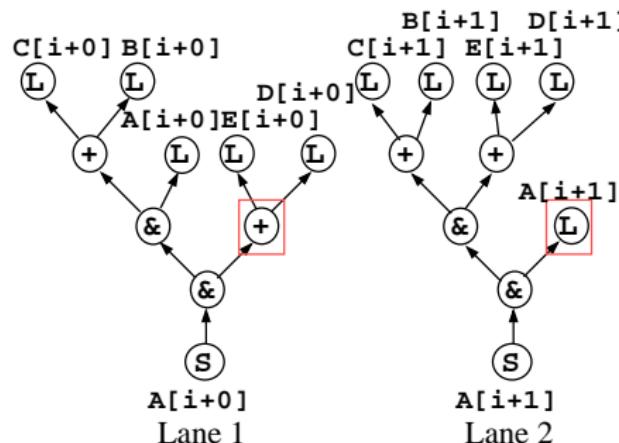
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



●	Non-Vectorizable	■	Vectorizable	+/#Cost
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3/3 Chains of Commutative Operations

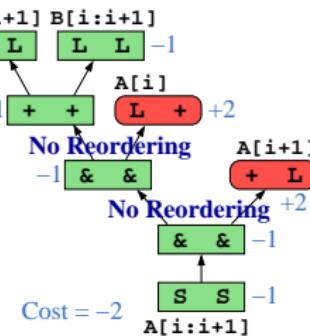
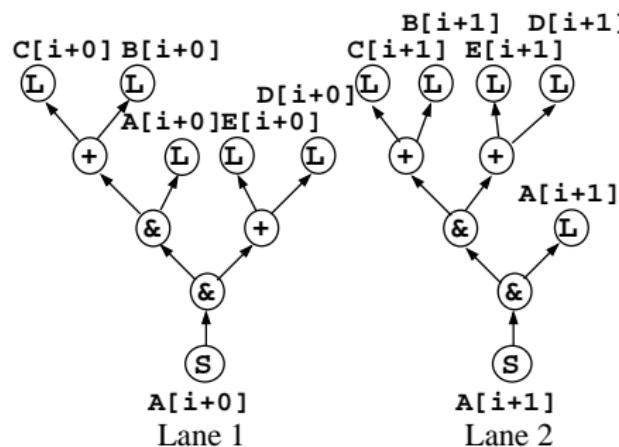
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/\#Cost$
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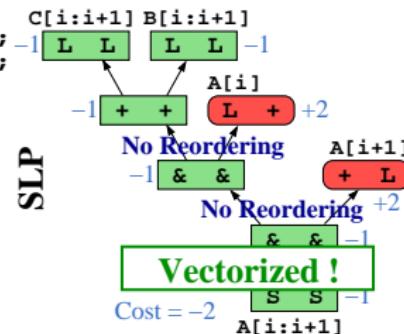
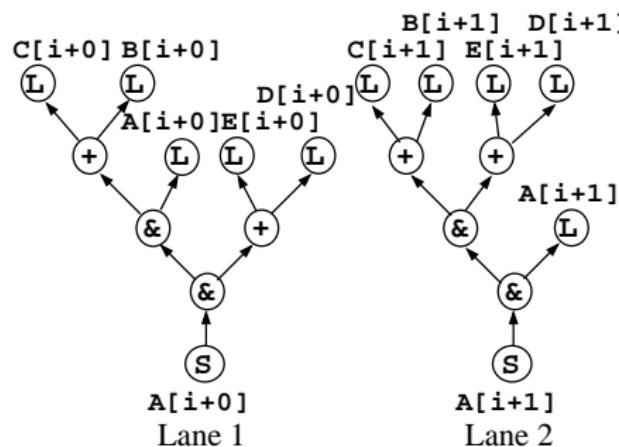
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



3/3 Chains of Commutative Operations

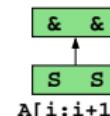
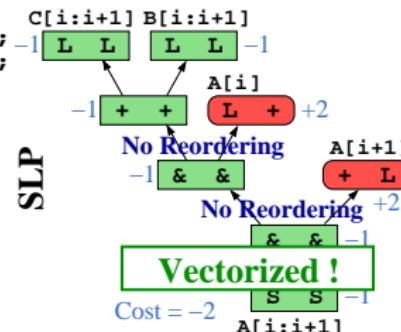
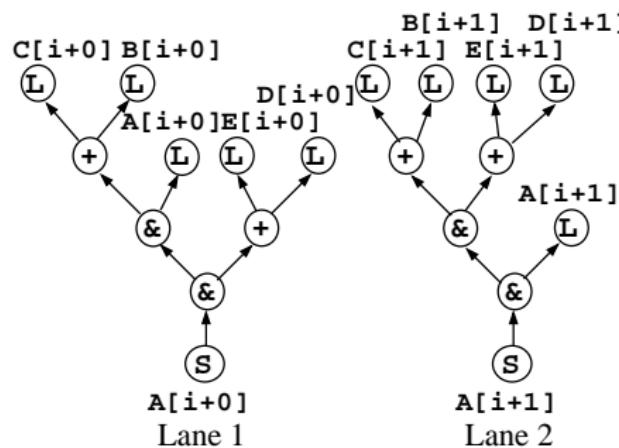
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



● Non-Vectorizable ■ Vectorizable +/#Cost

3/3 Chains of Commutative Operations

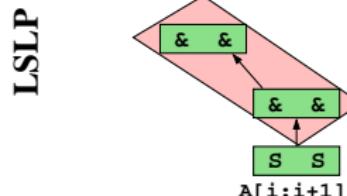
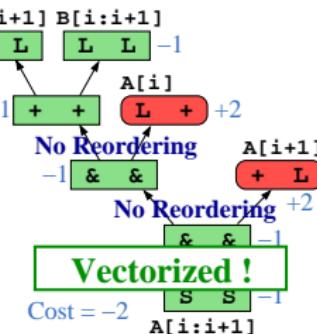
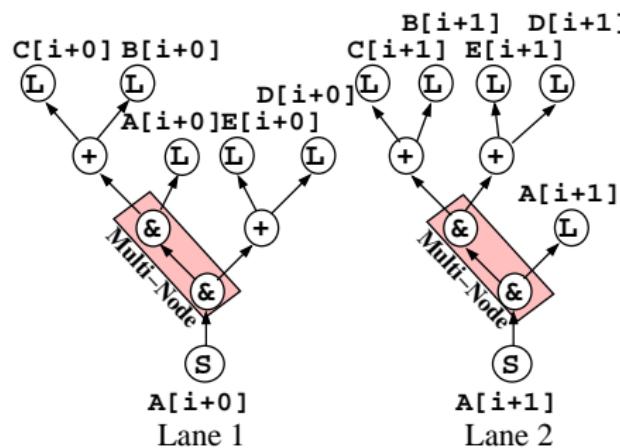
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



●	Non-Vectorizable	■	Vectorizable	$+/ \#$ Cost
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3/3 Chains of Commutative Operations

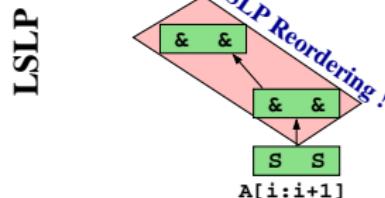
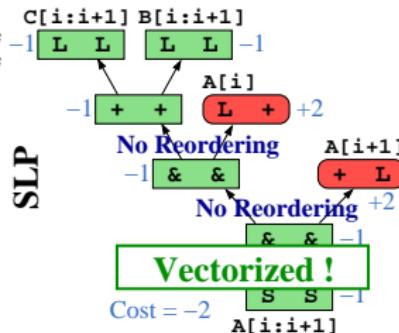
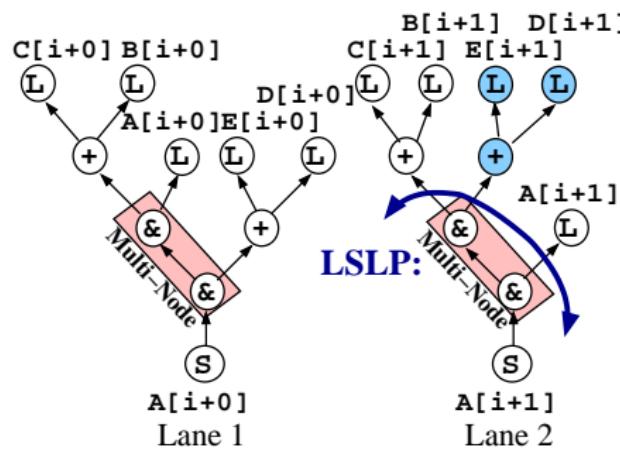
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable		#Cost
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3/3 Chains of Commutative Operations

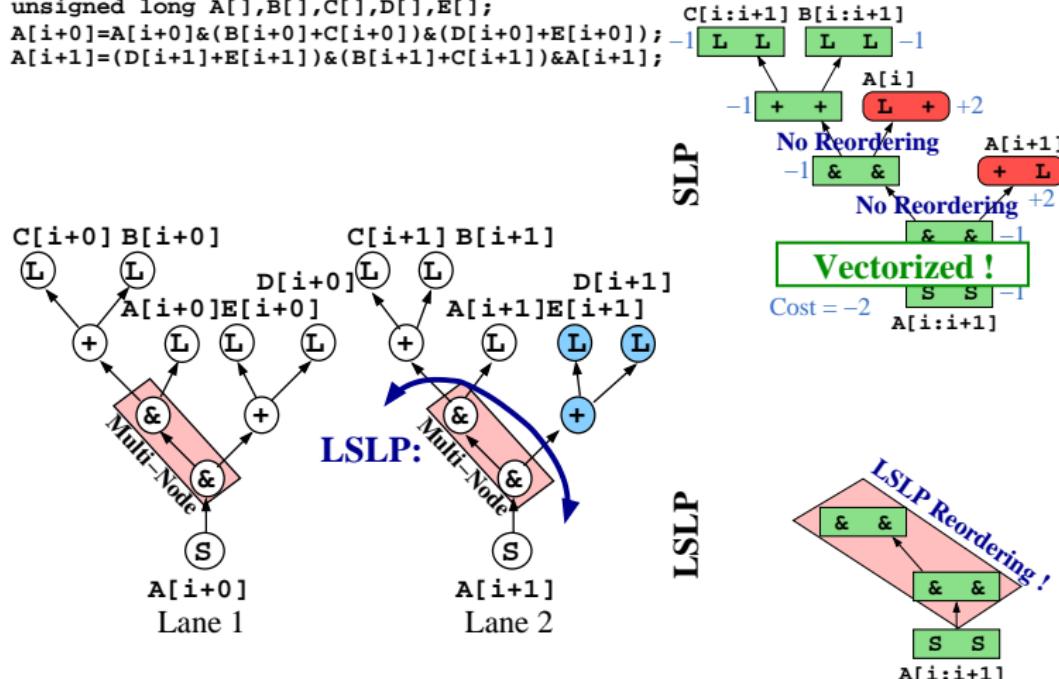
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/\#$ Cost
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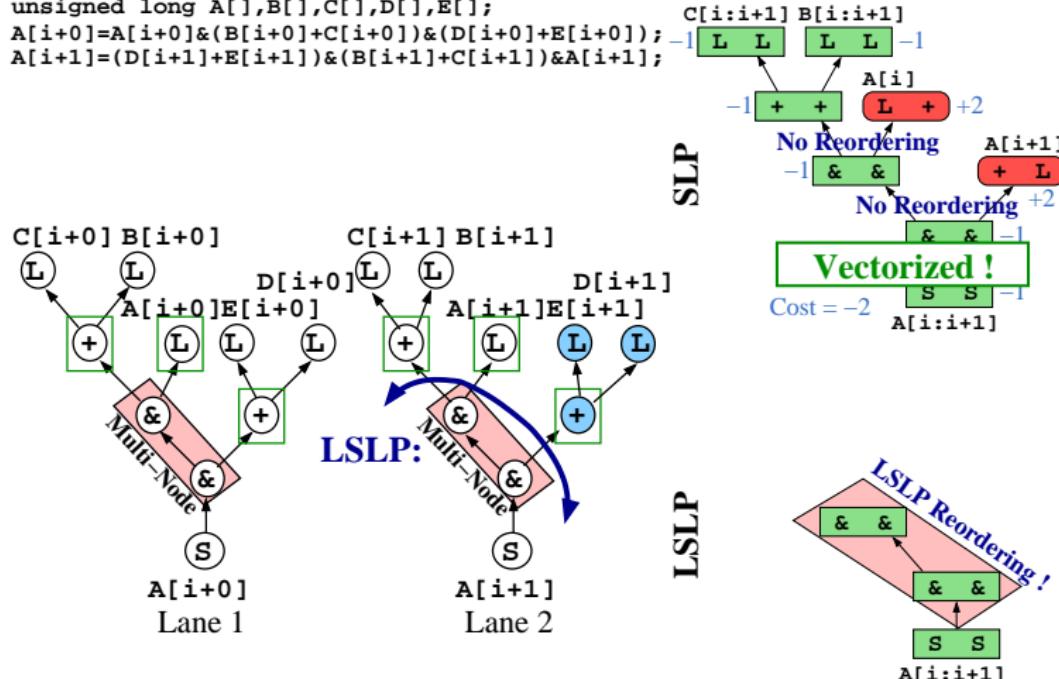
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



3/3 Chains of Commutative Operations

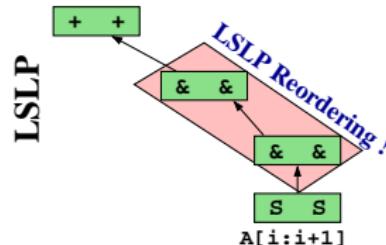
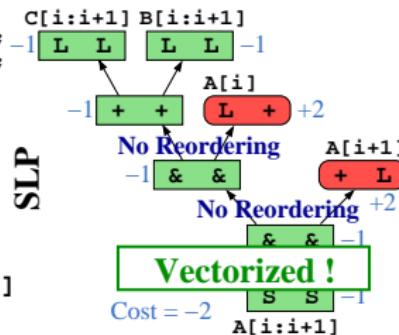
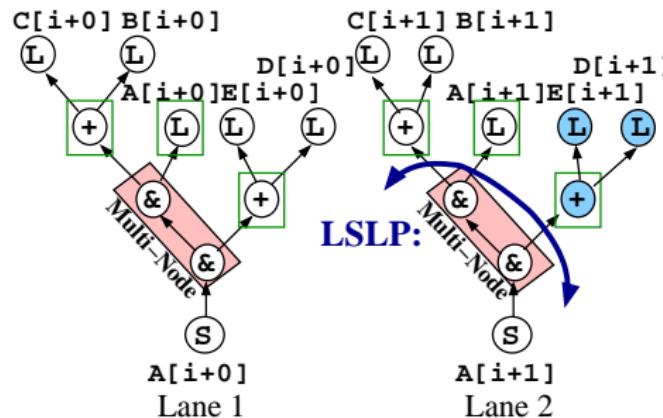
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/\#Cost$
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3/3 Chains of Commutative Operations

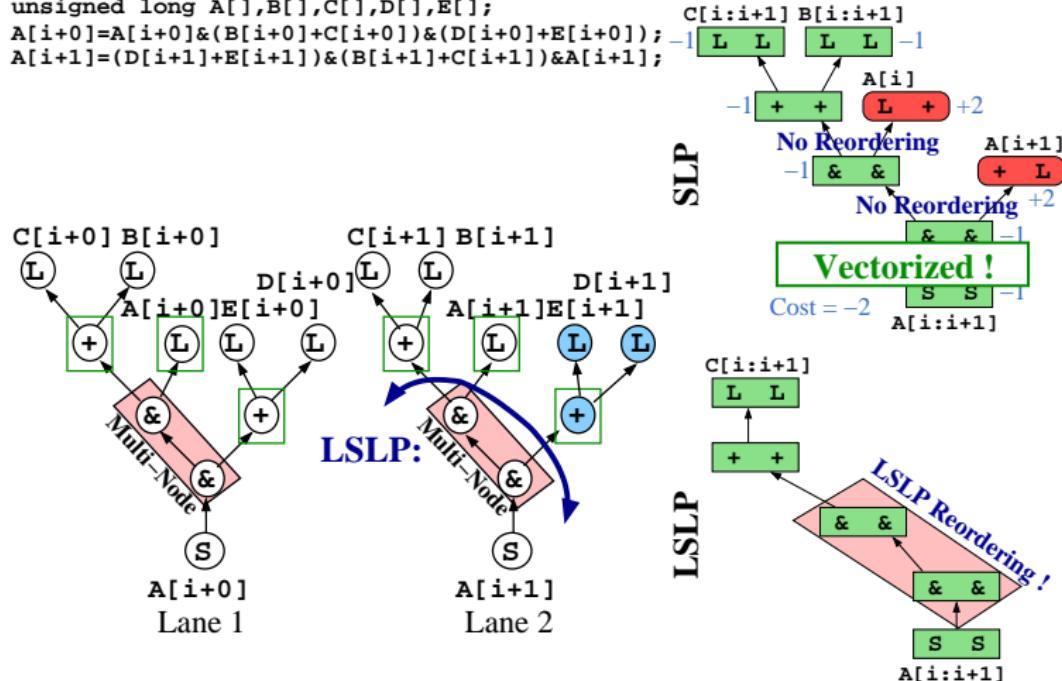
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable		#Cost
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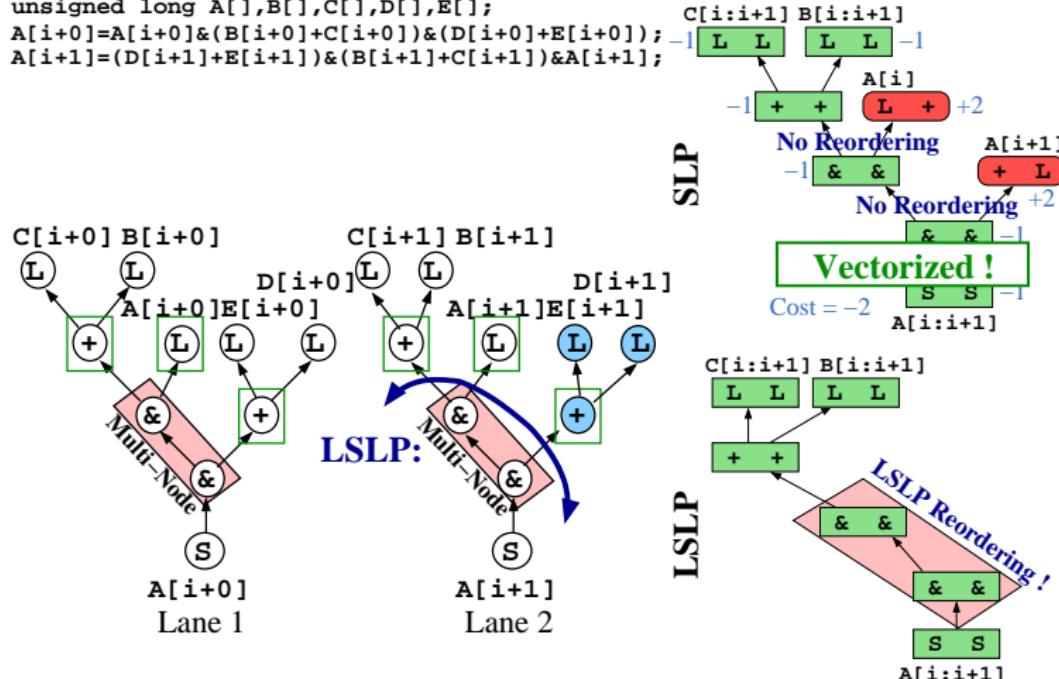
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



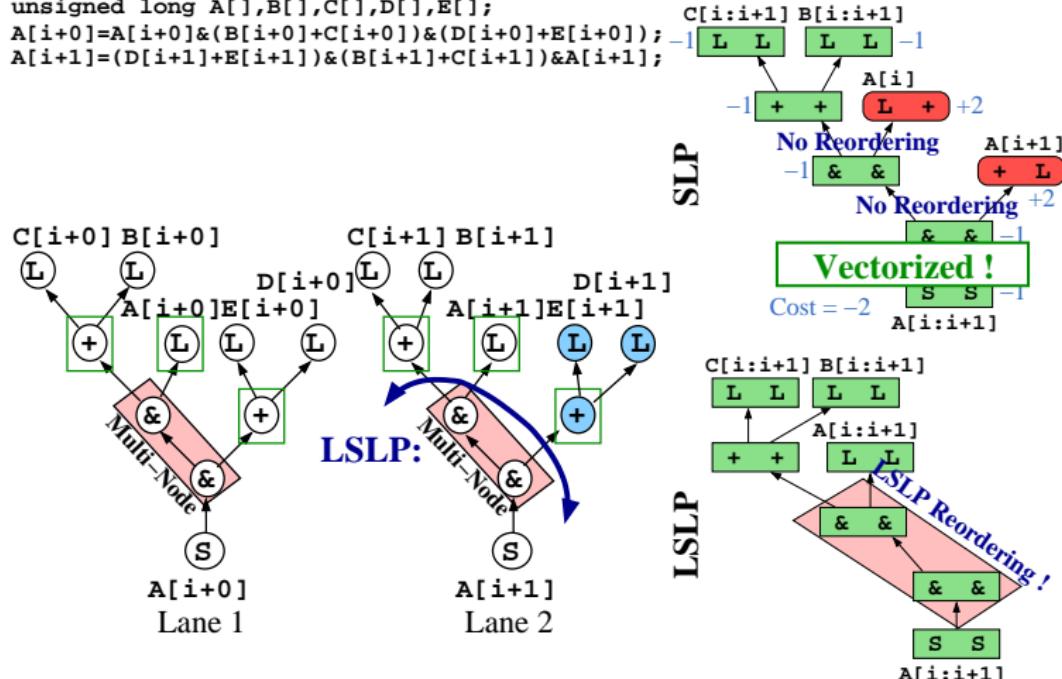
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



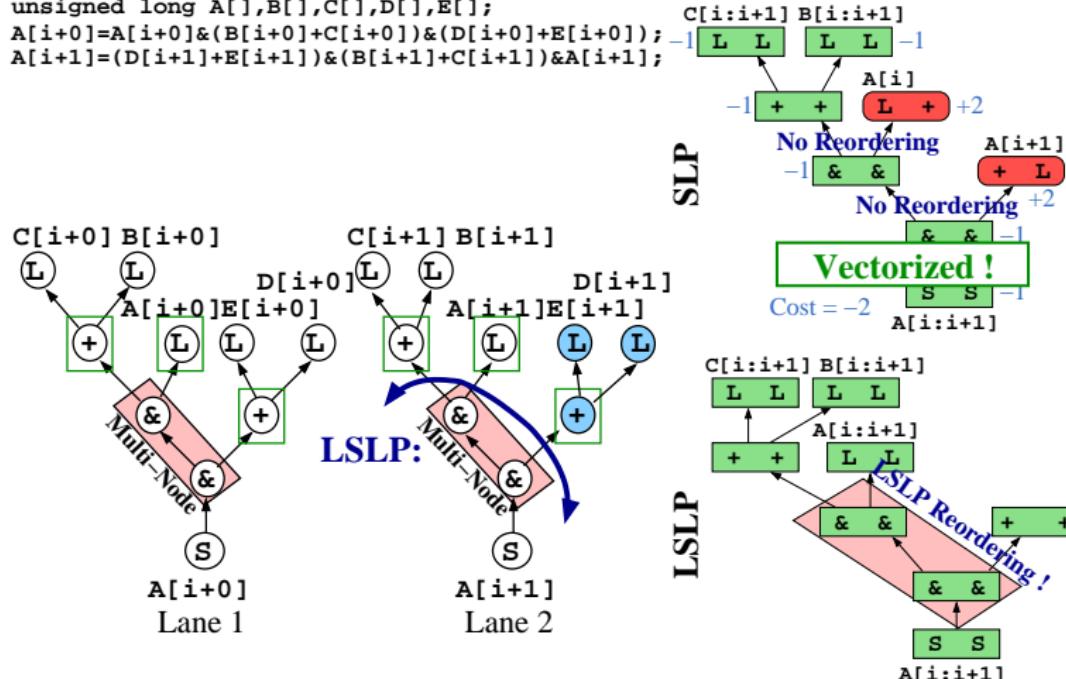
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



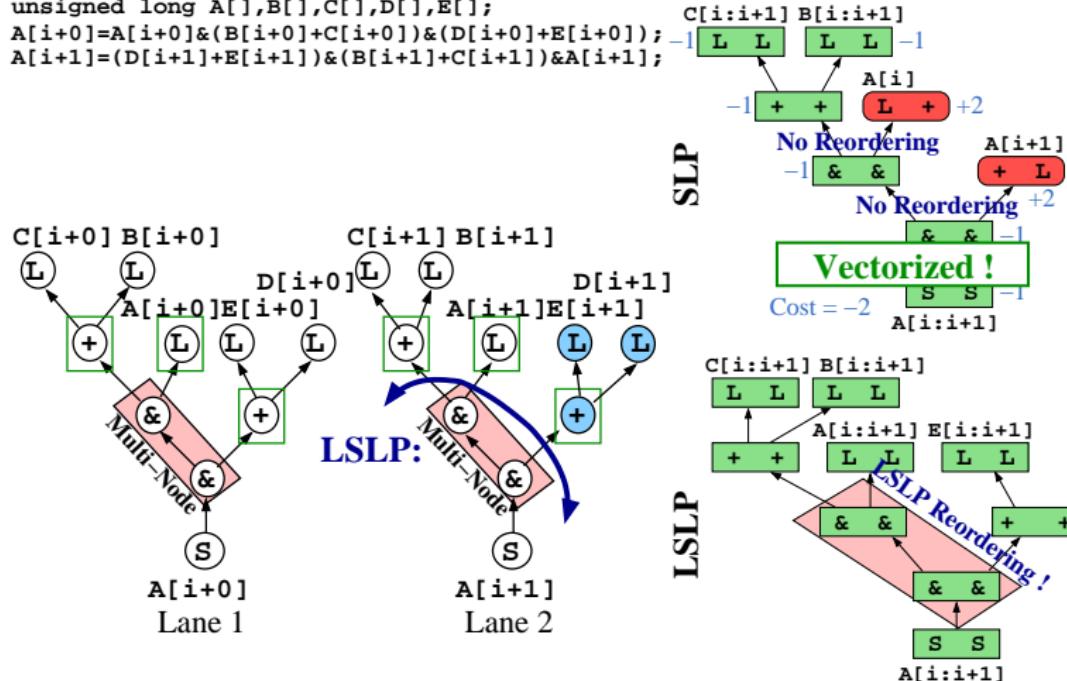
3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



3/3 Chains of Commutative Operations

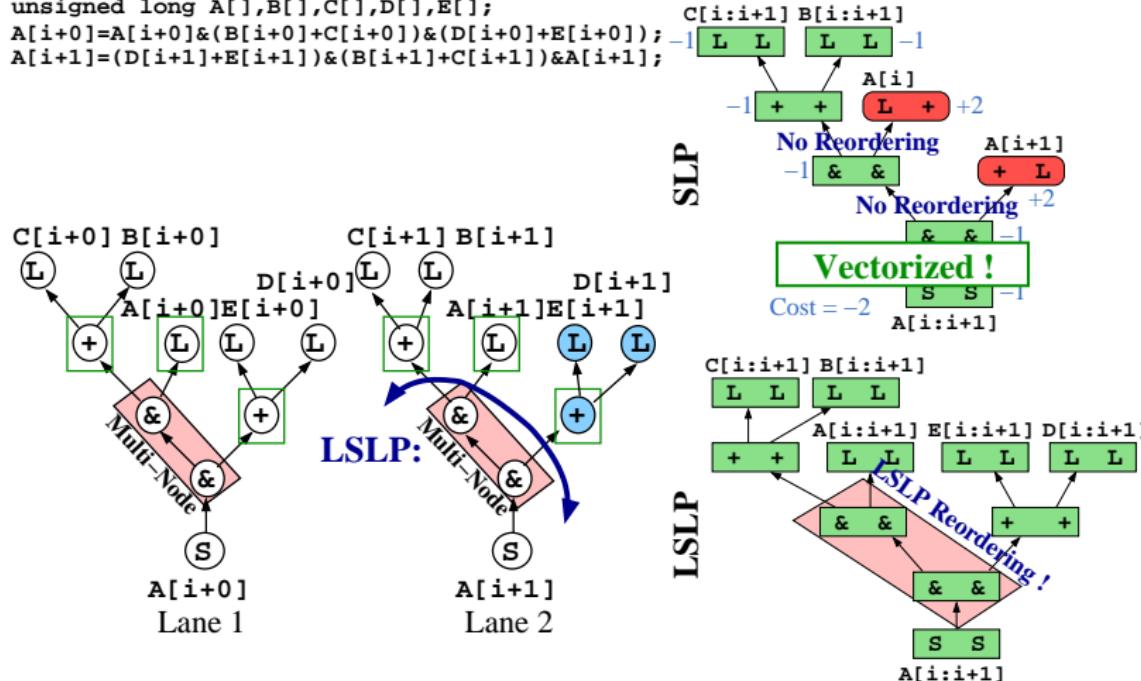
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



Non-Vectorizable	Vectorizable	$+/\#$ Cost
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3/3 Chains of Commutative Operations

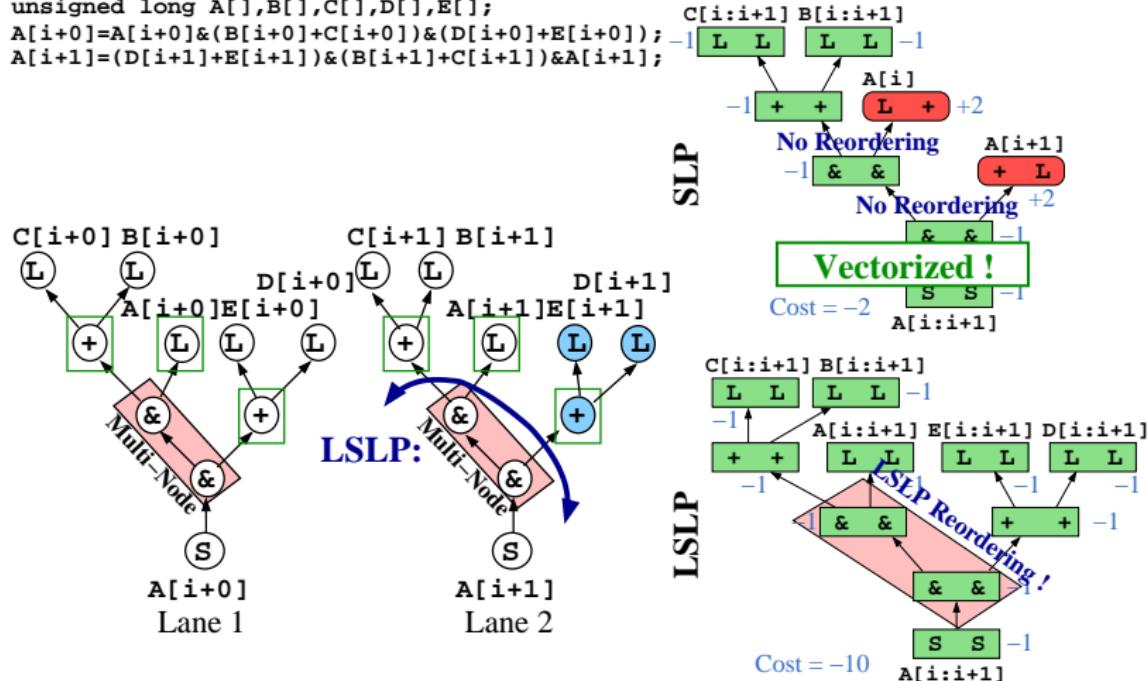
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



Non-Vectorizable	Vectorizable	$+/\#$ Cost
------------------	--------------	-------------

3/3 Chains of Commutative Operations

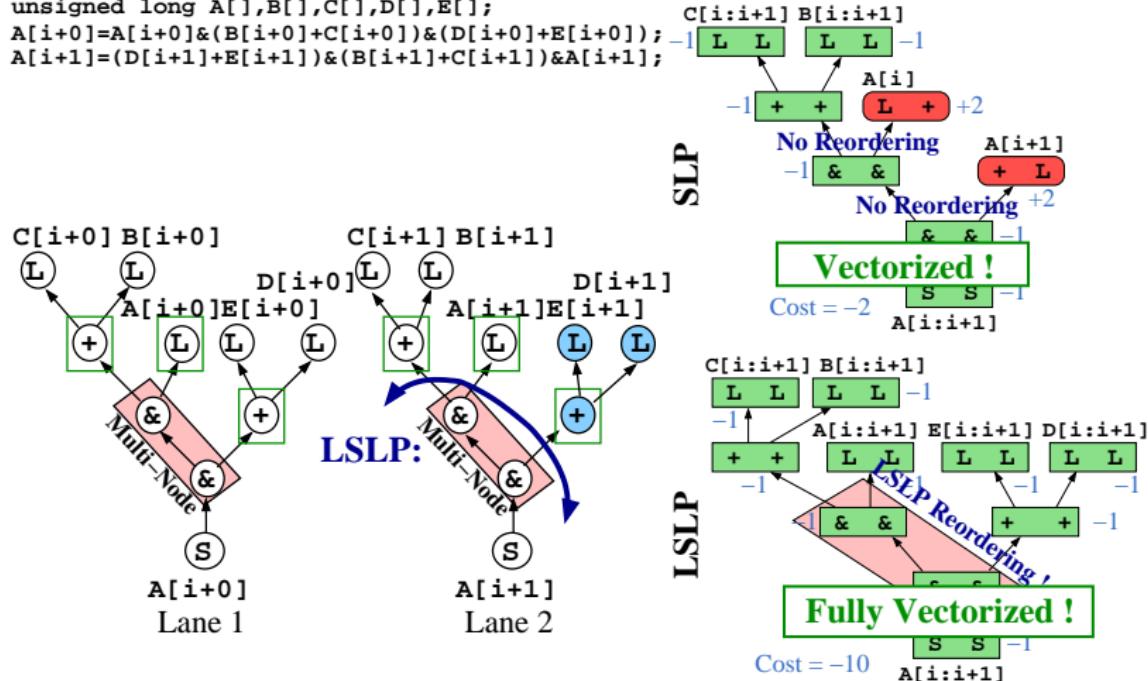
```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```



	Non-Vectorizable		Vectorizable	$+/\#Cost$
--	------------------	--	--------------	------------

3/3 Chains of Commutative Operations

```
unsigned long A[],B[],C[],D[],E[];
A[i+0]=A[i+0]&(B[i+0]+C[i+0])&(D[i+0]+E[i+0]);
A[i+1]=(D[i+1]+E[i+1])&(B[i+1]+C[i+1])&A[i+1];
```

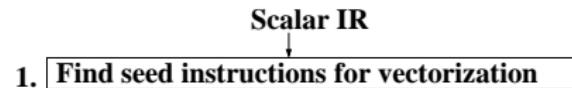


● Non-Vectorizable ● Vectorizable +/#Cost

(L)SLP Algorithm

- Seed instructions are usually:

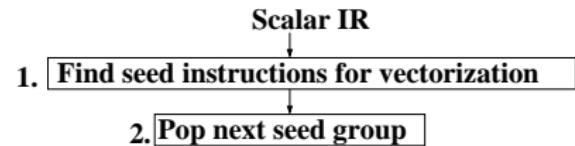
- ① Consecutive Stores
- ② Reductions



(L)SLP Algorithm

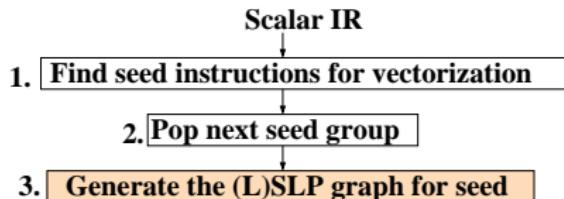
- Seed instructions are usually:

- ① Consecutive Stores
- ② Reductions



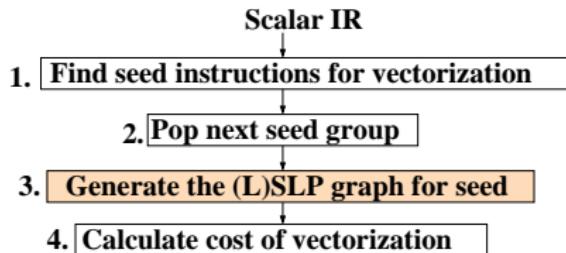
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions



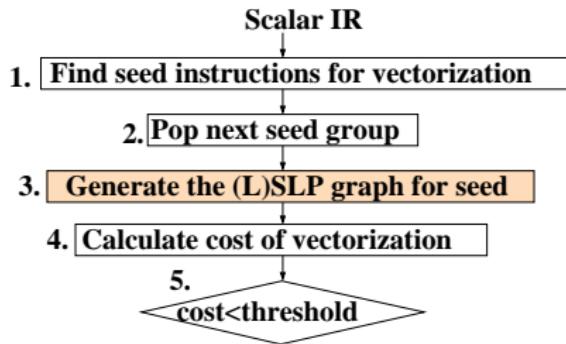
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count



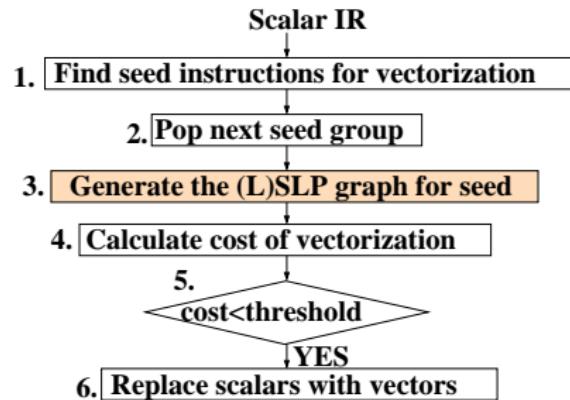
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability



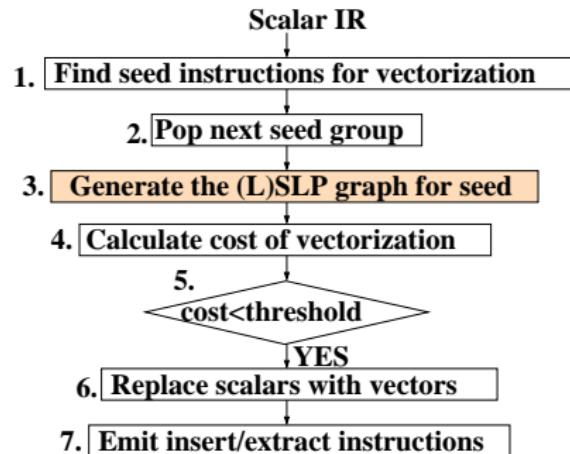
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability
- Generate vector code



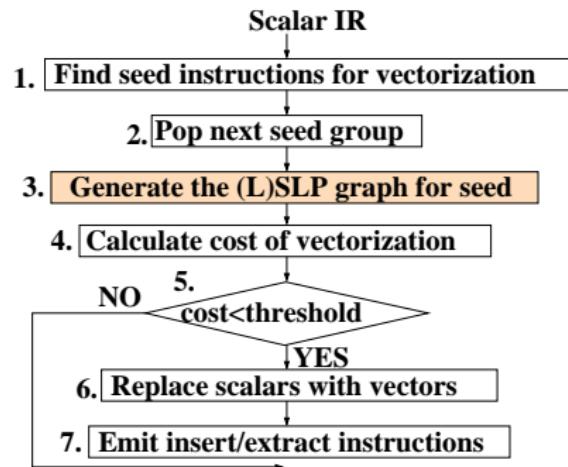
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability
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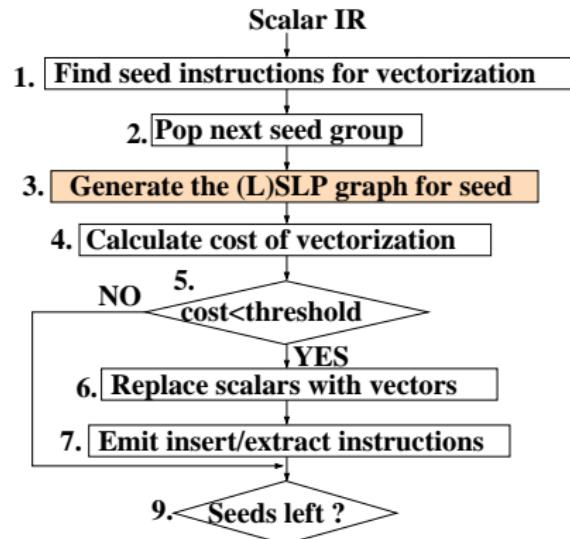
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
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- Check overall profitability
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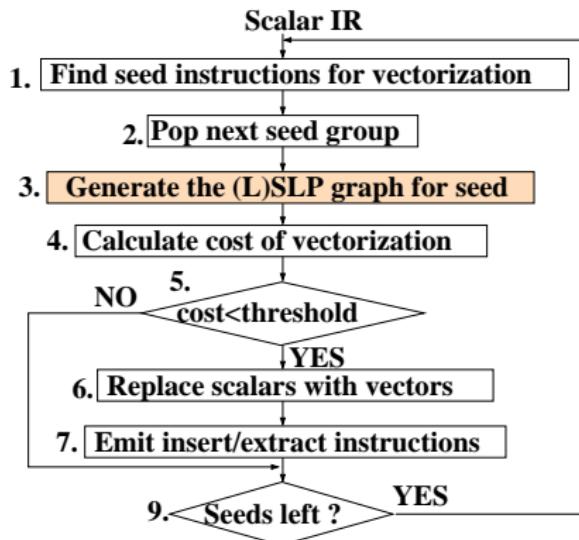
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability
- Generate vector code



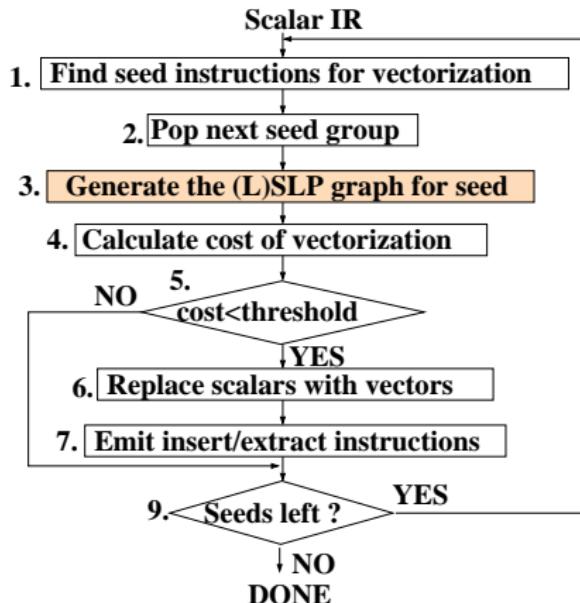
(L)SLP Algorithm

- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability
- Generate vector code
- Repeat



(L)SLP Algorithm

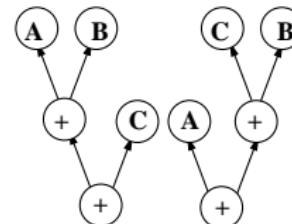
- Seed instructions are usually:
 - ① Consecutive Stores
 - ② Reductions
- Graph contains groups of vectorizable instructions
- Cost: weighted instr. count
- Check overall profitability
- Generate vector code
- Repeat



(L)SLP Graph Formation

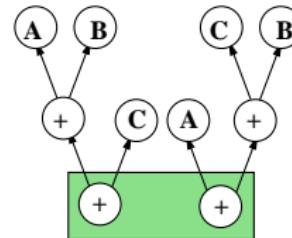
SLP

Entry



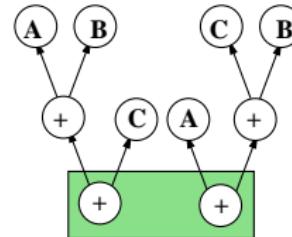
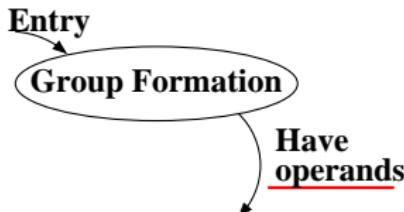
(L)SLP Graph Formation

SLP

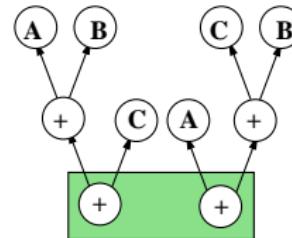
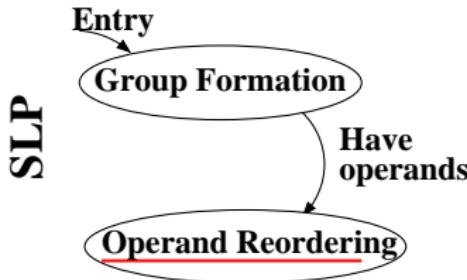


(L)SLP Graph Formation

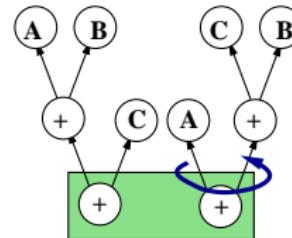
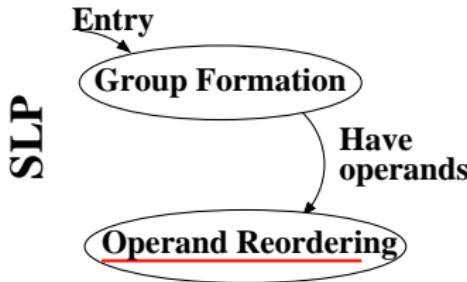
SLP



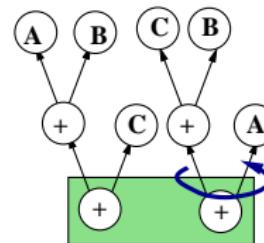
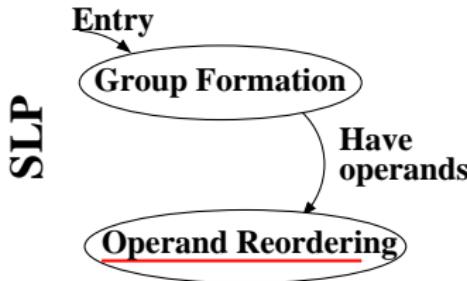
(L)SLP Graph Formation



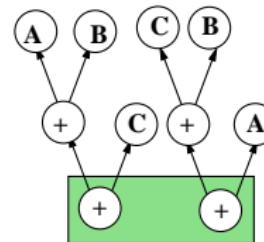
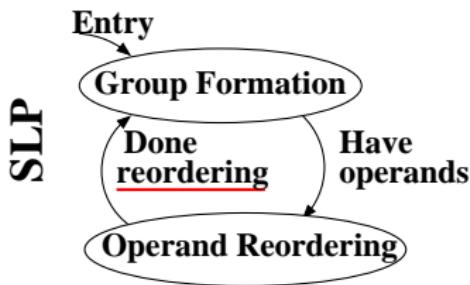
(L)SLP Graph Formation



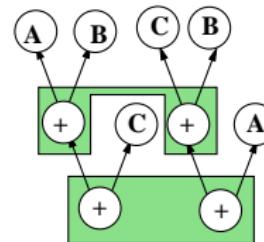
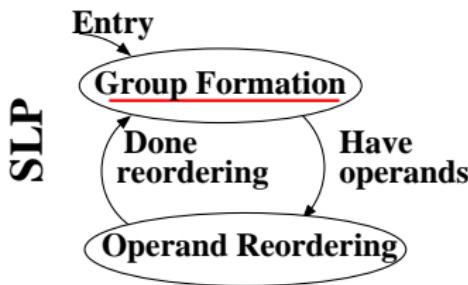
(L)SLP Graph Formation



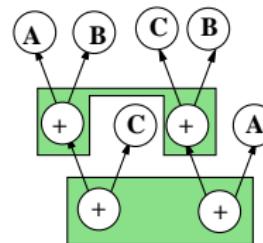
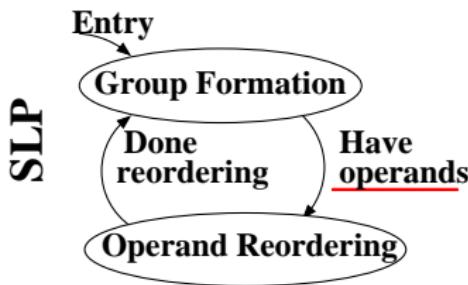
(L)SLP Graph Formation



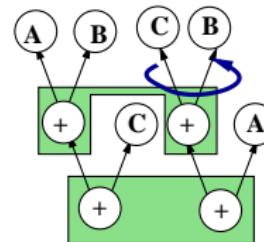
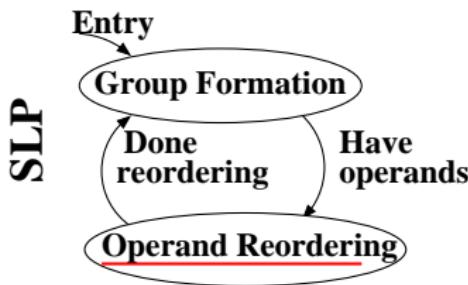
(L)SLP Graph Formation



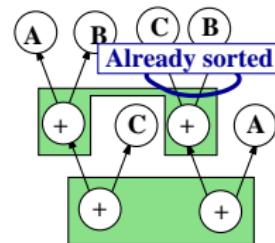
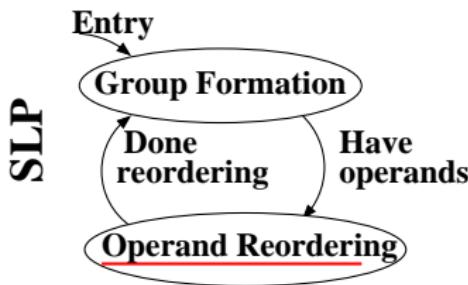
(L)SLP Graph Formation



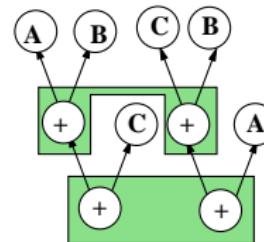
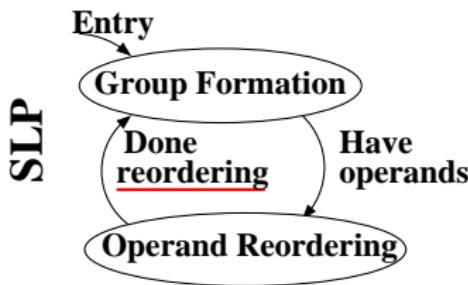
(L)SLP Graph Formation



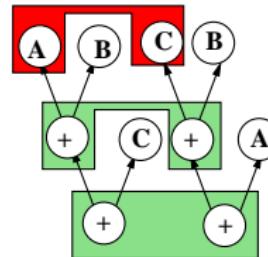
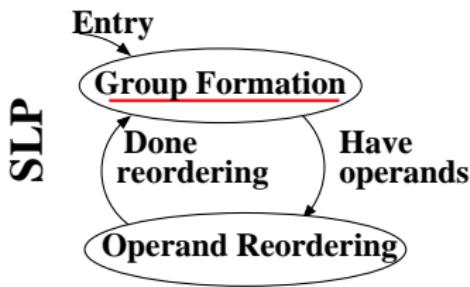
(L)SLP Graph Formation



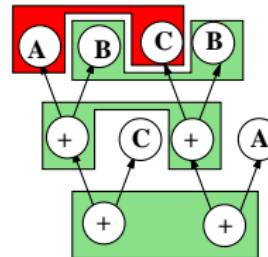
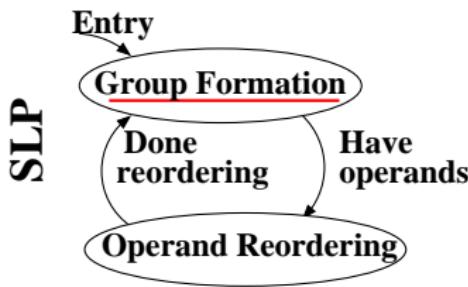
(L)SLP Graph Formation



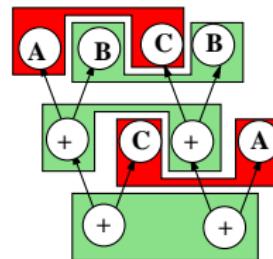
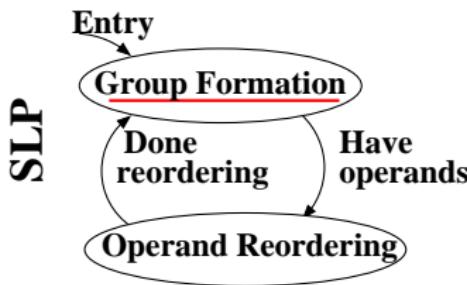
(L)SLP Graph Formation



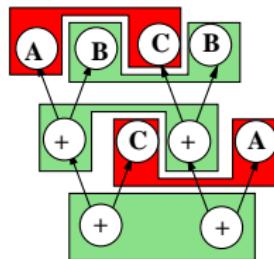
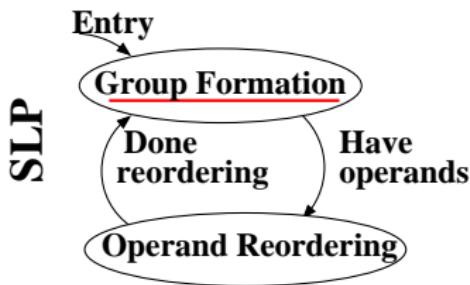
(L)SLP Graph Formation



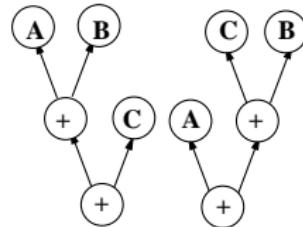
(L)SLP Graph Formation



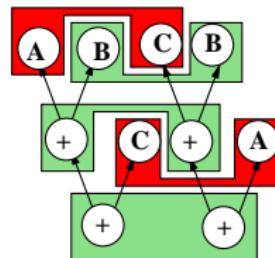
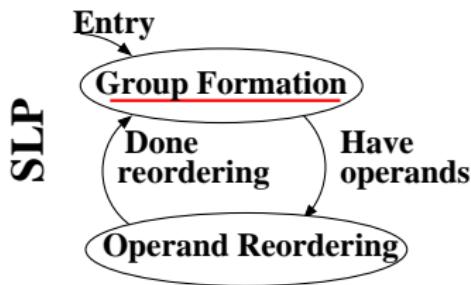
(L)SLP Graph Formation



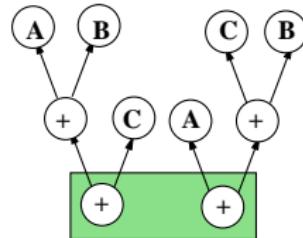
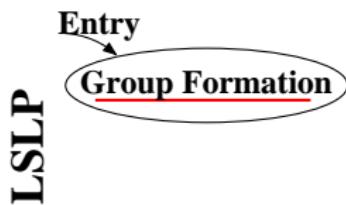
- LSPL: Extended DAG Formation with Multi-Nodes



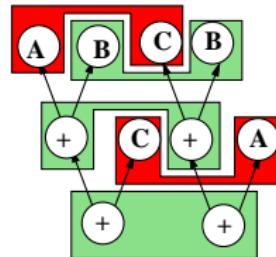
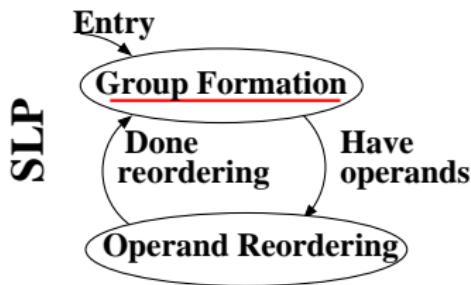
(L)SLP Graph Formation



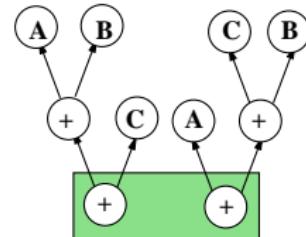
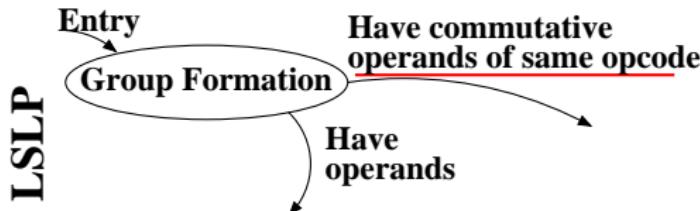
- LSPL: Extended DAG Formation with Multi-Nodes



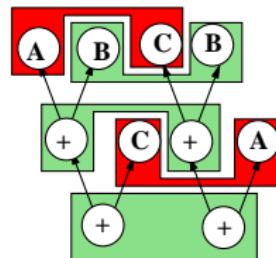
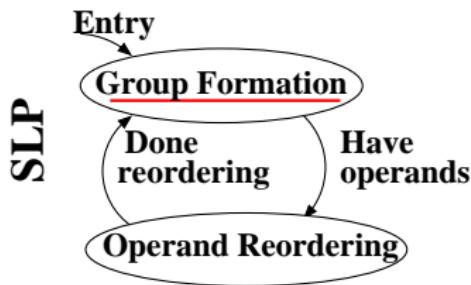
(L)SLP Graph Formation



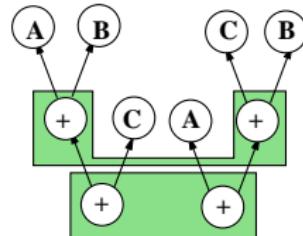
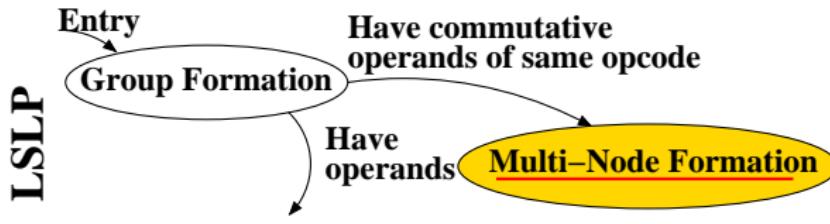
- LSPL: Extended DAG Formation with Multi-Nodes



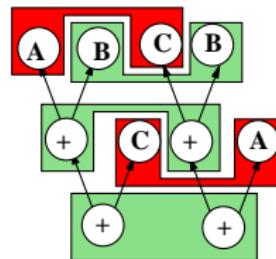
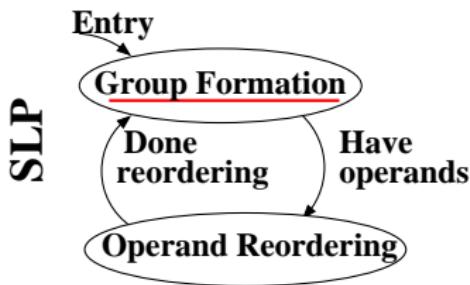
(L)SLP Graph Formation



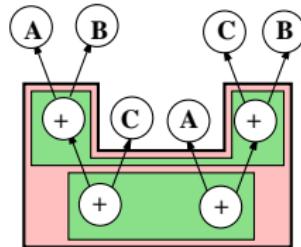
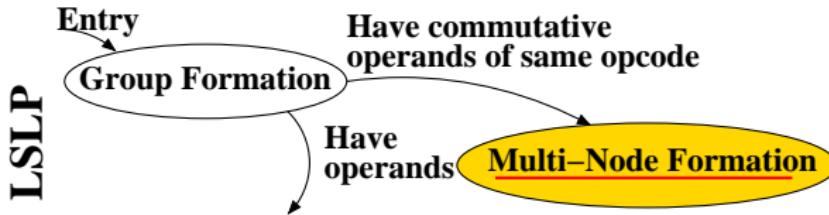
- LSLP: Extended DAG Formation with Multi-Nodes



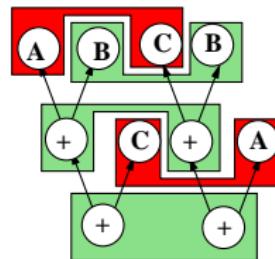
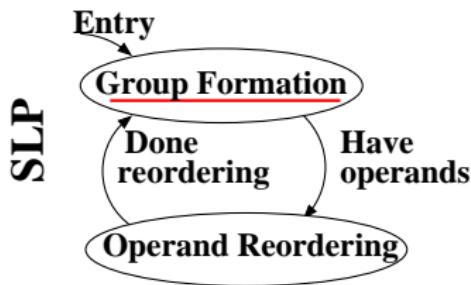
(L)SLP Graph Formation



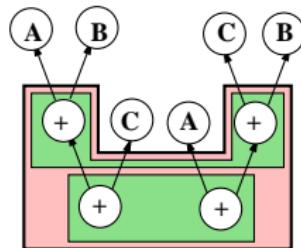
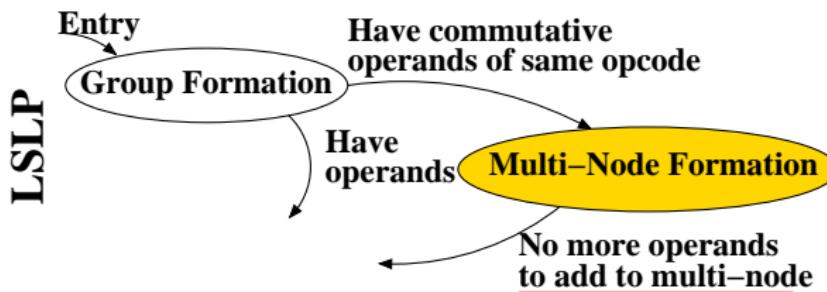
- LSPL: Extended DAG Formation with Multi-Nodes



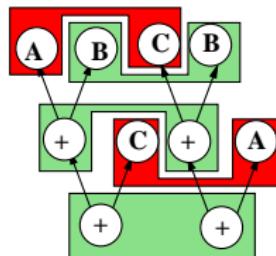
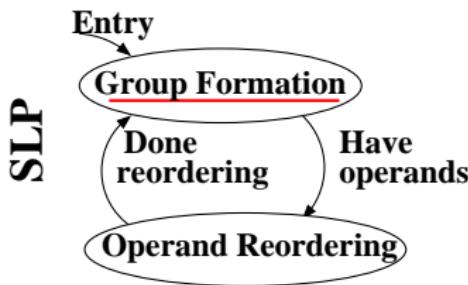
(L)SLP Graph Formation



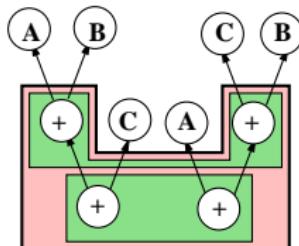
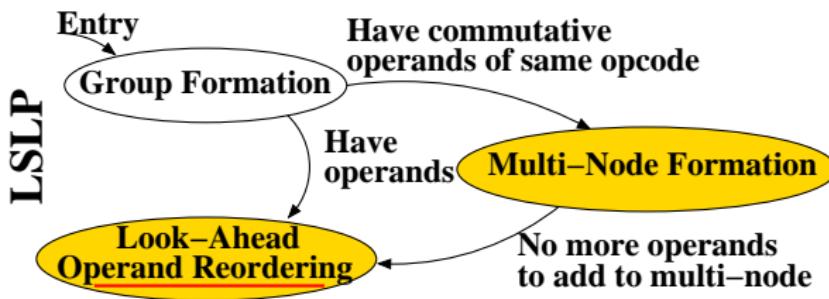
- LSPL: Extended DAG Formation with Multi-Nodes



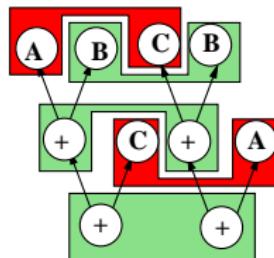
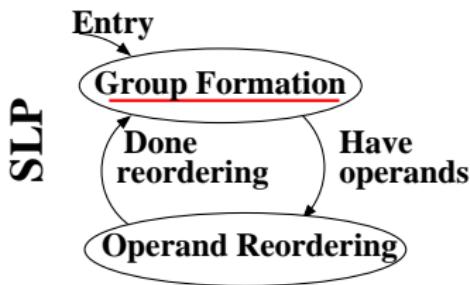
(L)SLP Graph Formation



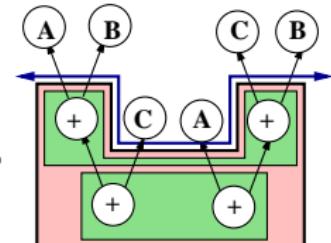
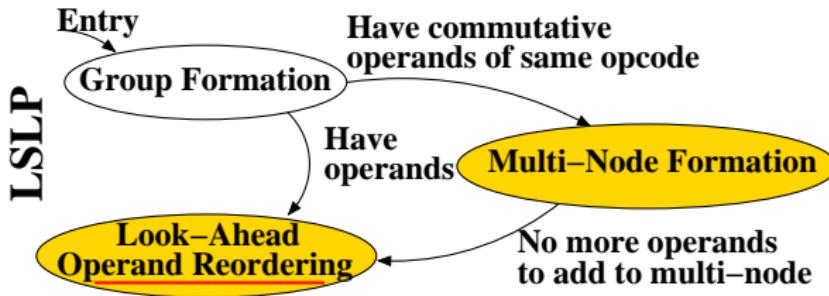
- LSLP: Extended DAG Formation with Multi-Nodes



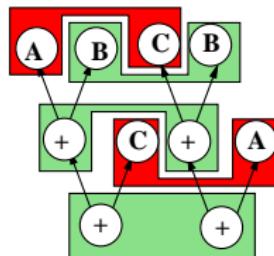
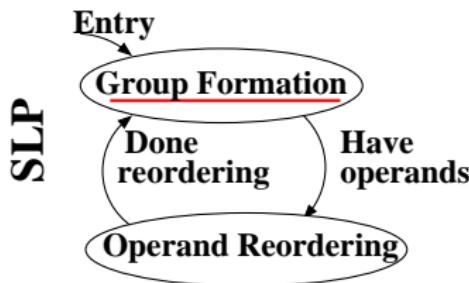
(L)SLP Graph Formation



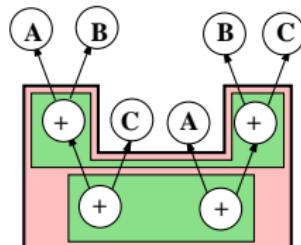
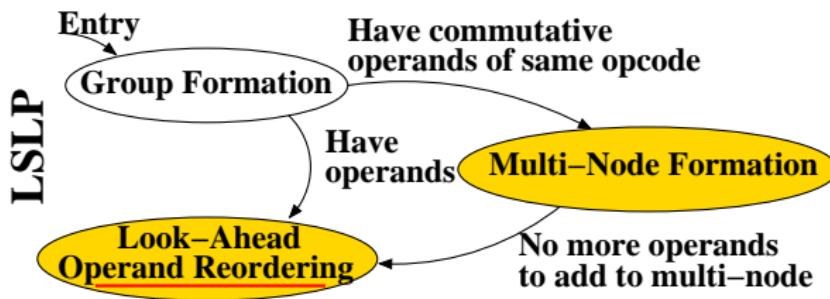
- LSLP: Extended DAG Formation with Multi-Nodes



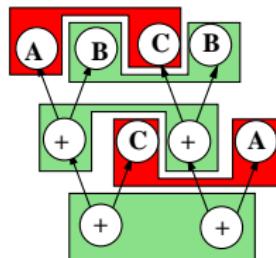
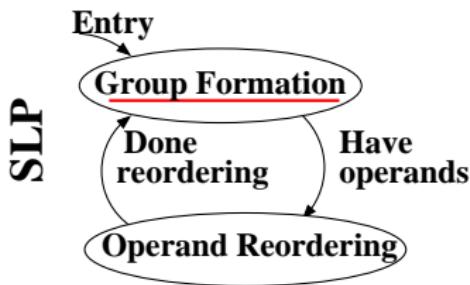
(L)SLP Graph Formation



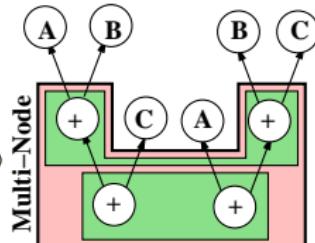
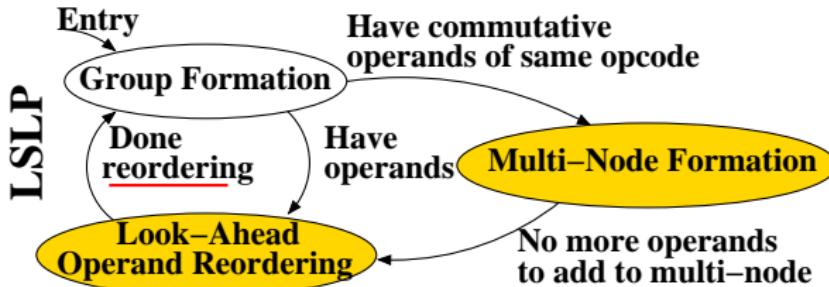
- LSLP: Extended DAG Formation with Multi-Nodes



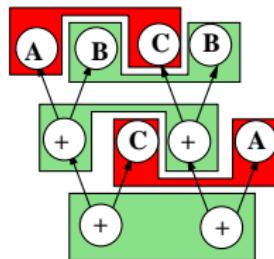
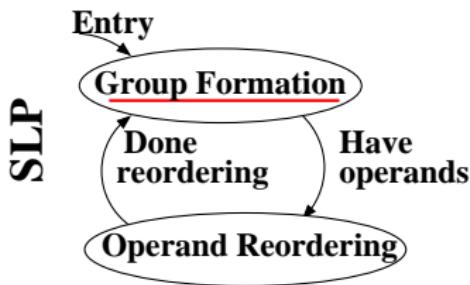
(L)SLP Graph Formation



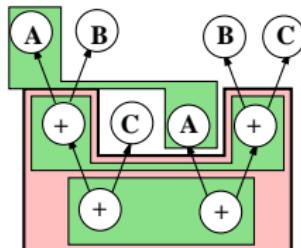
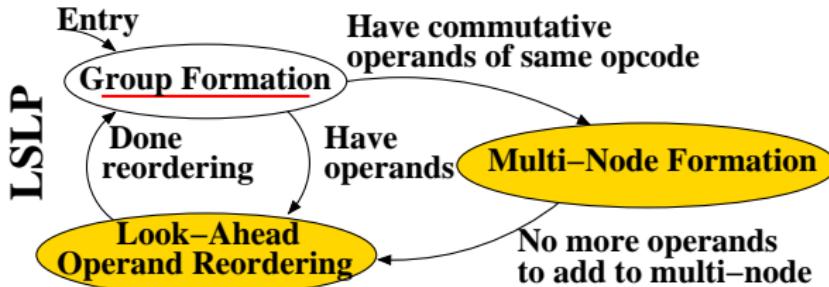
- LSLP: Extended DAG Formation with Multi-Nodes



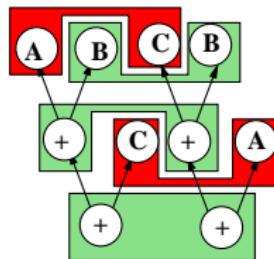
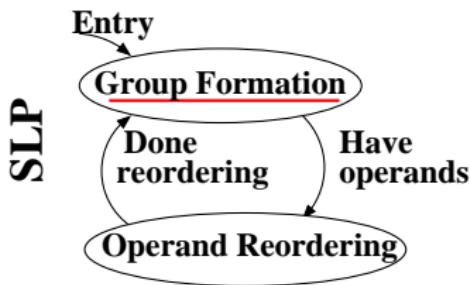
(L)SLP Graph Formation



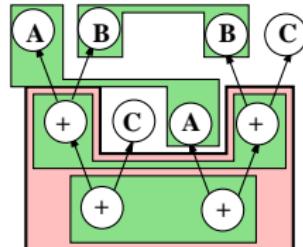
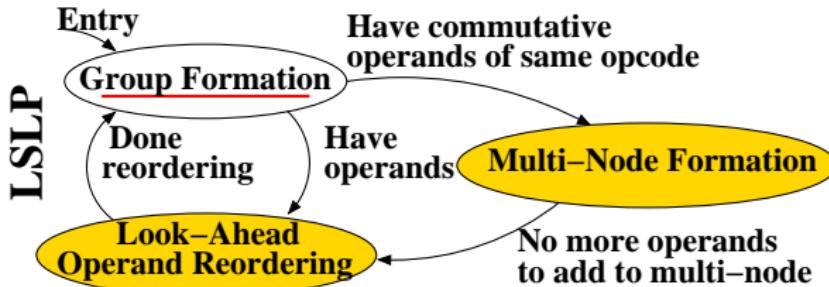
- LSLP: Extended DAG Formation with Multi-Nodes



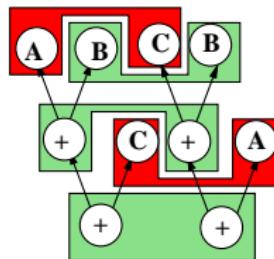
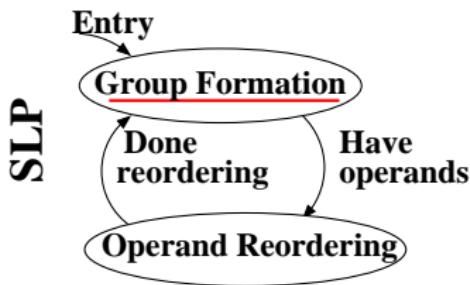
(L)SLP Graph Formation



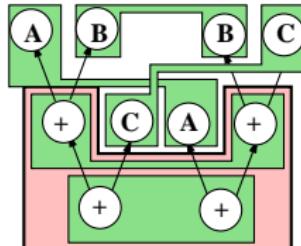
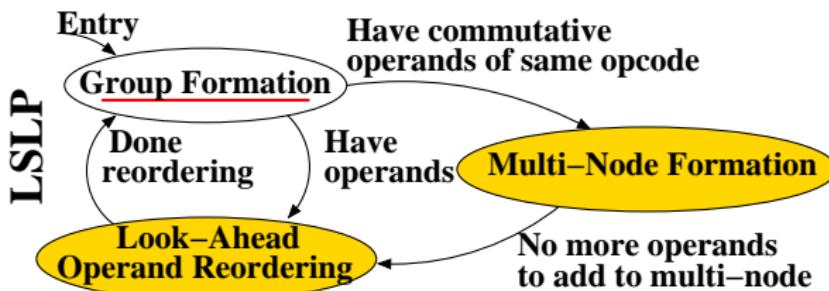
- LSLP: Extended DAG Formation with Multi-Nodes



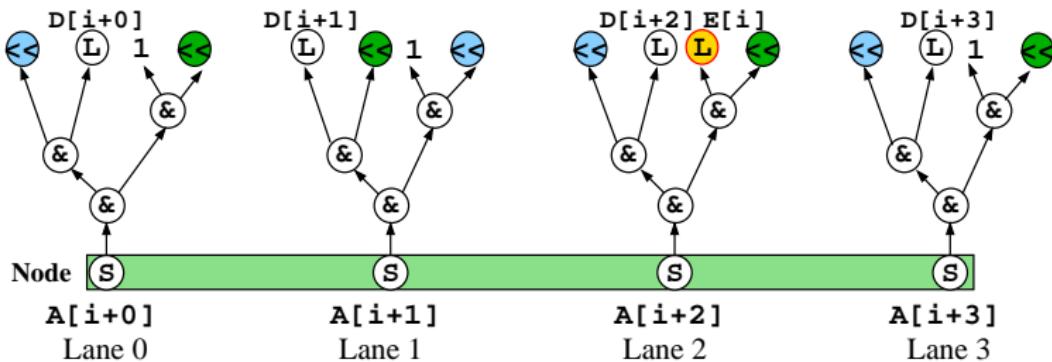
(L)SLP Graph Formation



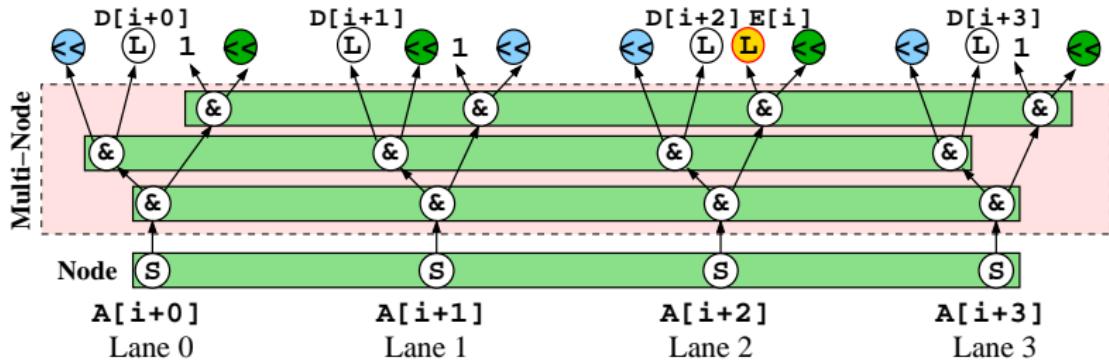
- LSLP: Extended DAG Formation with Multi-Nodes



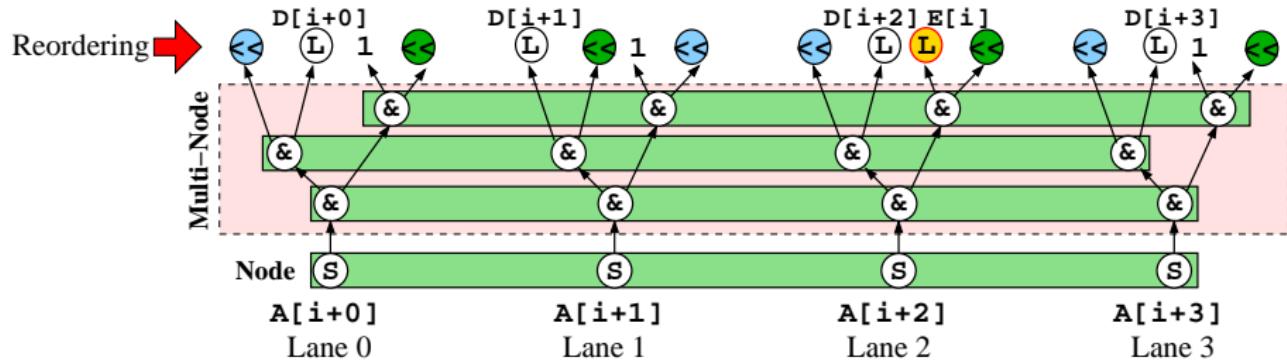
Operand Reordering with Look-Ahead



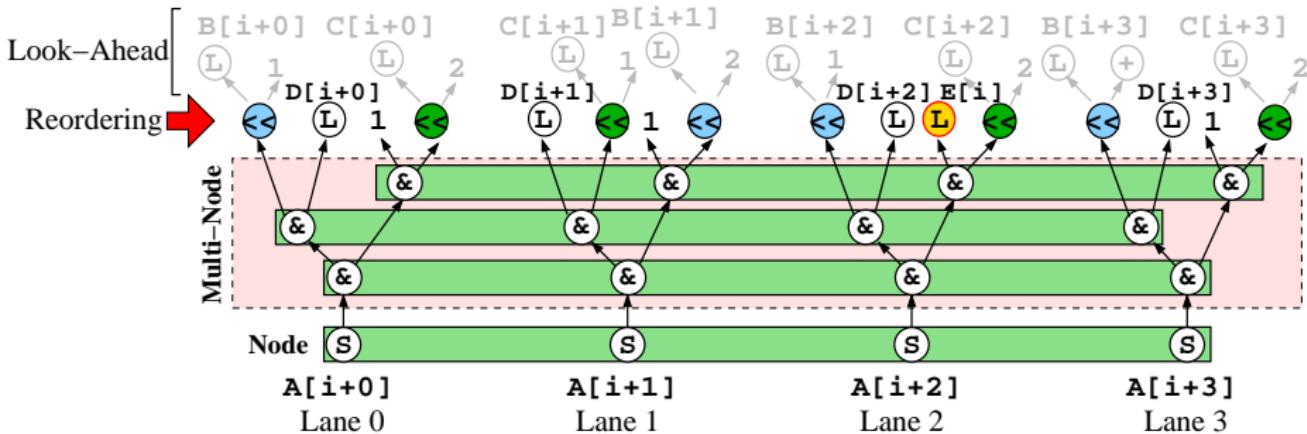
Operand Reordering with Look-Ahead



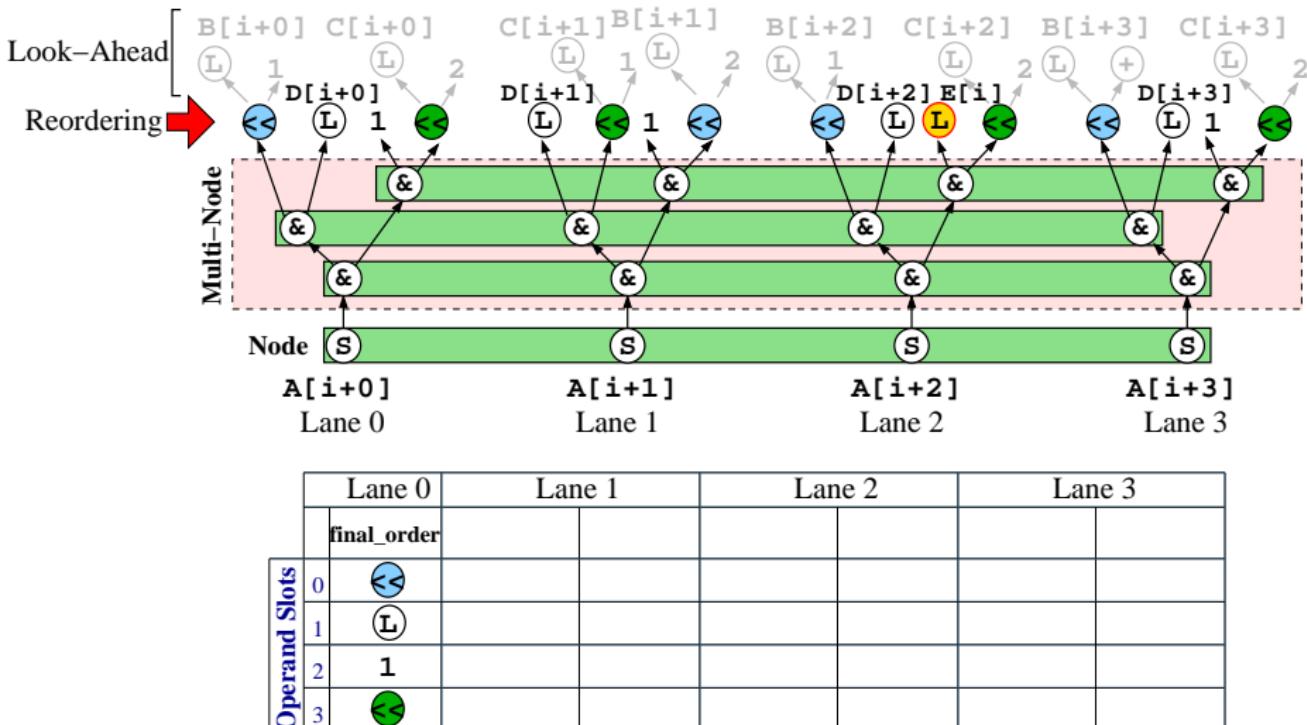
Operand Reordering with Look-Ahead



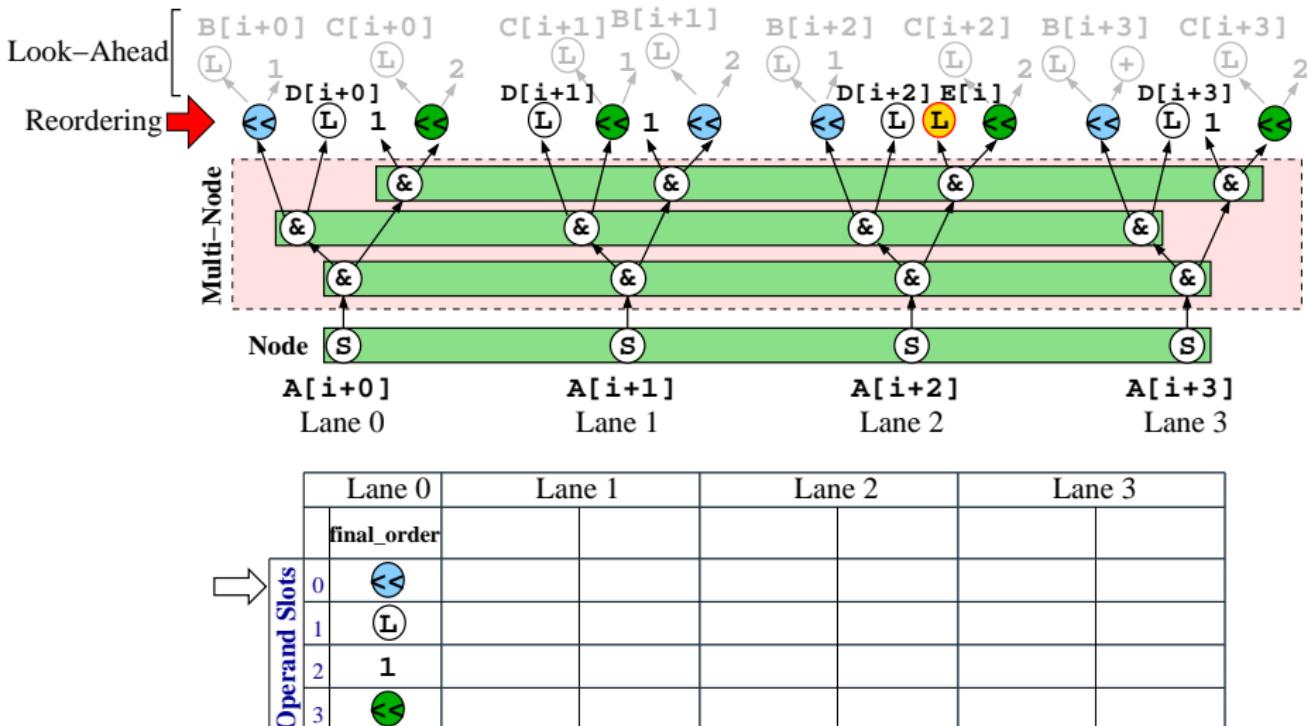
Operand Reordering with Look-Ahead



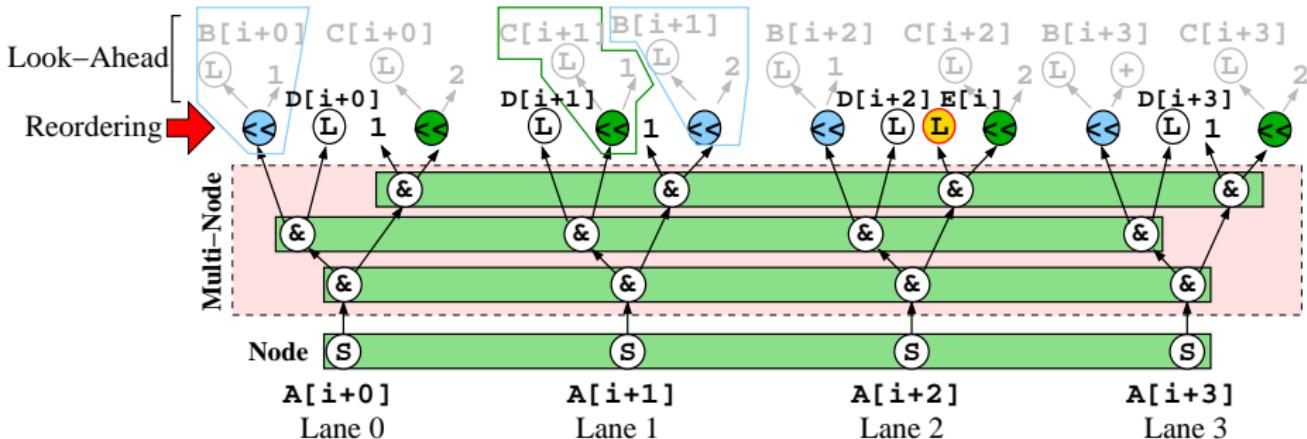
Operand Reordering with Look-Ahead



Operand Reordering with Look-Ahead



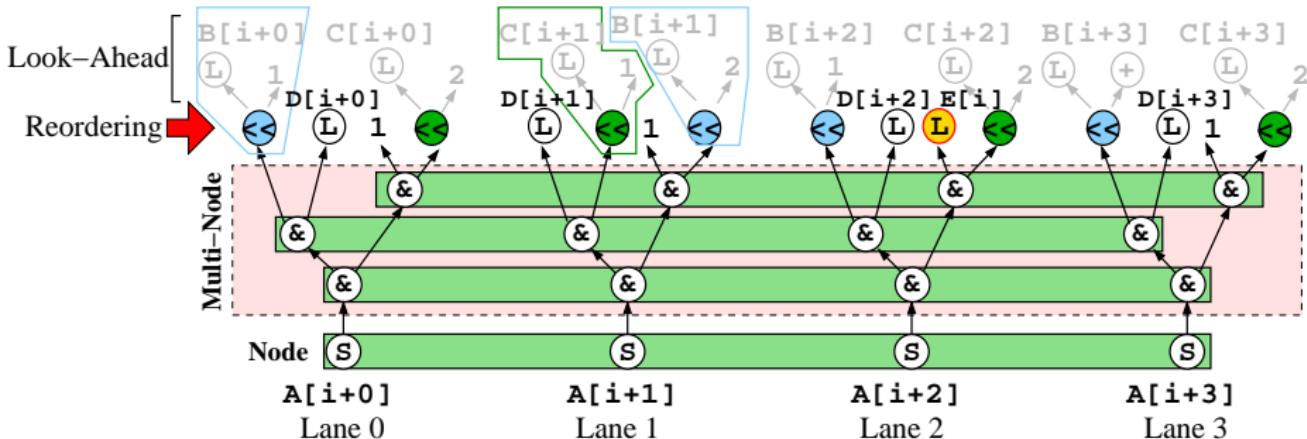
Operand Reordering with Look-Ahead



→

Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score				
Operand Slots	0	1	2	3	4	5
	0	1	2	3	4	5
	1	2	3	4	5	6
	2	3	4	5	6	7
	3	4	5	6	7	8

Operand Reordering with Look-Ahead

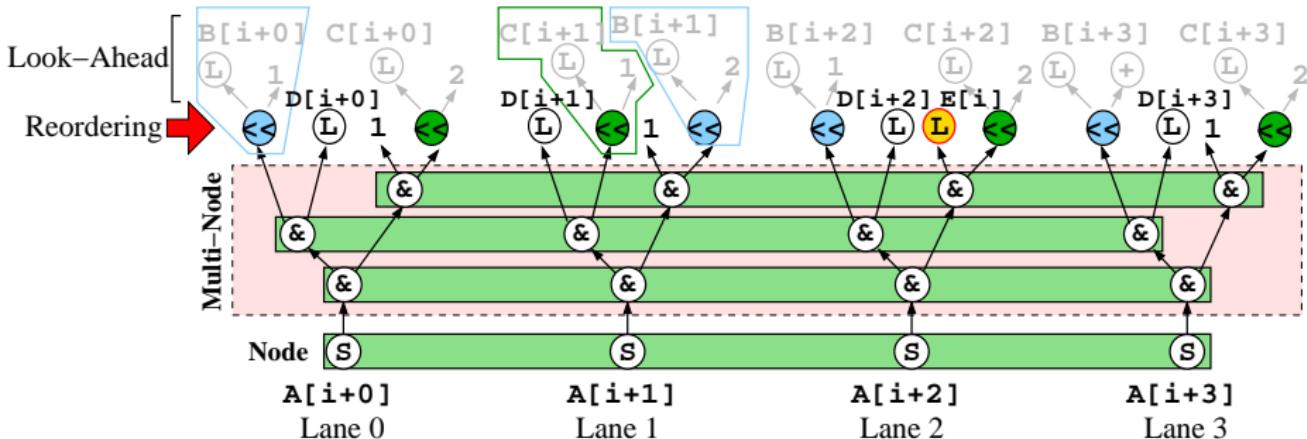


→

Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score				
0	0	1:1 2:2				
1	1					
2	2					
3	3					

Operands Slots

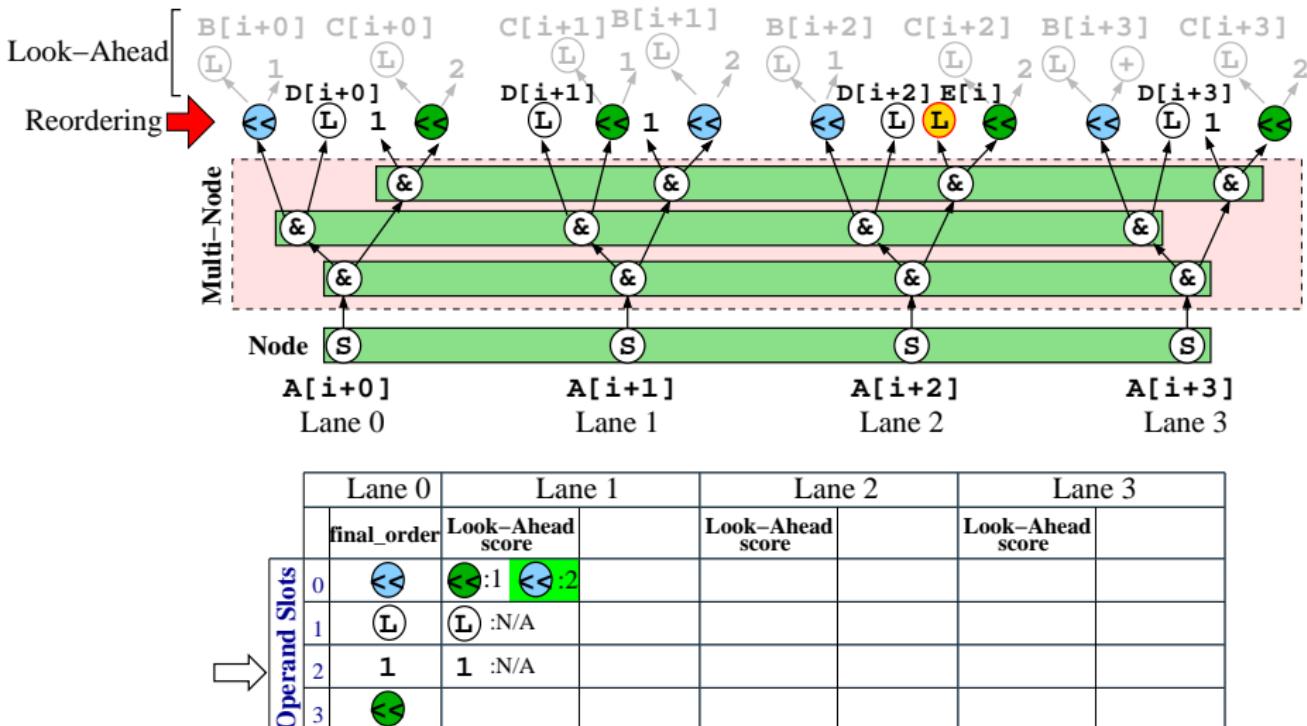
Operand Reordering with Look-Ahead



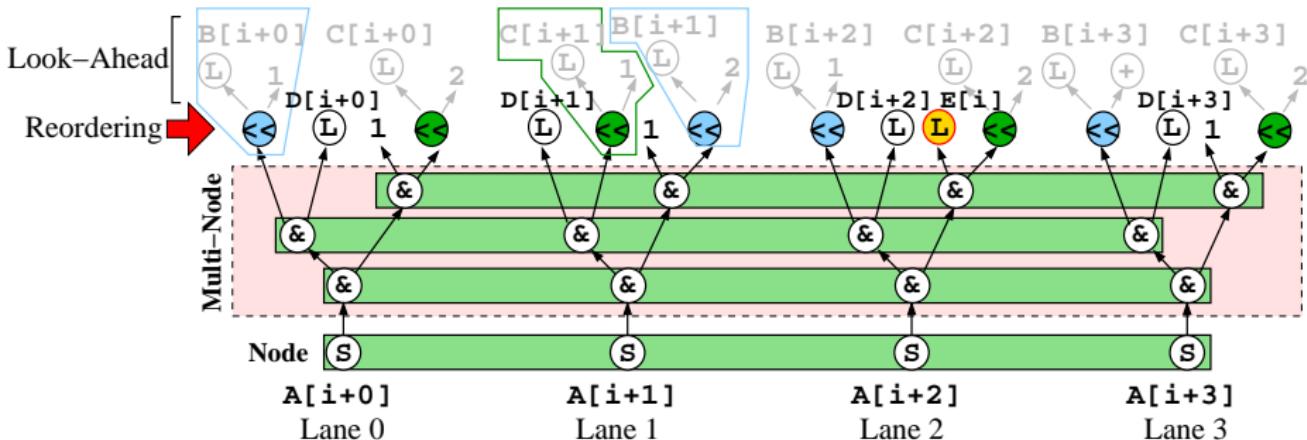
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Lane	Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score		Look-Ahead score		Look-Ahead score	
Operand Slots	0	blue	green:1	green:2			
	1	blue					
	2	1					
	3	green					

Operand Reordering with Look-Ahead



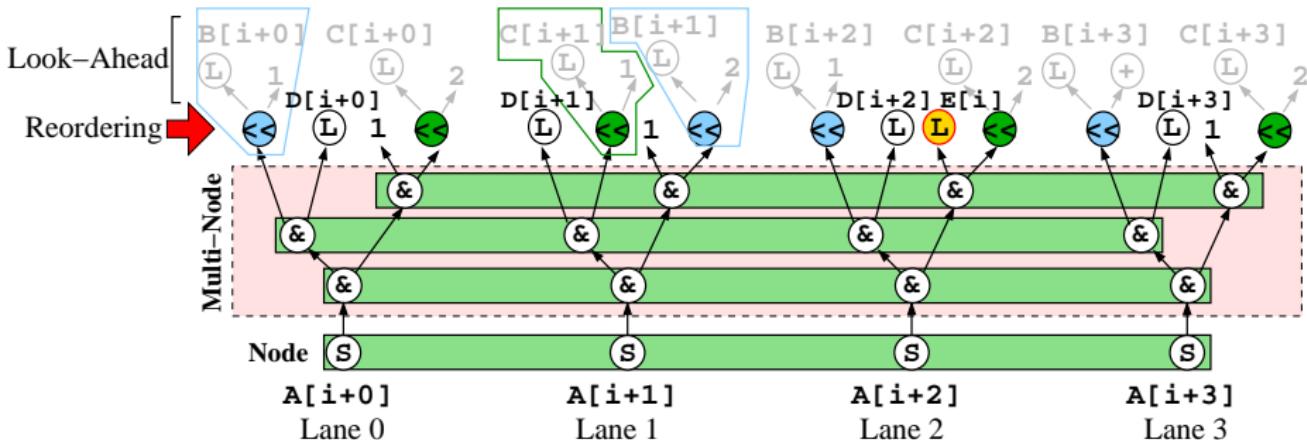
Operand Reordering with Look-Ahead



Lane 0		Lane 1		Lane 2		Lane 3	
final_order	Look-Ahead score			Look-Ahead score		Look-Ahead score	
Operand Slots	0	🕒	🕒:1	🕒:2			
	1	🕒	🕒:N/A				
	2	1	1 :N/A				
	3	🕒	🕒:2	🕒:1			

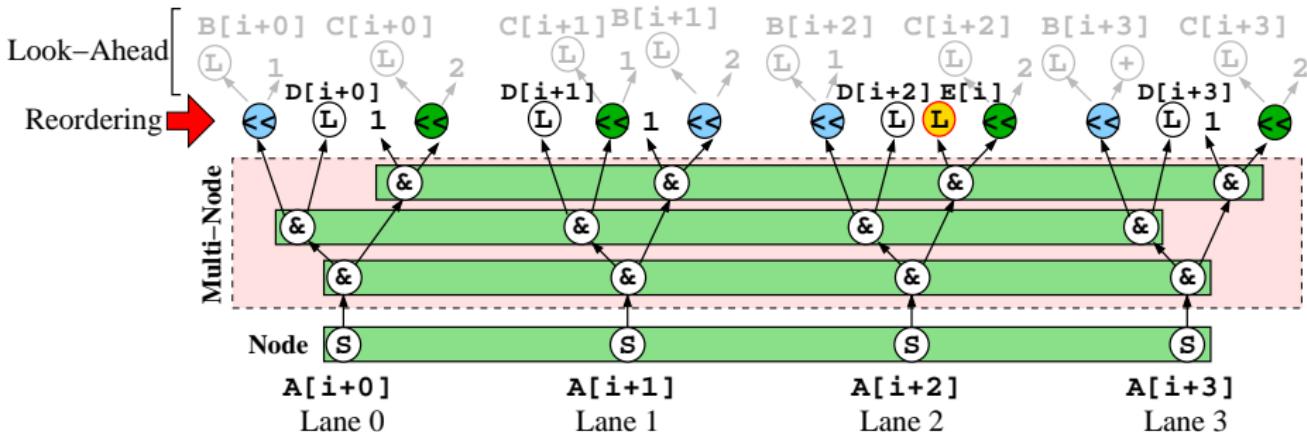
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Operand Reordering with Look-Ahead



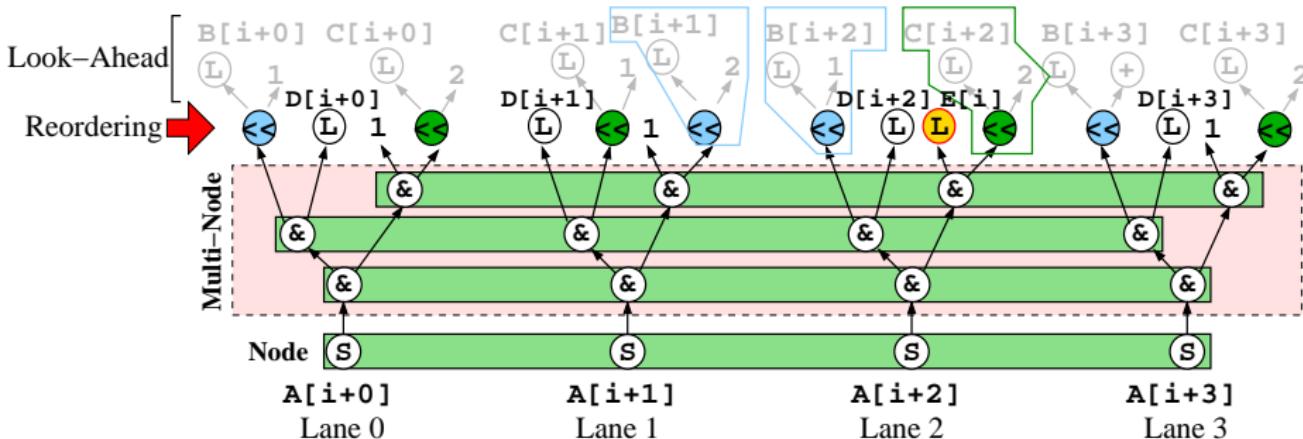
	Lane 0	Lane 1		Lane 2	Lane 3	
	final_order	Look-Ahead score		Look-Ahead score		Look-Ahead score
Operand Slots	0	(L)	(C):1 (B):2			
	1	(L)	(L):N/A			
	2	1	1:N/A			
	3	(C)	(B):2 (L):1			

Operand Reordering with Look-Ahead



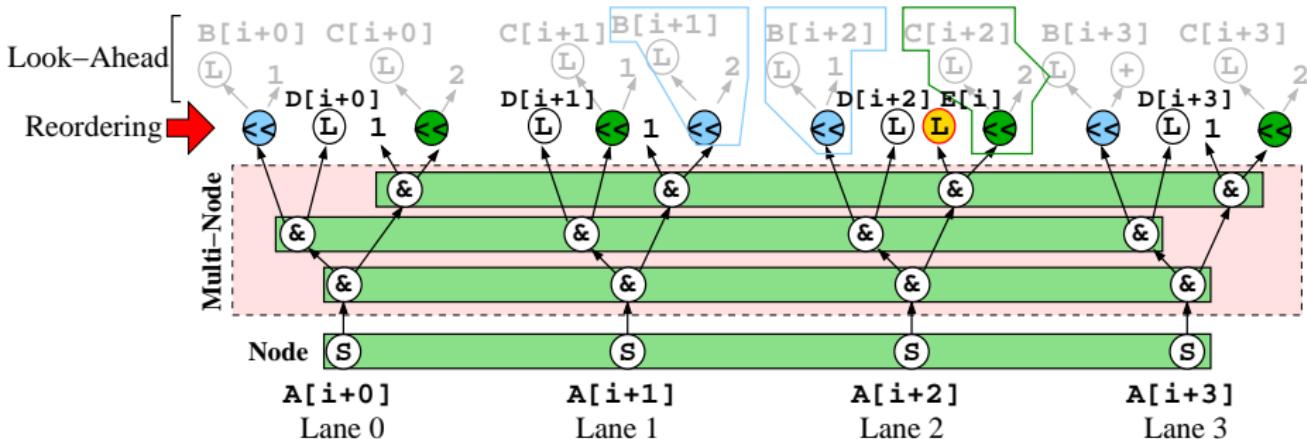
Lane 0		Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order
Operand Slots	0		:1 :2				
	1		:N/A				
	2		:N/A				
	3		:2 :1				

Operand Reordering with Look-Ahead



	Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order
Operand Slots	0		:1 :2		:2 :1		
	1		:N/A				
	2	1	1 :N/A	1			
	3		:2 :1				

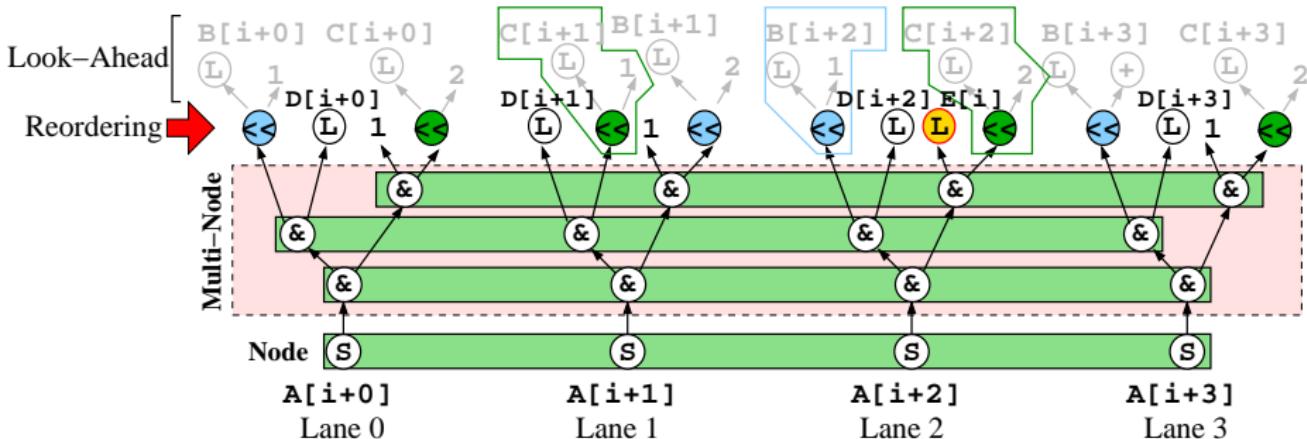
Operand Reordering with Look-Ahead



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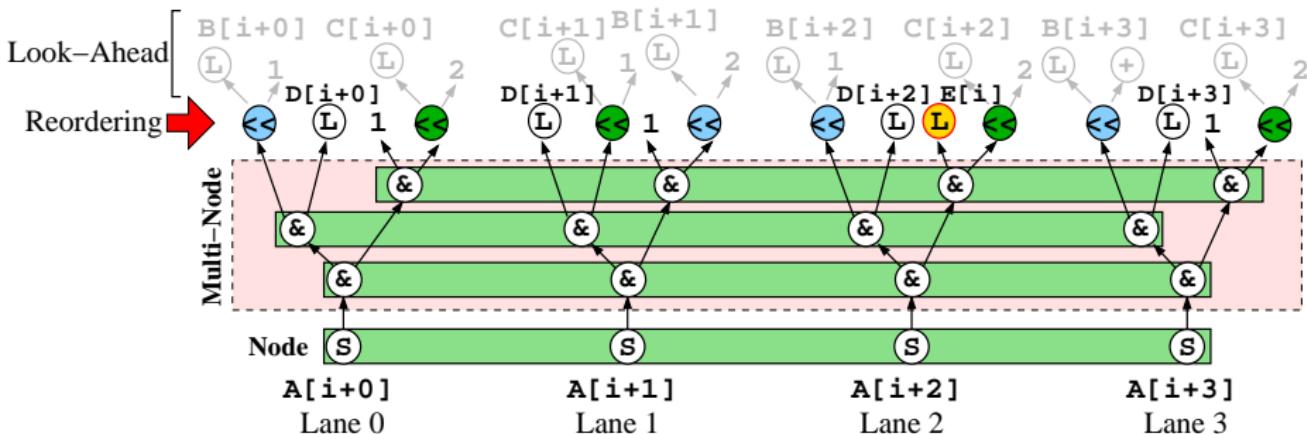
Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score
Operand Slots	0	1:N/A	2	1:N/A	3	2:N/A
	(L)	(L)	(L)	(L)		
	1	1:N/A	1			
	(L)	(L)	(L)			

Operand Reordering with Look-Ahead



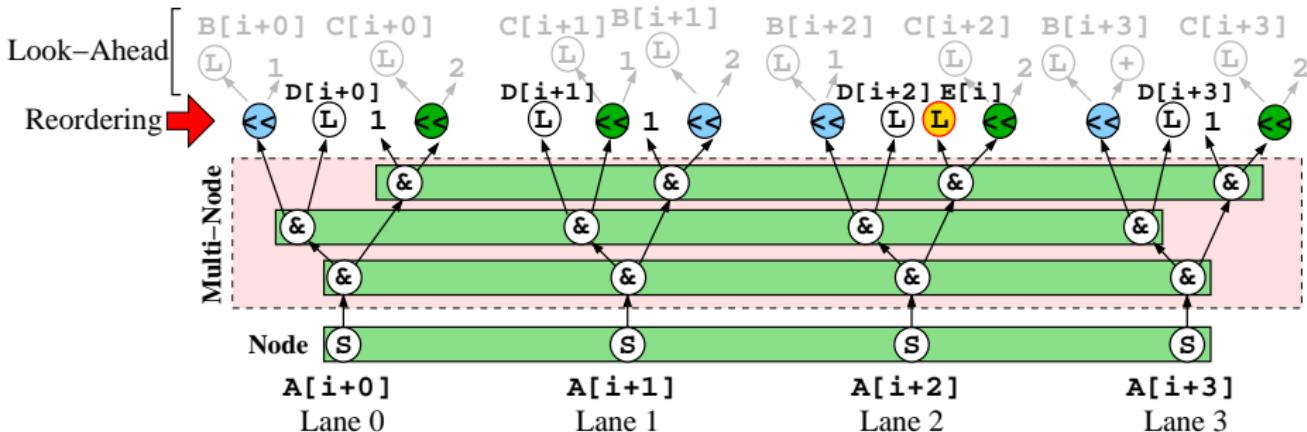
Lane 0		Lane 1		Lane 2		Lane 3	
final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	
Operand Slots	0	<:1	<:2	<:1	<:2		
	1	(L)	(L):N/A	(L)	(L):N/A		
	2	1	1:N/A	1	(L):N/A		
	3	<:	<:2	<:1	<:1	<:2	

Operand Reordering with Look-Ahead



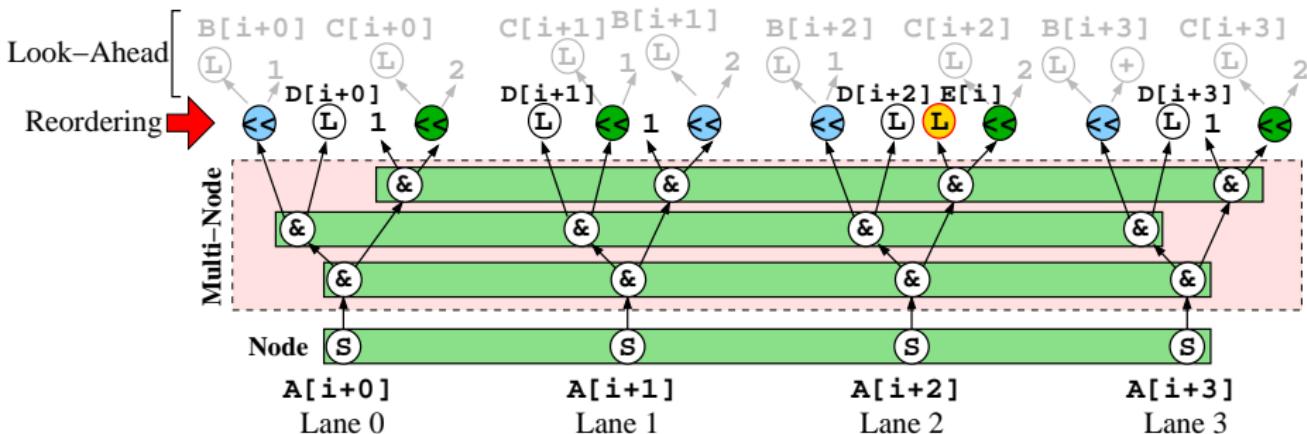
Lane	Lane 0		Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	
Operand Slots	0							
	1							
	2							
	3							

Operand Reordering with Look-Ahead



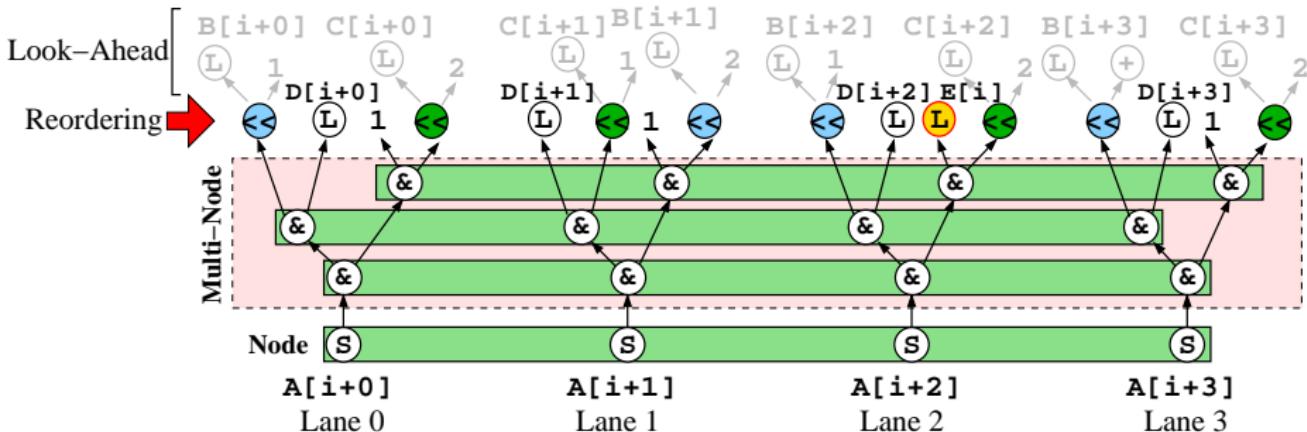
Lane	Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order
Operand Slots	0		:1 :2		:2 :1		
	1		:N/A		:N/A		
	2		:N/A		:N/A	FAILED	
	3		:2 :1		:1 :2		

Operand Reordering with Look-Ahead



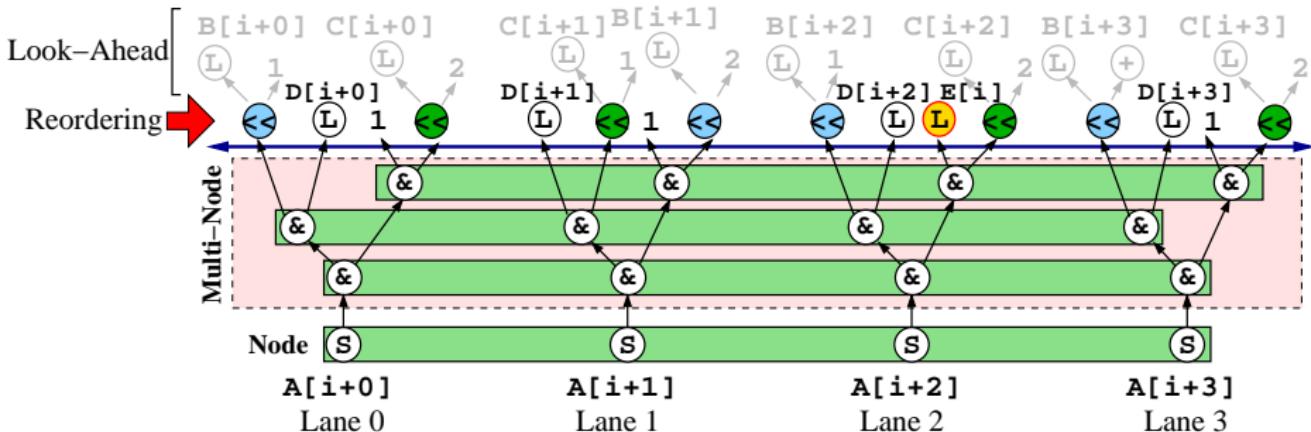
Lane	Lane 0	Lane 1		Lane 2		Lane 3	
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order
Operand Slots	0		 		 		
	1						
	2						
	3		 		 		

Operand Reordering with Look-Ahead



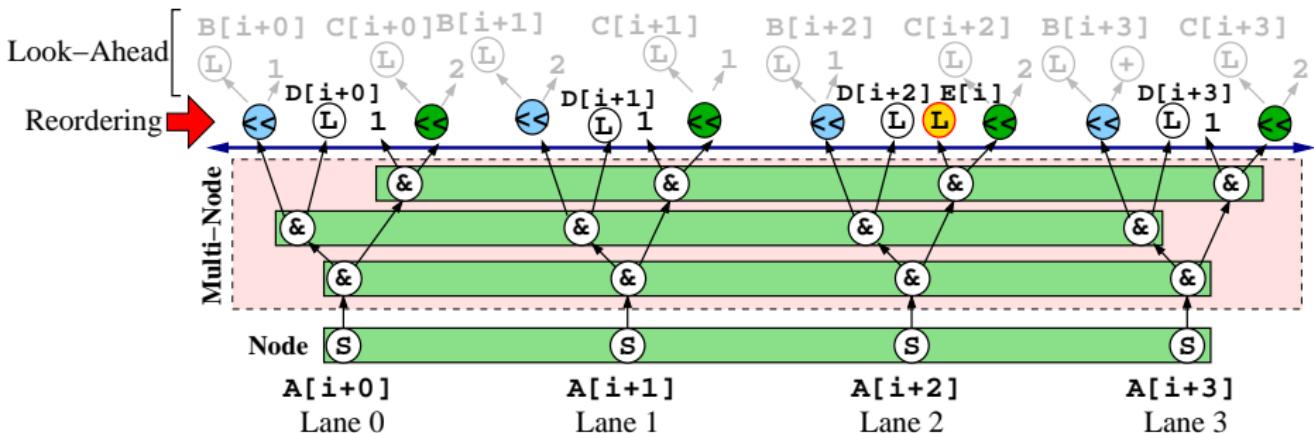
Lane	Lane 0	Lane 1		Lane 2		Lane 3					
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order				
Operand Slots	0										
1											
2											
3											

Operand Reordering with Look-Ahead



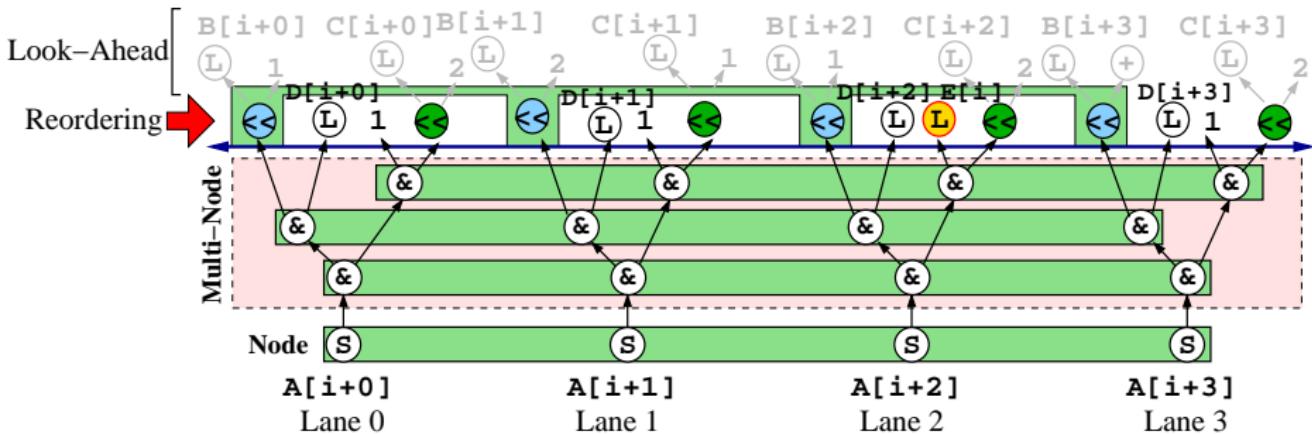
Lane	Lane 0	Lane 1		Lane 2		Lane 3						
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order					
Operand Slots	0											
	1											
	2											
	3											

Operand Reordering with Look-Ahead



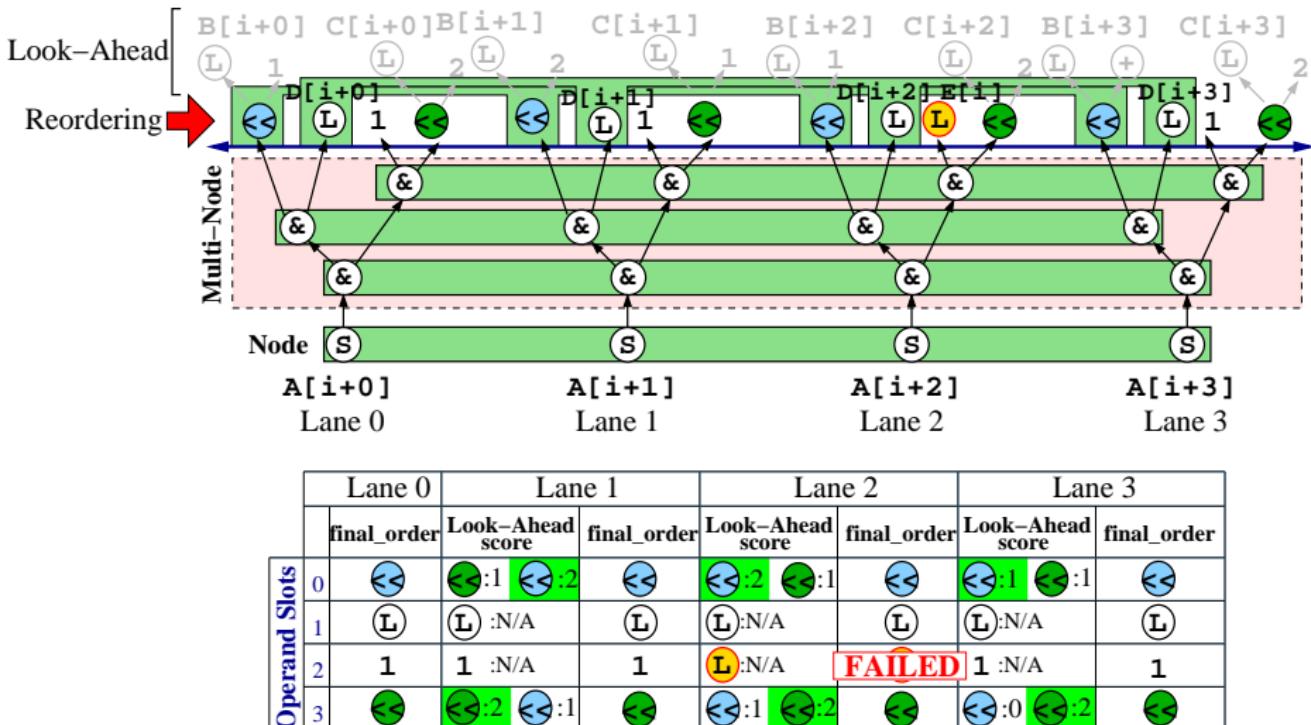
Lane	Lane 0	Lane 1		Lane 2		Lane 3		
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	
Operand Slots	0		:1 :2		:2 :1		:1 :1	
	1		:N/A		:N/A		:N/A	
	2	1	1 :N/A	1	:N/A	FAILED	1 :N/A	1
	3		:2 :1		:1 :2		:0 :2	

Operand Reordering with Look-Ahead

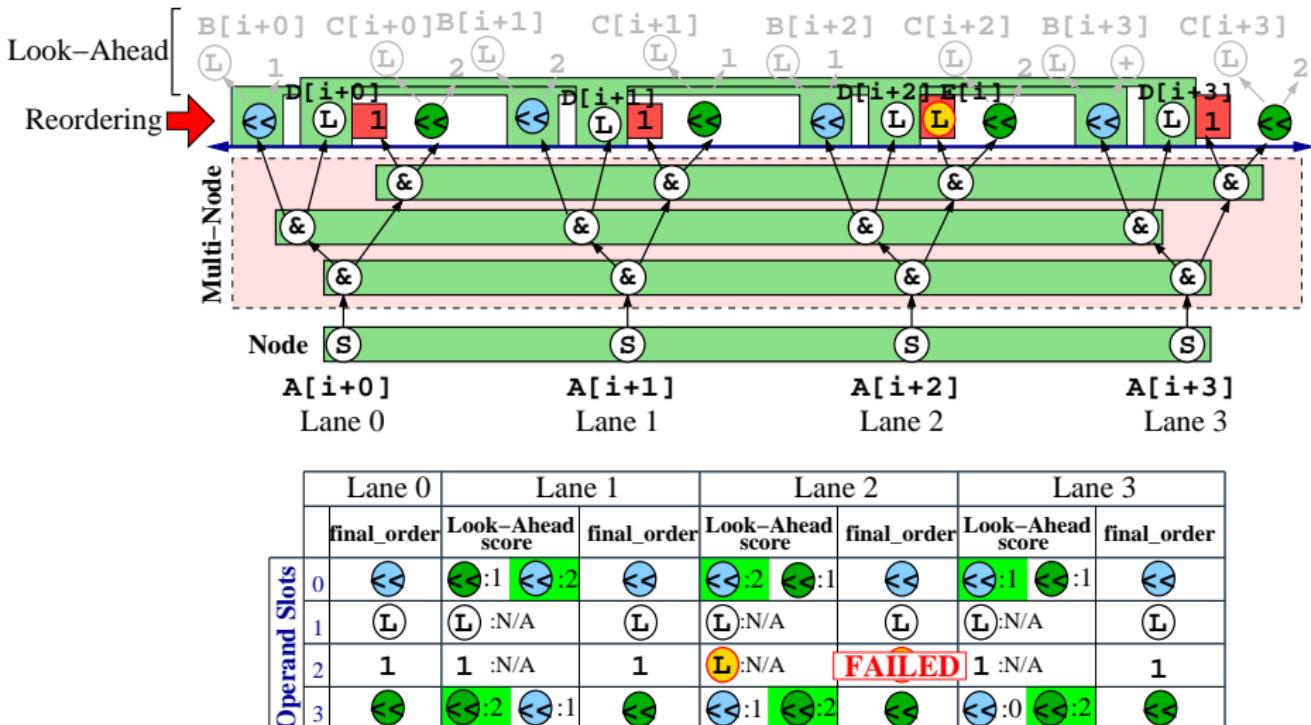


Lane	Lane 0		Lane 1		Lane 2		Lane 3				
	final_order	Look-Ahead score	final_order								
Operand Slots	0										
	1										
	2										
	3										

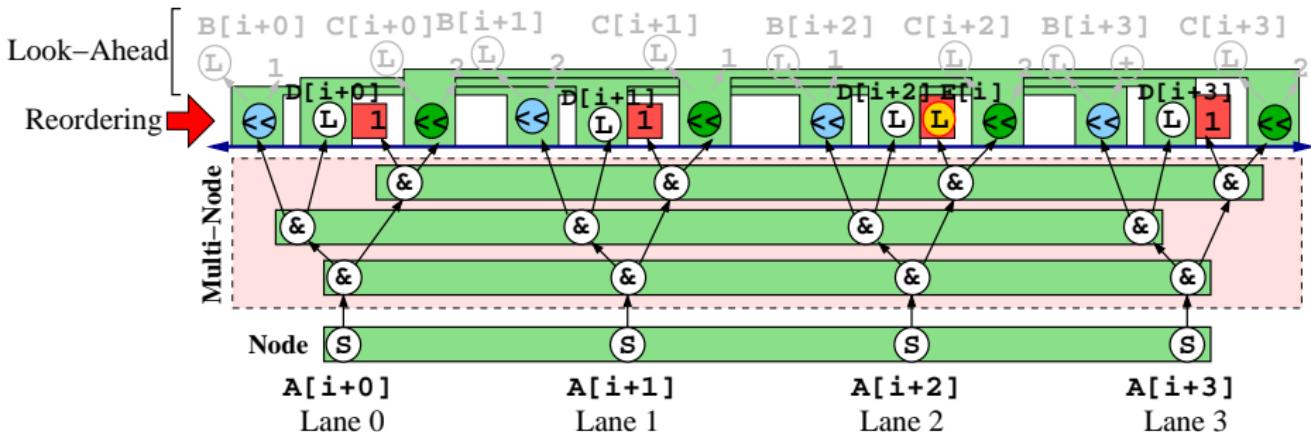
Operand Reordering with Look-Ahead



Operand Reordering with Look-Ahead

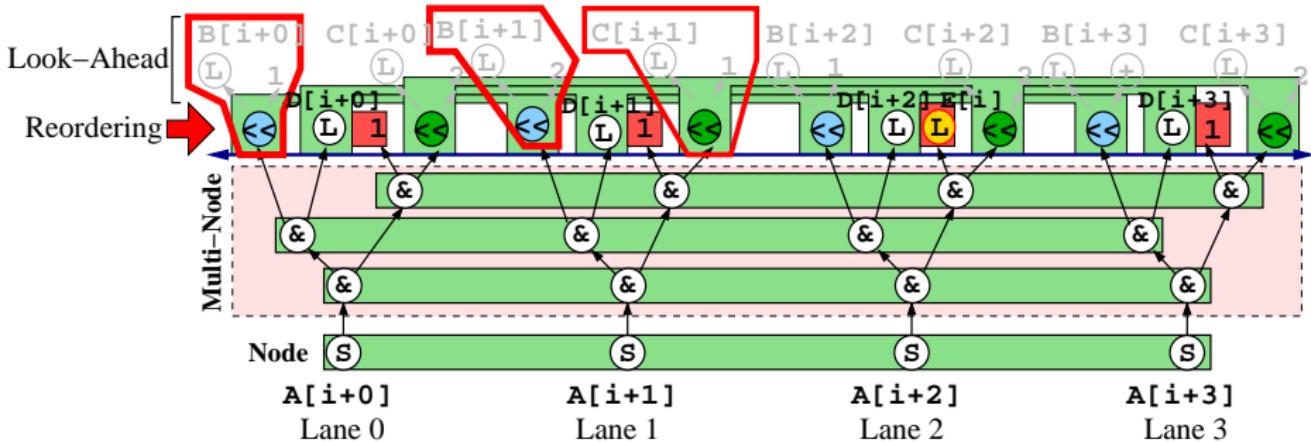


Operand Reordering with Look-Ahead



	Lane 0	Lane 1		Lane 2		Lane 3				
	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order			
Operand Slots	0									
	1									
	2					FAILED				
	3									

Operand Reordering with Look-Ahead

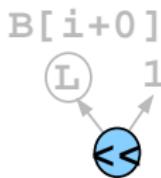


	Lane 0	Lane 1	Lane 2	Lane 3			
final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	Look-Ahead score	final_order	
Operand Slots	0	1 :N/A	2 :N/A	3 :N/A	4 :N/A	5 :N/A	6 :N/A
0	1 :N/A	2 :N/A	1 :N/A	2 :N/A	1 :N/A	2 :N/A	1 :N/A
1	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A
2	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A
3	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A	1 :N/A

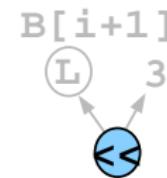
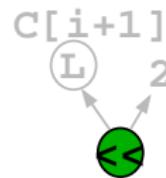
Score Calculation with Look-Ahead

slot 0

Lane 0



Lane 1 Candidates



Score Calculation with Look-Ahead

slot 0		
Lane 0	Lane 1 Candidates	
$B[i+0]$  1	$C[i+1]$  2	$B[i+1]$  3
		

Score Calculation with Look-Ahead

slot 0			
Lane 0	Lane 1 Candidates		
Operands	B[i+0]	C[i+1]	B[i+1]
	$\text{B}[i+0]$  1 	$\text{C}[i+1]$  2 	$\text{B}[i+1]$  3 
	$\text{B}[i+0]$  1		

Score Calculation with Look-Ahead

slot 0			
Lane 0	Lane 1 Candidates		
Operands	B[i+0]	C[i+1]	B[i+1]
	$\text{B}[i+0]$  1 	$\text{C}[i+1]$  2 	$\text{B}[i+1]$  3 
	$\text{B}[i+0]$  1	$\text{C}[i+1]$  2	

Score Calculation with Look-Ahead

slot 0					
Lane 0	Lane 1 Candidates				
Operands	B[i+0]	C[i+1]	Score	B[i+1]	Score
	 1 	 2 		 3 	
	 1 	 2 			

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]	B[i+1]	Score	Score
	 1	 2	 3		
	 1	 2	 3	0	
		 2			

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]	B[i+1]	Score	
				0	0
	B[i+0] 1	C[i+1] 2 Not Consecutive Different Opcodes			

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]	B[i+1]	Score	
	 1	 2	 3	0	0
	B[i+0]	C[i+1]	B[i+1]		

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]		B[i+1]	
	 1	 2		 3	
	 1	 2	Score	 3	Score
	 1	 2	Not Consecutive Different Opcodes Different Opcodes	0	0
				0	

Score Calculation with Look-Ahead

Lane 0		Lane 1 Candidates		
Operands		C[i+1]	B[i+1]	
B[i+0]  1 		 2 		 3 
B[i+0]  1 		 2 	Score	 3 
		$\frac{\text{Not Consecutive}}{\text{Different Opcodes}}$ $\frac{\text{Different Opcodes}}{\text{Both Constants}}$	0 0 0 1	Score

Score Calculation with Look-Ahead

Lane 0		Lane 1 Candidates		
Operands		C[i+1]	B[i+1]	
B[i+0]  1 		 2 		 3 
B[i+0]  1		C[i+1] Not Consecutive  2 C[i+1] Different Opcodes  2	Score 0 0 0 + 1	Score
		Both Constants		
		Look-Ahead score:	1	

Score Calculation with Look-Ahead

slot 0					
Lane 0	Lane 1 Candidates				
Operands	B[i+0]	C[i+1]	Score	B[i+1]	Score
	 B[i+0] 1	 C[i+1] 2		 B[i+1] 3	
	 B[i+0] 1	 C[i+1] 2 C[i+1] 2	Not Consecutive → 0 Different Opcodes → 0 Different Opcodes → 0 Both Constants → + 1	 B[i+1] 3 B[i+1] 3	1
		Look-Ahead score:	1		

Score Calculation with Look-Ahead

slot 0					
Lane 0	Lane 1 Candidates				
Operands	B[i+0]	C[i+1]	Score	B[i+1]	Score
		1 2 2 2		Not Consecutive Different Opcodes Different Opcodes Both Constants	
		0 0 0 + 1		Consecutive Different Opcodes	
Look-Ahead score: 1					

Score Calculation with Look-Ahead

slot 0						
Lane 0	Lane 1 Candidates					
Operands	B[i+0]	C[i+1]	Score	B[i+1]	Score	
			Not Consecutive Different Opcodes Different Opcodes Both Constants	0 0 0 + 1	 	1 0 0
				Look-Ahead score:	1	

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]	B[i+1]	Score	
	B[i+0] 1	C[i+1] 2	B[i+1] 3		
	B[i+0] 	C[i+1] 2 C[i+1] 2	B[i+1] 3 B[i+1] 3	Not Consecutive Different Opcodes Different Opcodes Both Constants	0 0 0 + 1
				Different Opcodes Both Constants	1 0 0 1
	Look-Ahead score:		1		

Score Calculation with Look-Ahead

slot 0					
Lane 0		Lane 1 Candidates			
Operands	B[i+0]	C[i+1]	B[i+1]	Score	
	B[i+0] 1	C[i+1] 2	B[i+1] 3		
				Score	Score
	B[i+0] 	C[i+1] 2	B[i+1] 3	1	0
		Not Consecutive Different Opcodes	Consecutive Different Opcodes		
		 2	 3		
		Different Opcodes	Different Opcodes		
		 2	 3		
		Both Constants	Both Constants		
		 1	 2	+ 1	+ 1
	Look-Ahead score:		1	Look-Ahead score:	

Score Calculation with Look-Ahead

Lane 0		Lane 1 Candidates		
Operands	B[i+0]	C[i+1]	B[i+1]	
			Score	
	B[i+0]	C[i+1]	B[i+1]	Score
			Not Consecutive	1
			Different Opcodes	0
			Consecutive	0
			Different Opcodes	0
			Both Constants	0
			Both Constants	0
			Both Constants	+
			Both Constants	1
			Both Constants	+
			Both Constants	1
			Both Constants	+
			Both Constants	1
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			Both Constants	1
			Both Constants	+
			Both Constants	1
	<img alt="Lane 0: B[i+0] with L1 score 1. Two arrows point from Lane 0 to Lane			

Experimental Setup

- Implemented LSMP in LLVM 4.0

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- Implemented LSPL in LLVM 4.0
- Target: Intel Core i5-6440HQ Skylake CPU
- Compiler flags: -O3 -ffast-math -mavx2
-march=skylake -mtune=skylake

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- Kernels from SPEC CPU2006
- We evaluated the following cases:

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 - ② O3 + SLP enabled but No Reordering (SLP-NR)

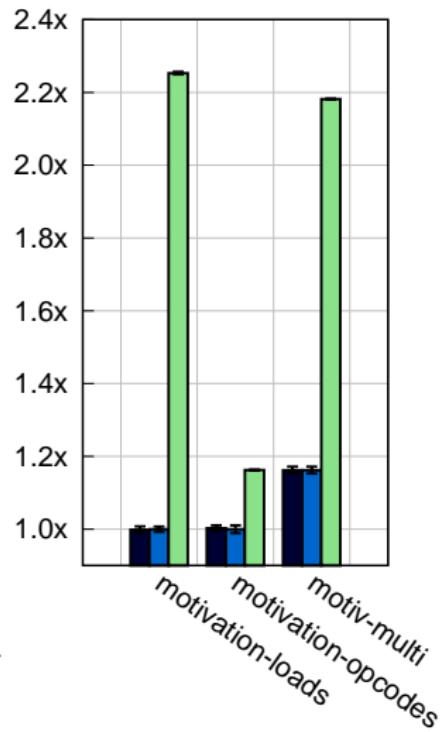
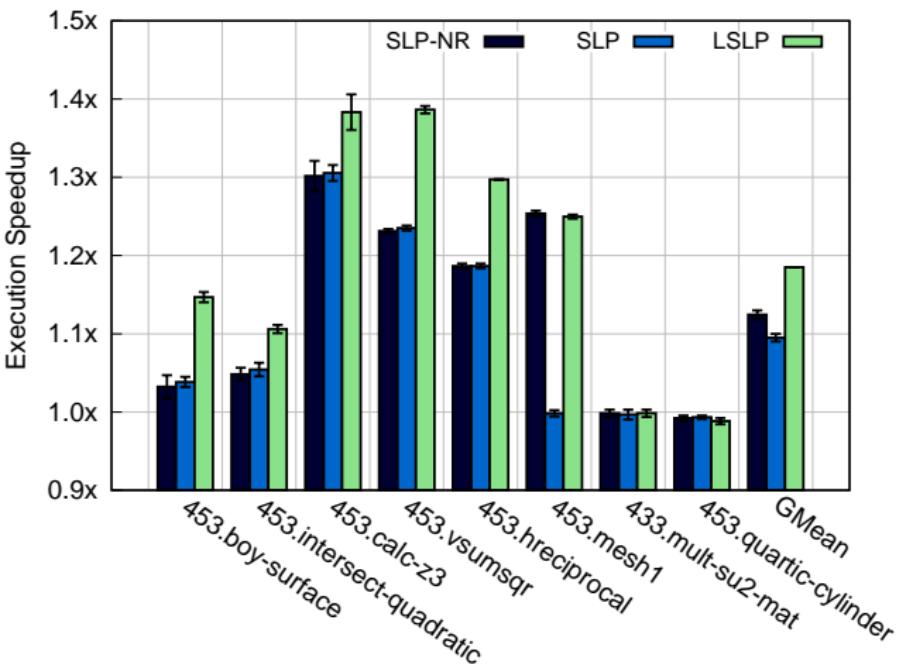
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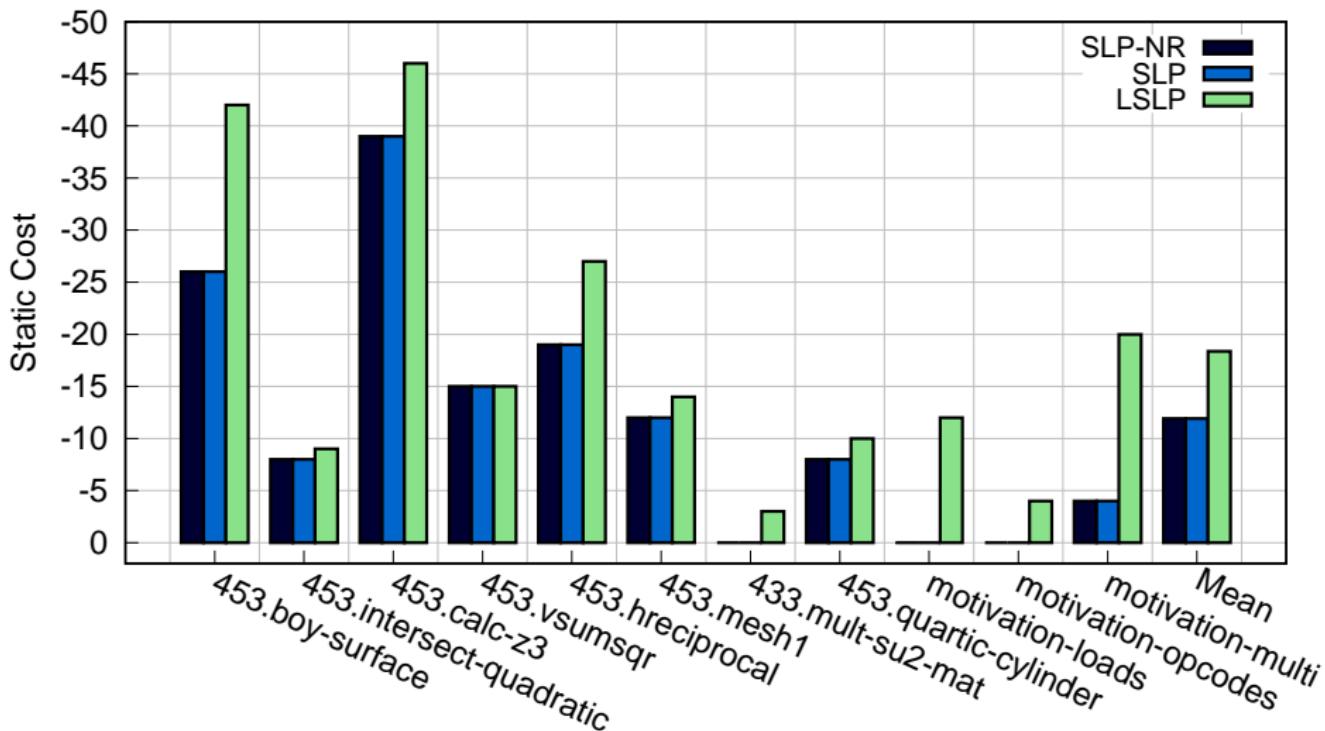
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 - ③ O3 + SLP enabled (SLP)
 - ④ O3 + LSPL enabled (LSPL)

Performance (normalized to O3)

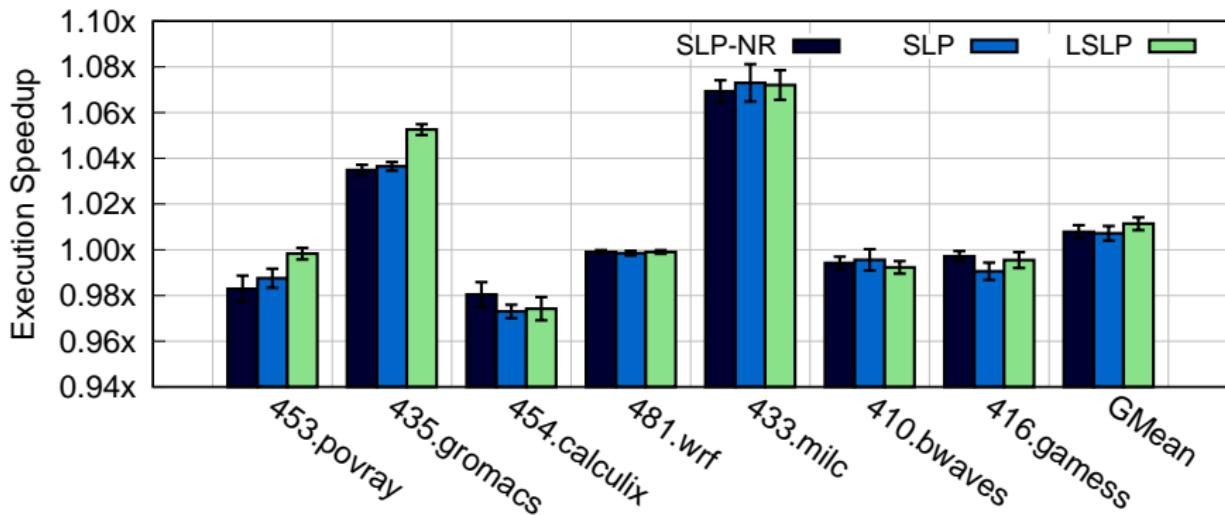


Static Cost (the higher the better)



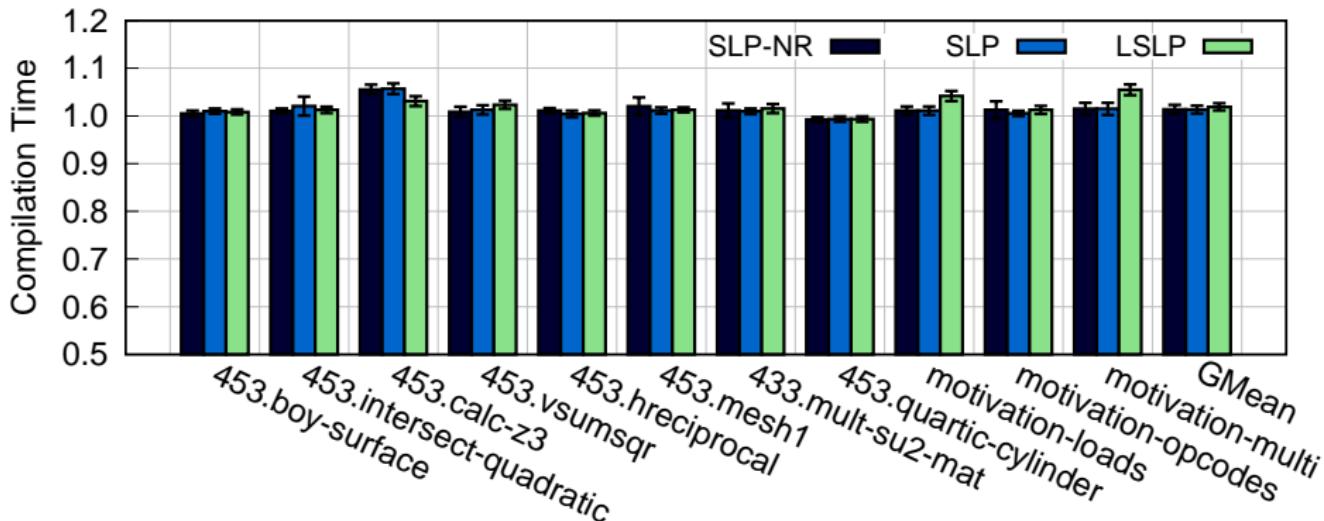
Performance (Full Benchmarks)

- About 1% speedup in 453.povray and 435.gromacs



Total Compilation Time

- No significant difference in compilation time



Conclusion

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- Implemented in LLVM and evaluated on a real machine
- Improves performance and coverage
- Similar compilation time